

FIG. 1

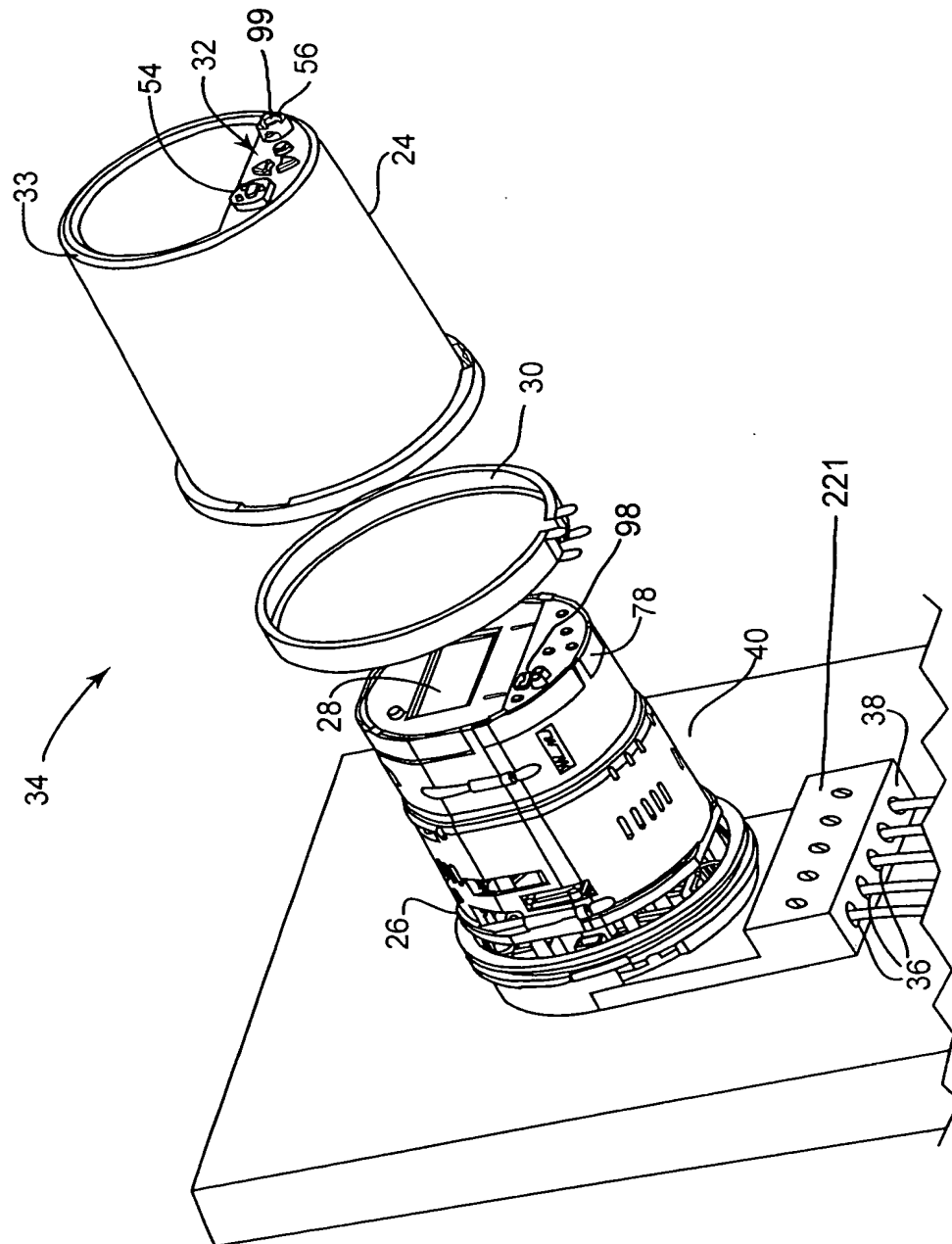
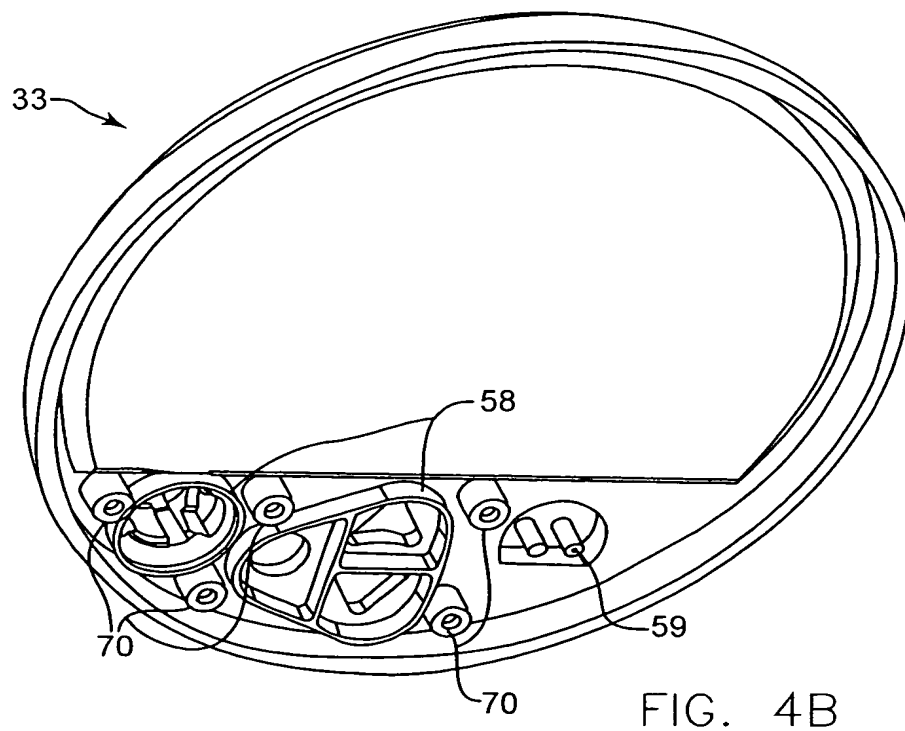
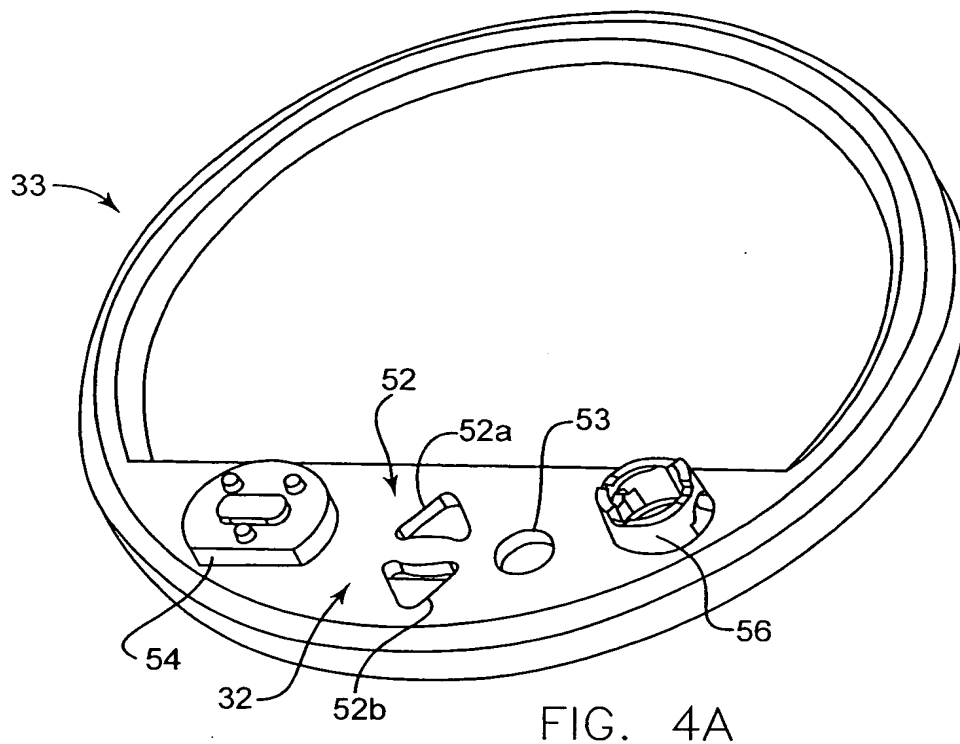


FIG. 2



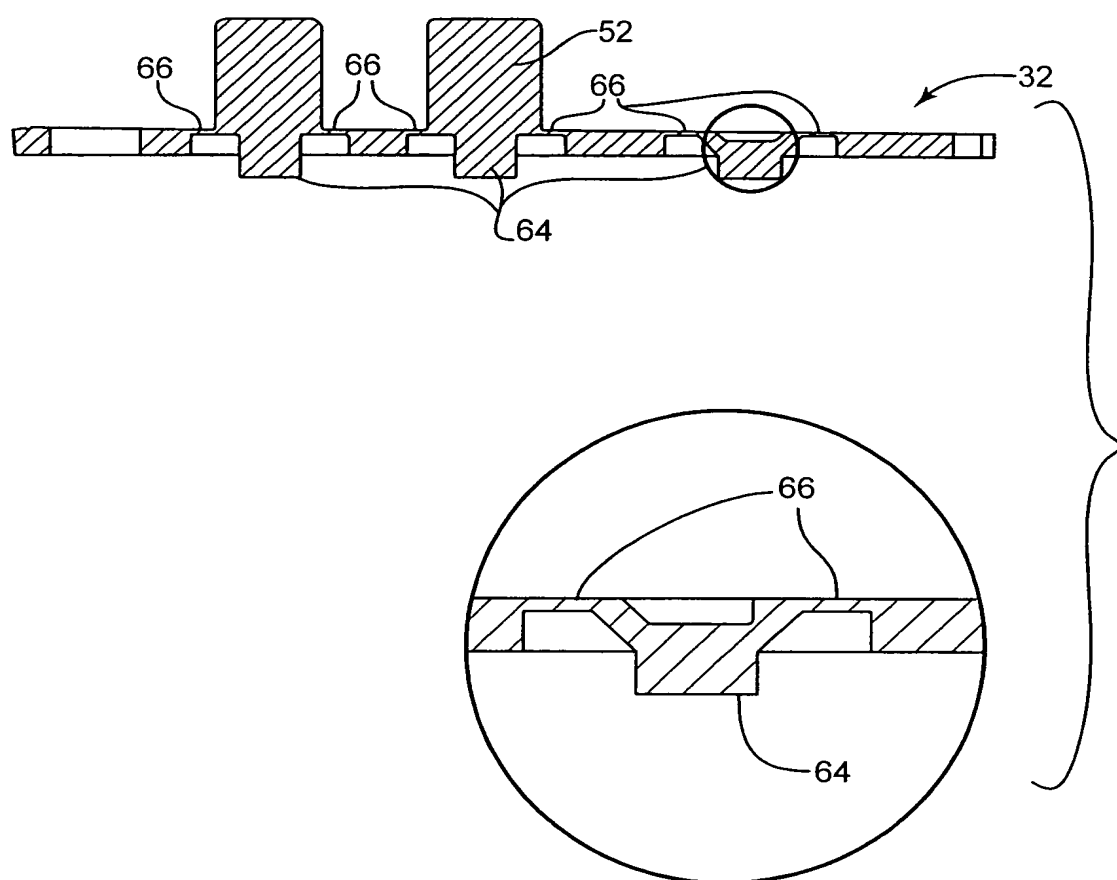


FIG. 5A

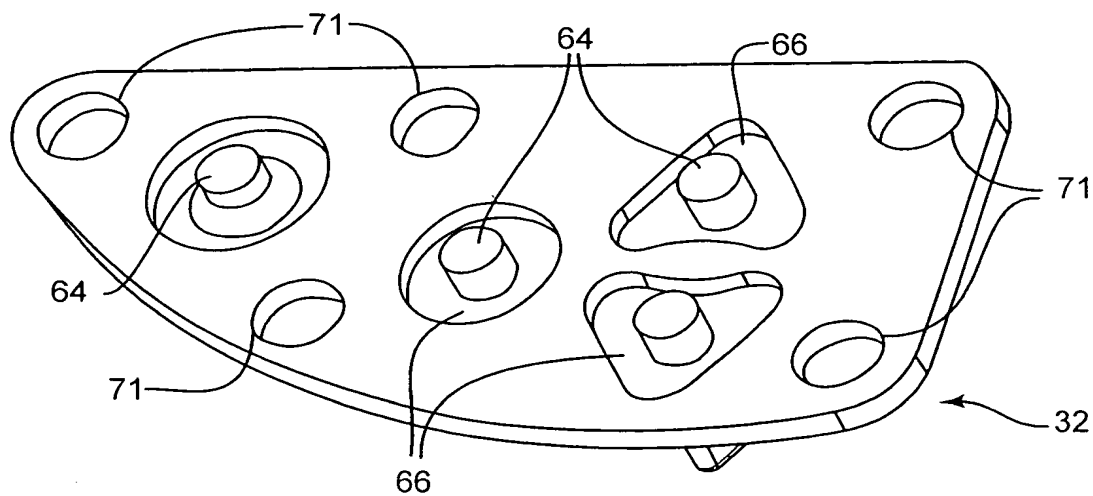


FIG. 5B

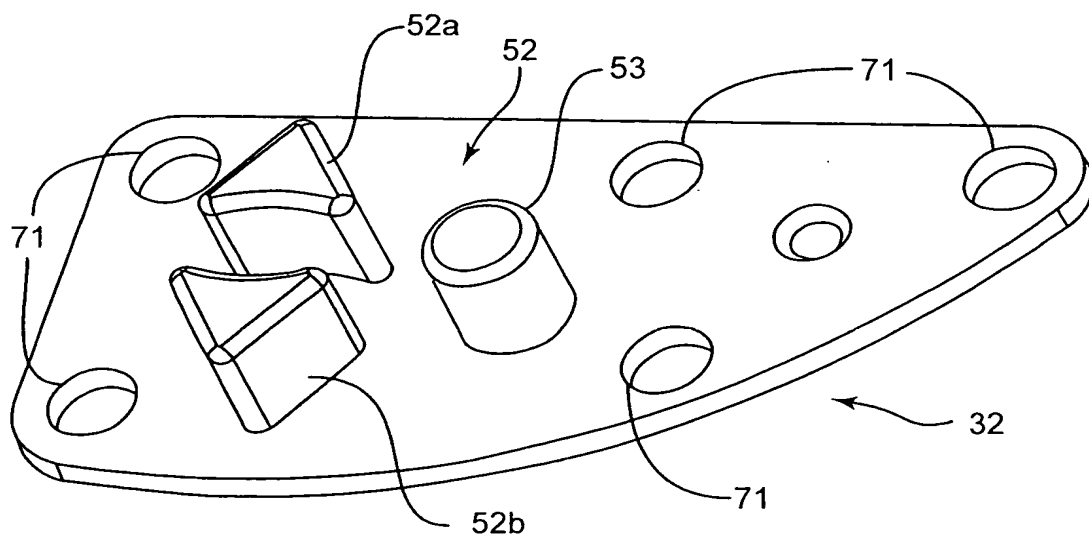


FIG. 5C

Patent Application For: INTERACTIVE USER INTERFACE FOR A REVENUE METER

Inventor(s): David W. Giles et al

Attorney Docket No.: OUR CASE NO. 6270/125

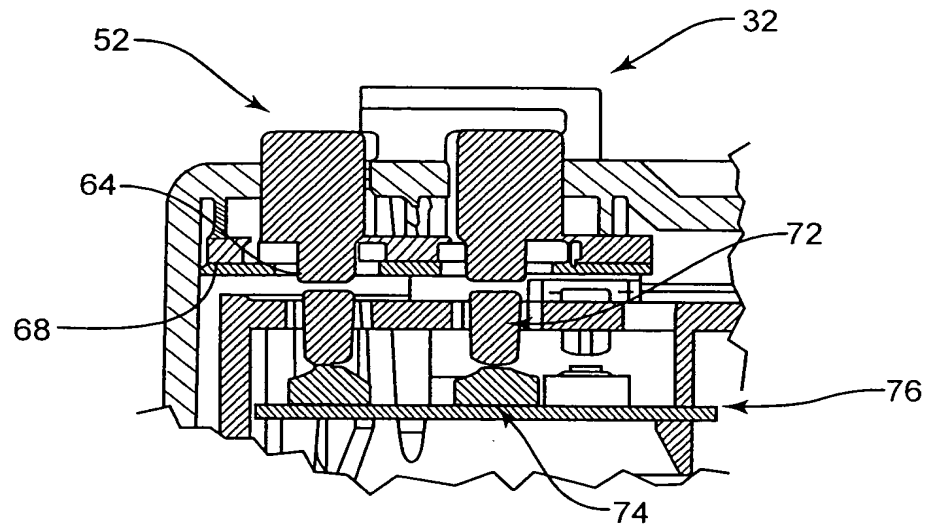


FIG. 6A

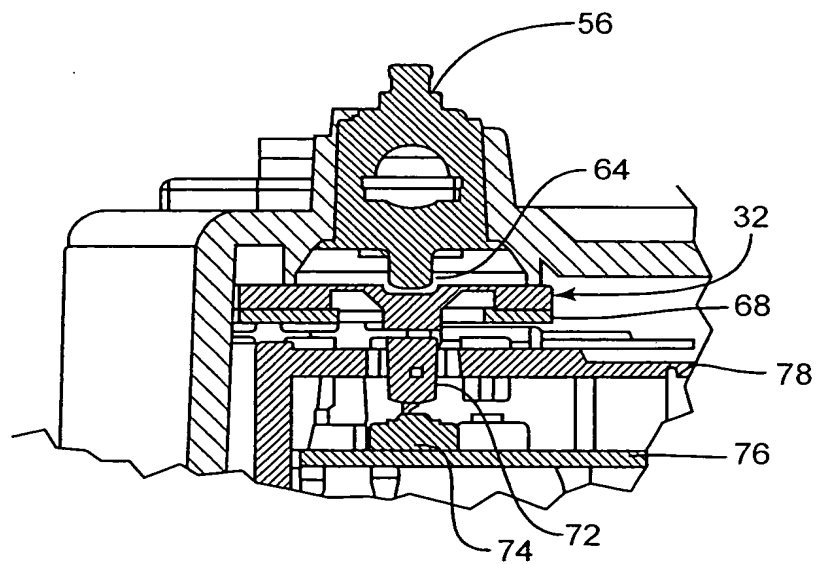


FIG. 6B

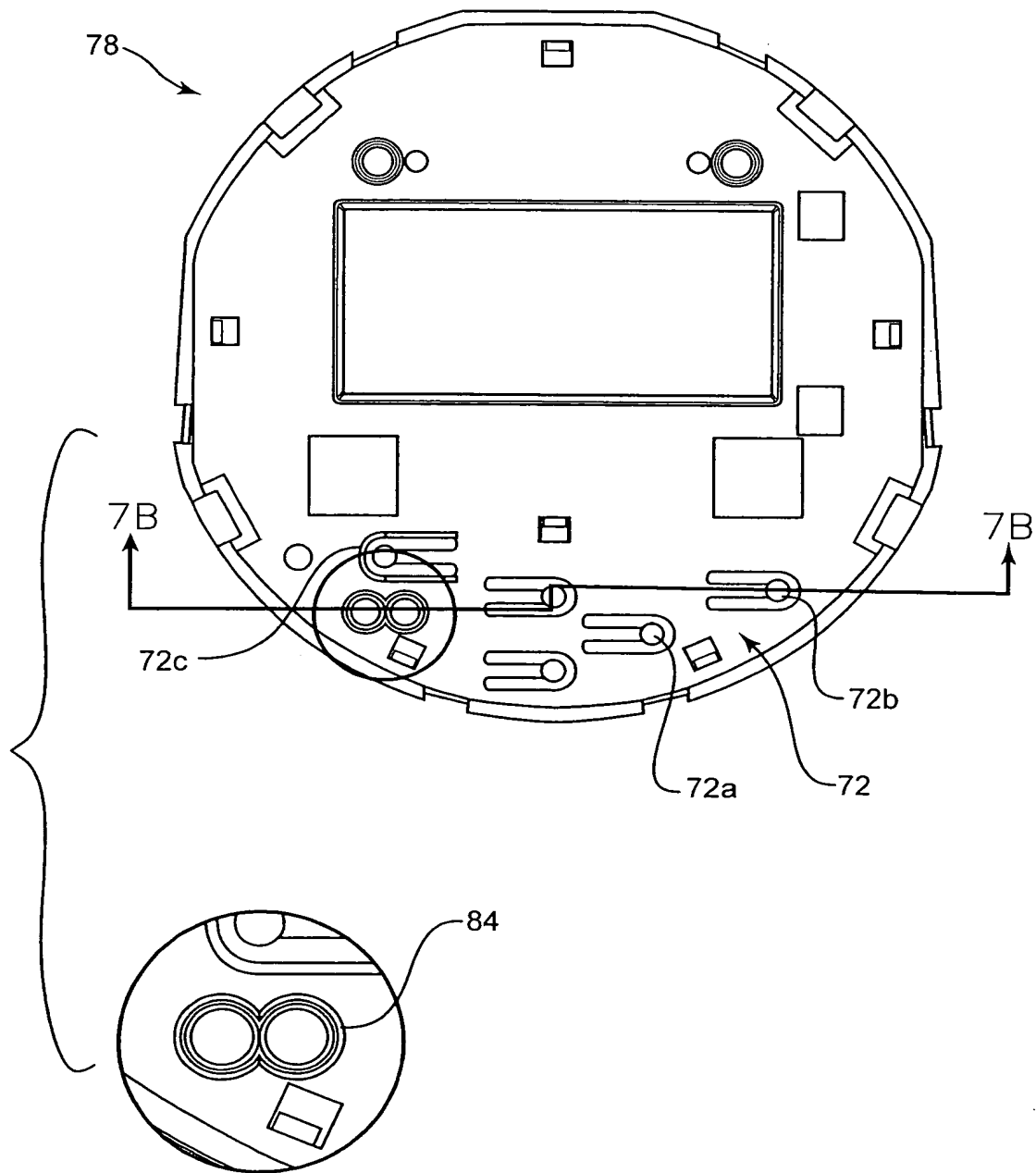


FIG. 7A

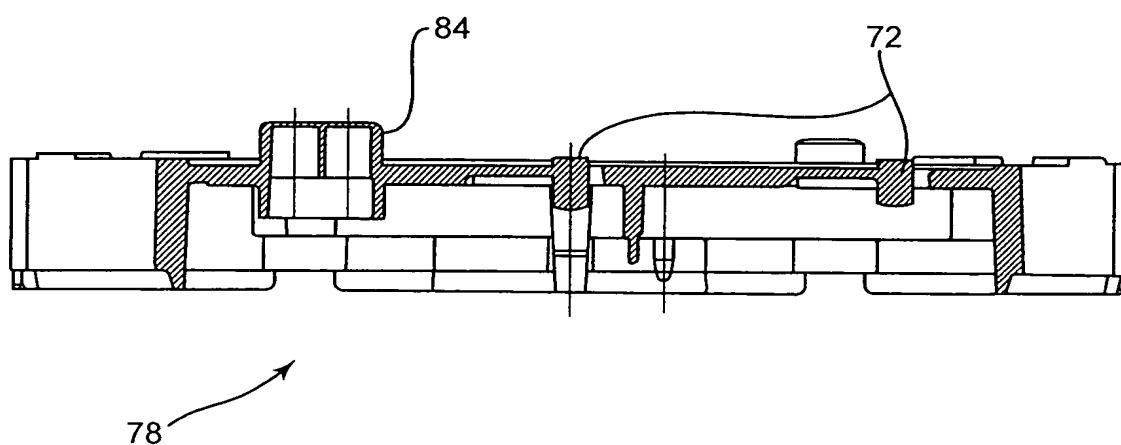
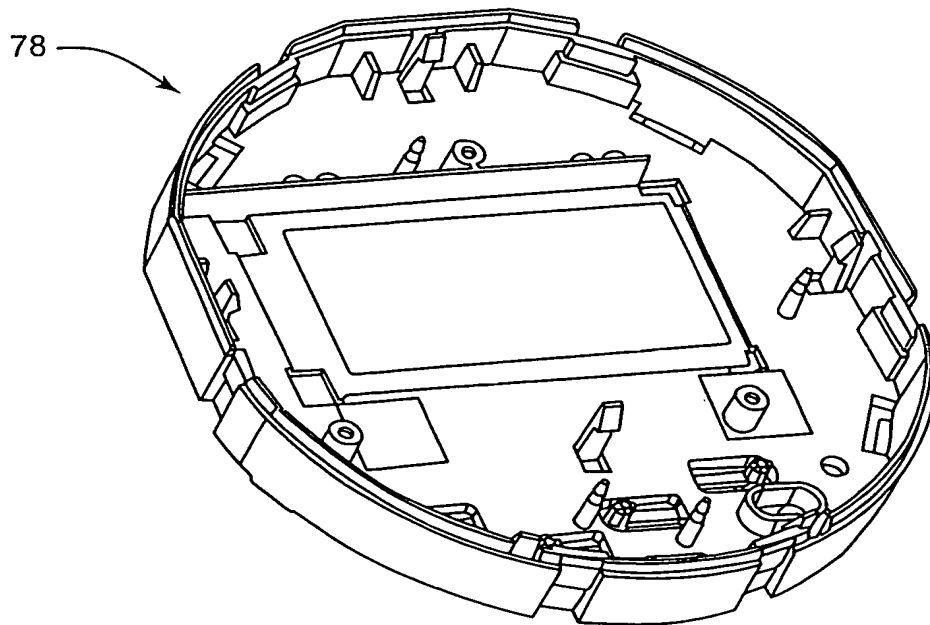
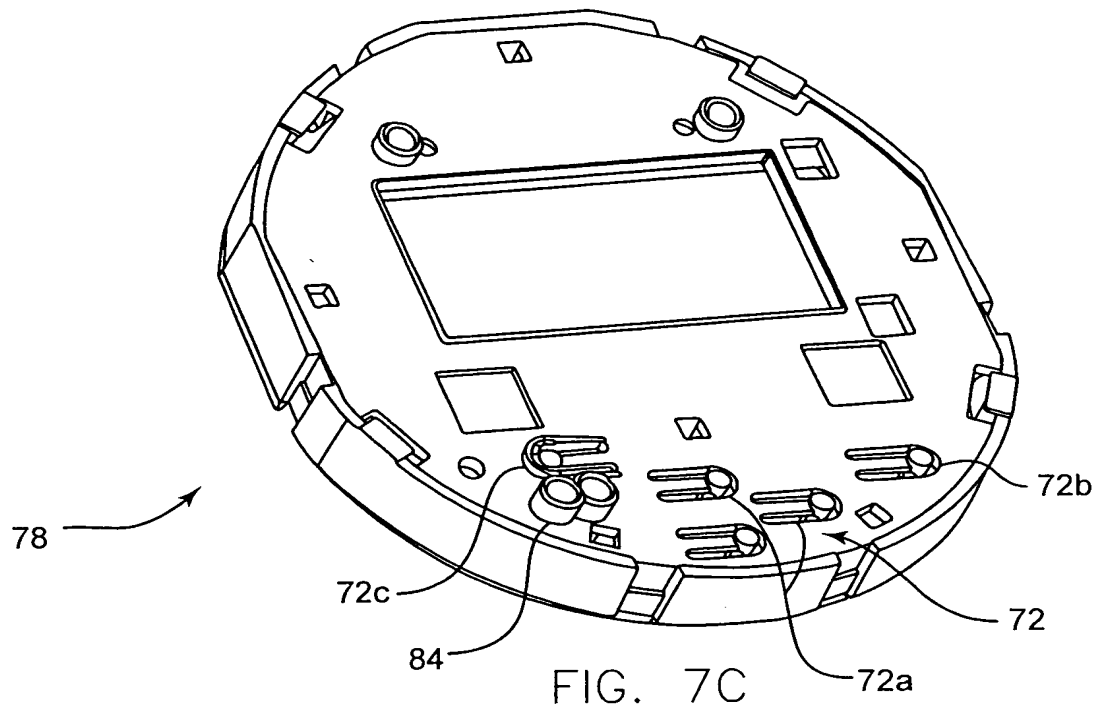


FIG. 7B

Patent Application For: INTERACTIVE USER INTERFACE FOR A REVENUE METER

Inventor(s): David W. Giles et al

Attorney Docket No.: OUR CASE NO. 6270/125



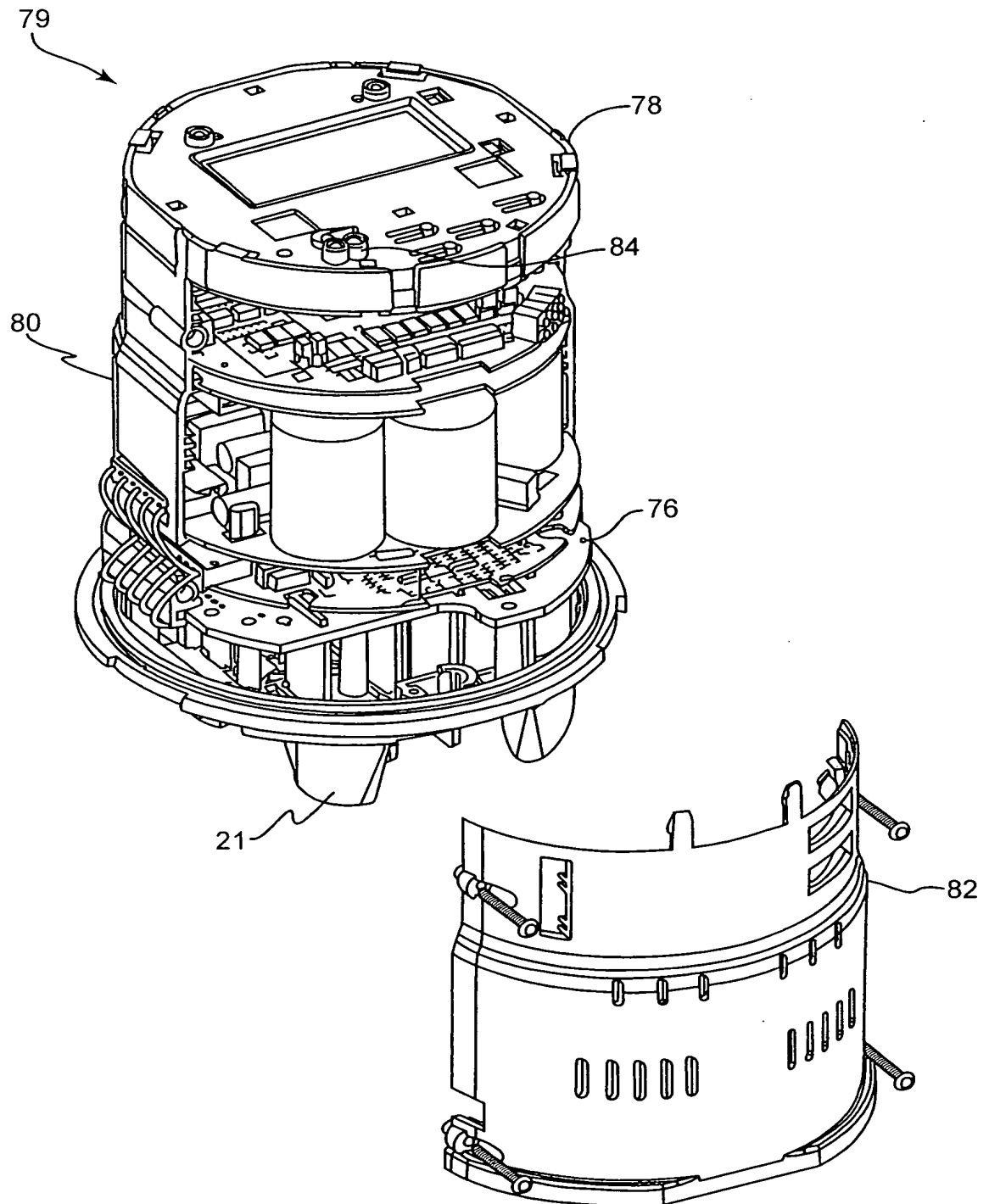


FIG. 8

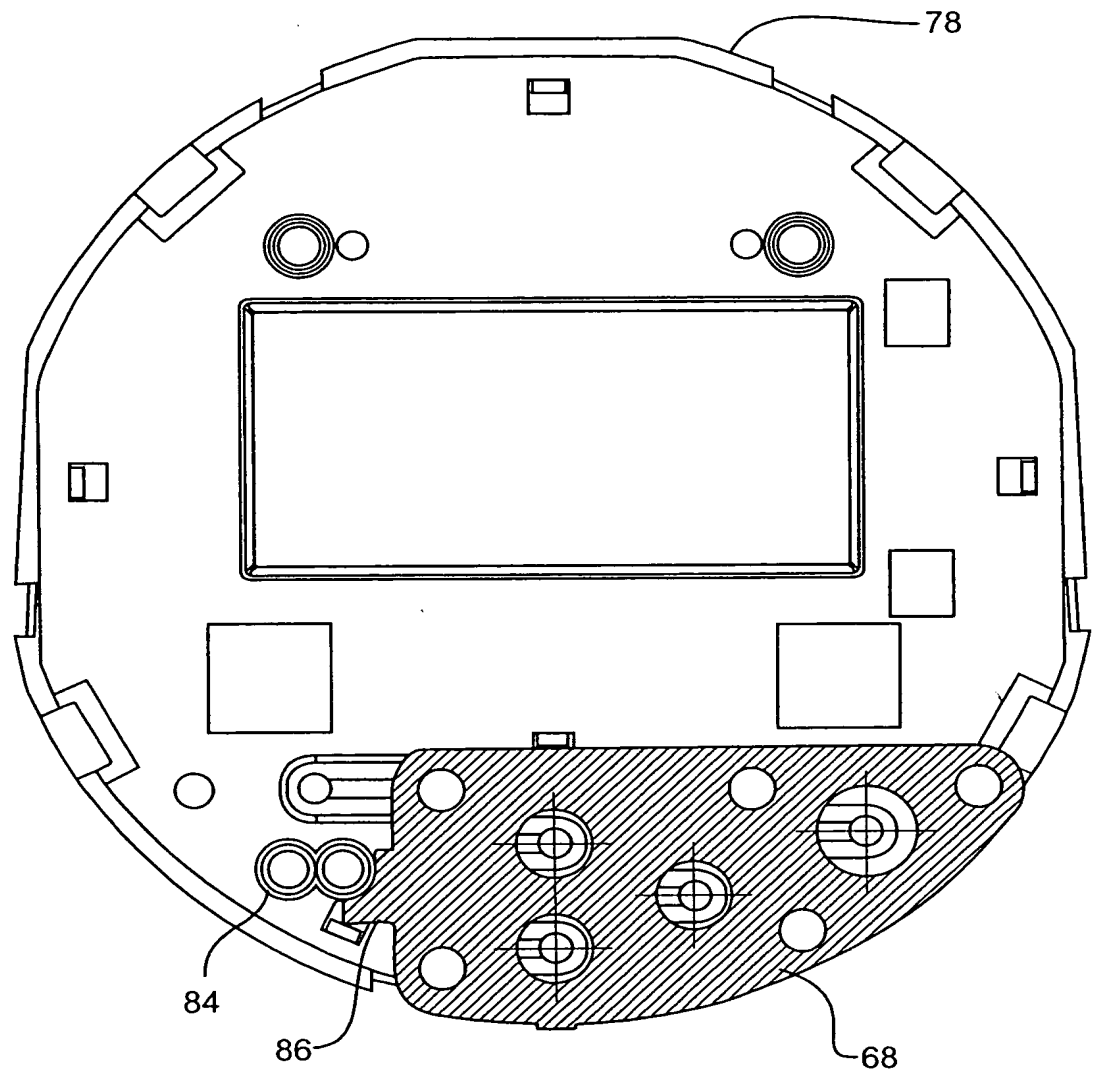


FIG. 9

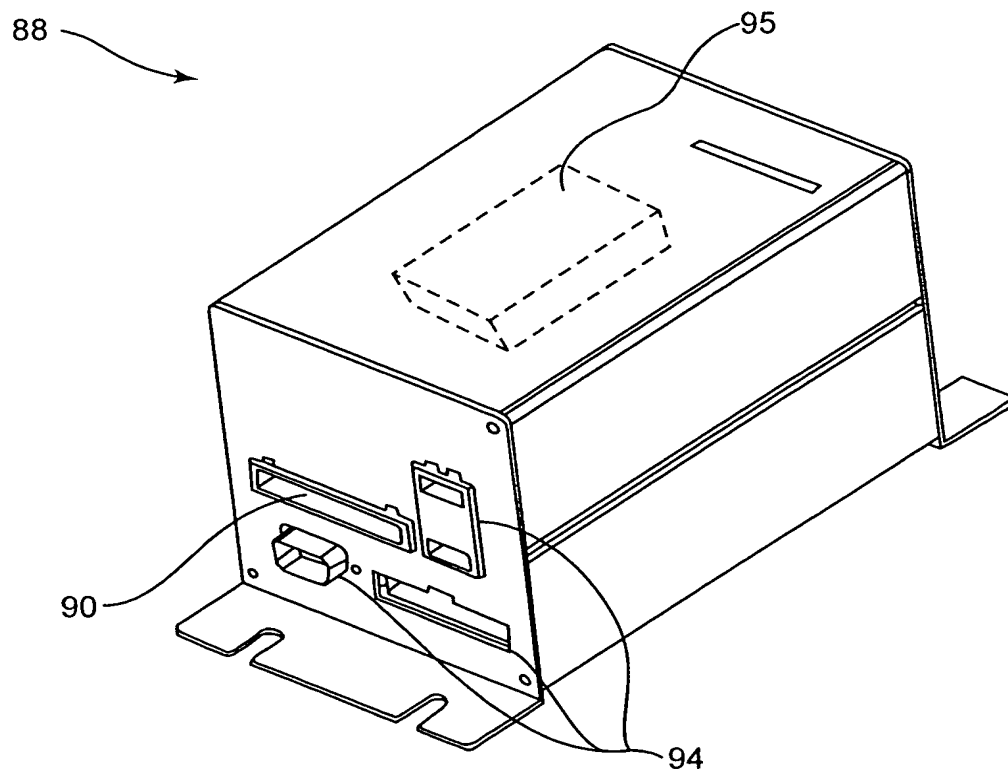


FIG. 10

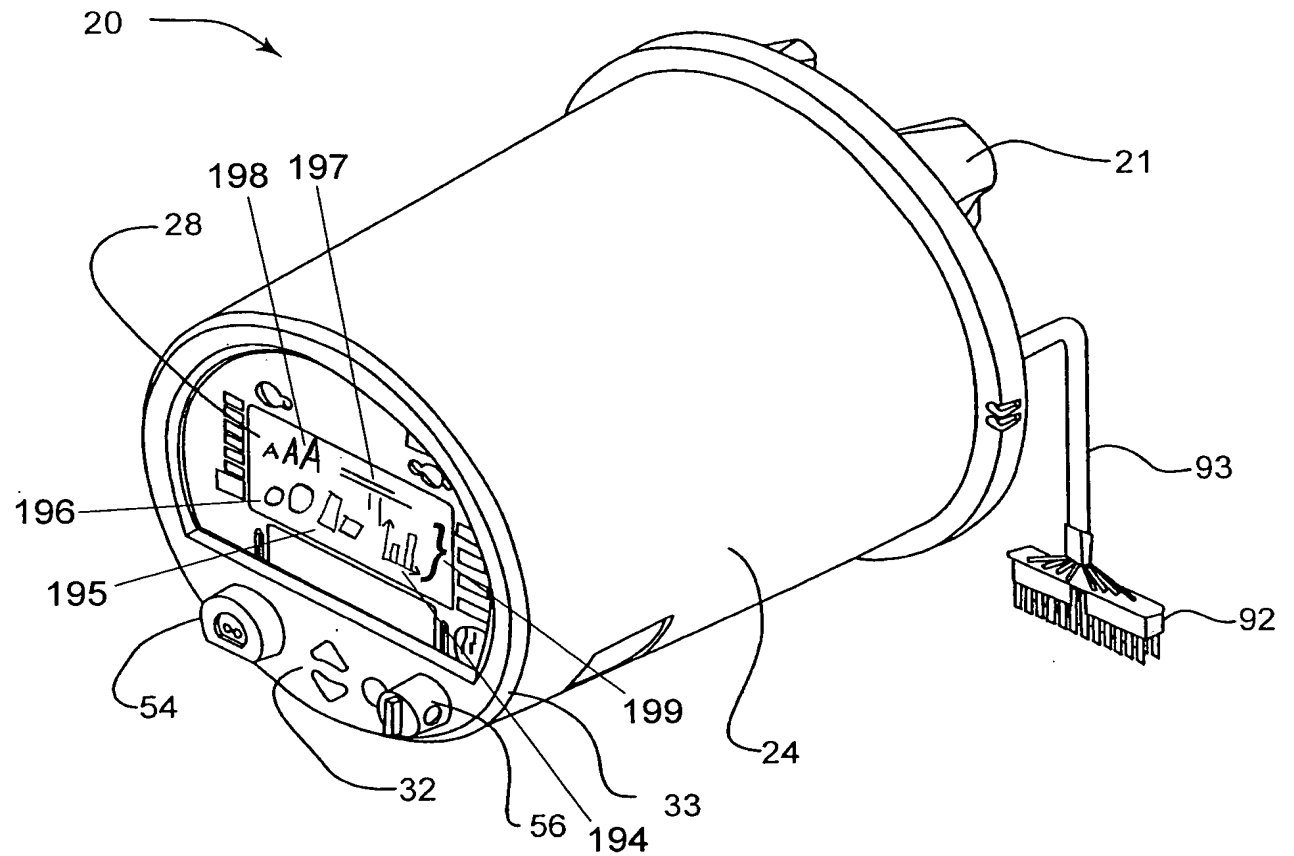


FIG. 11

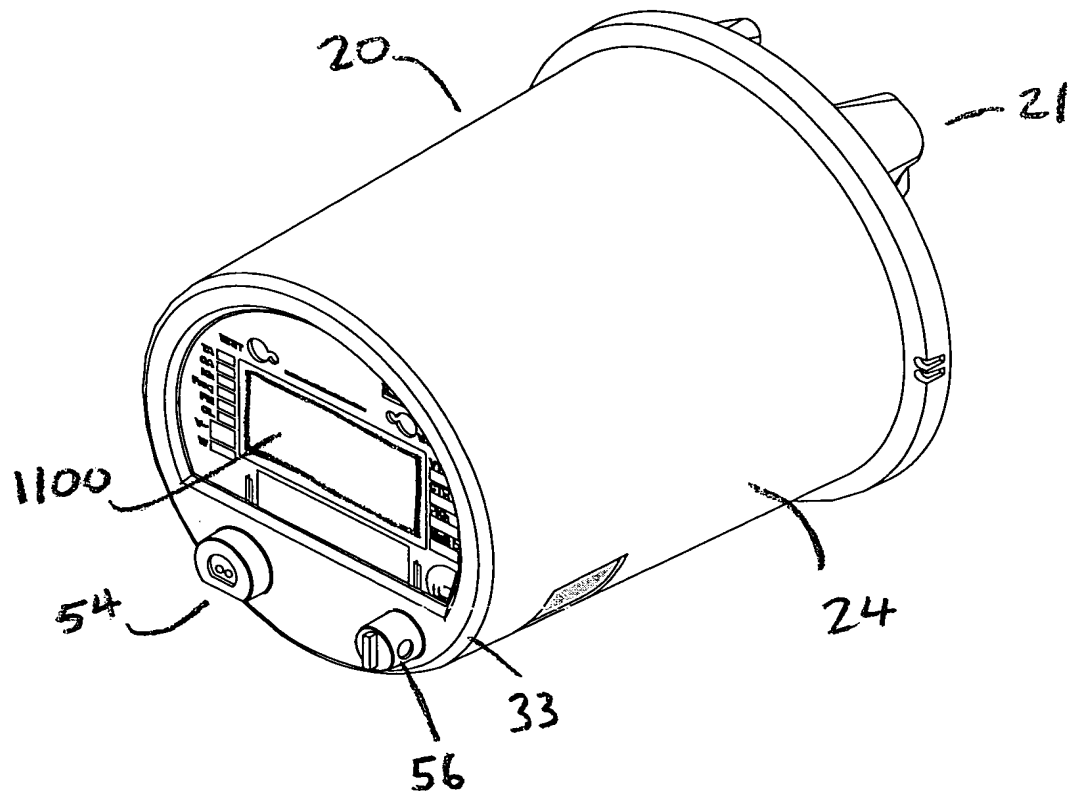


Fig. 11a

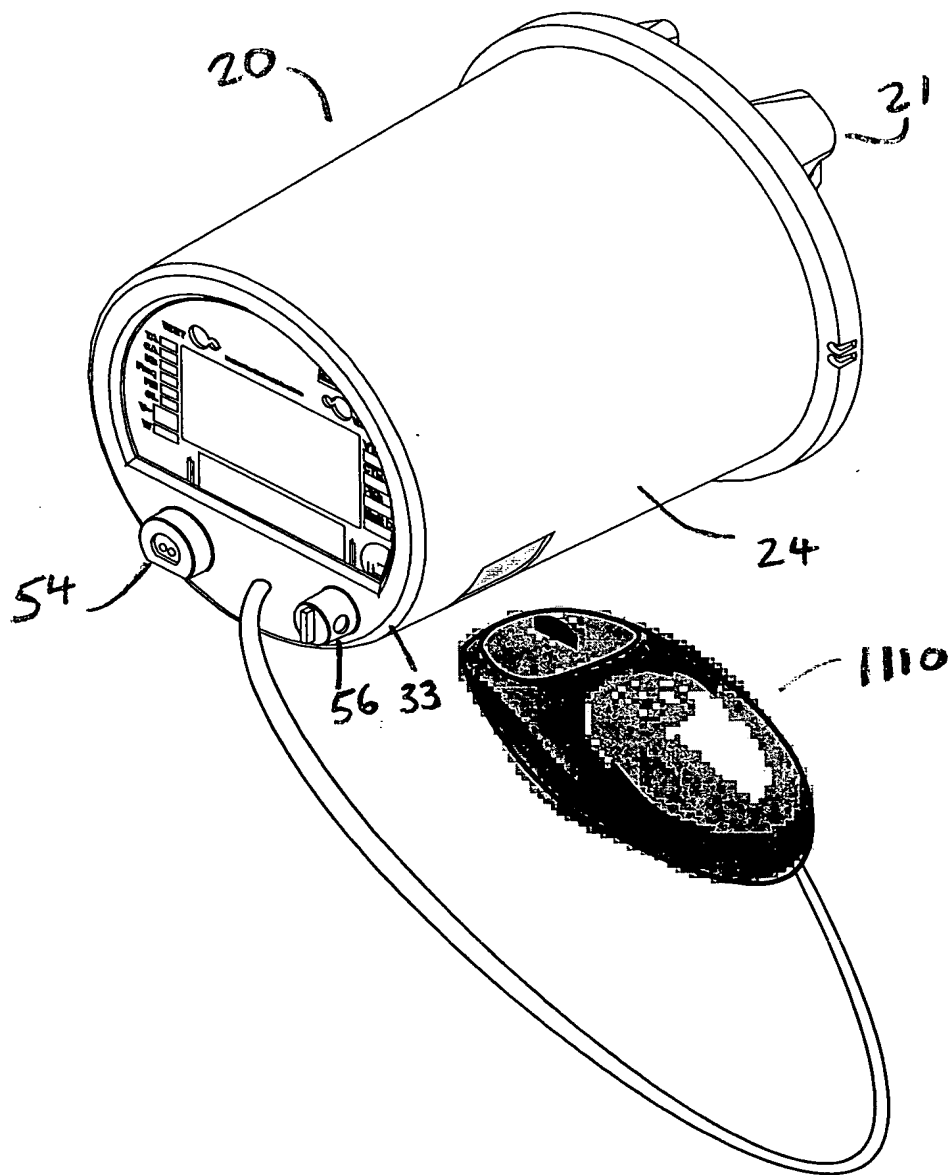


Fig. 11b

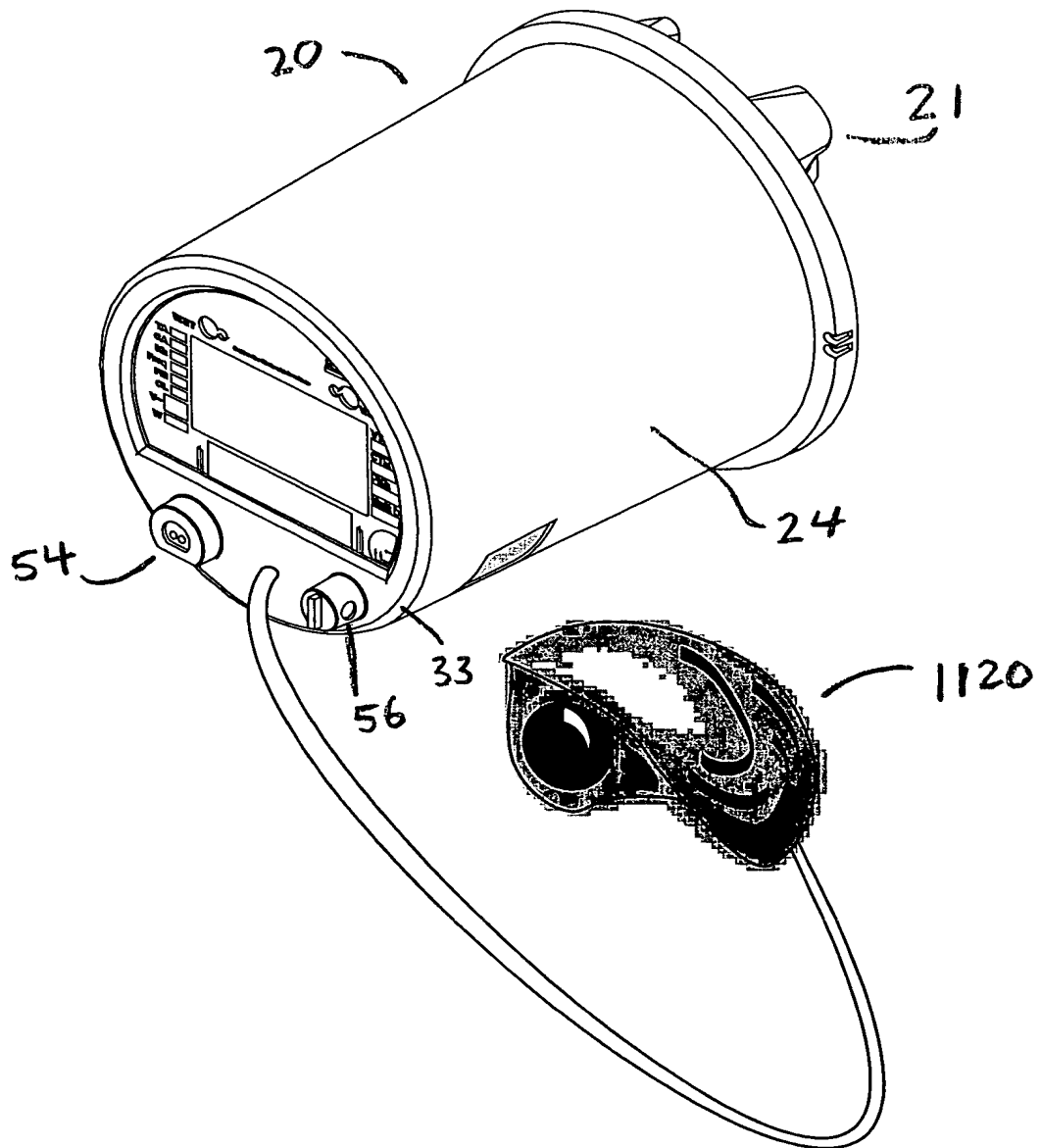


Fig. 11c

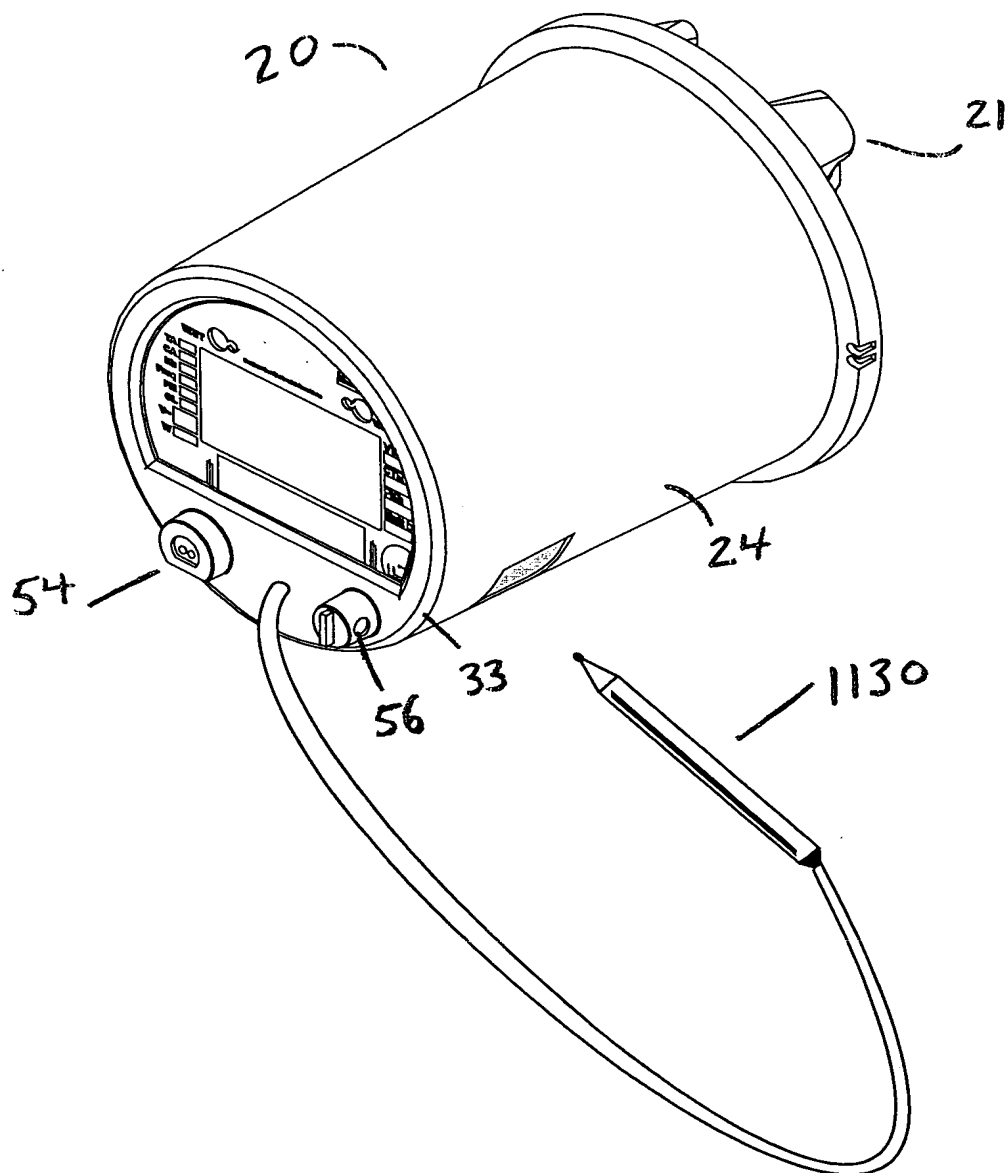


Fig. 11d

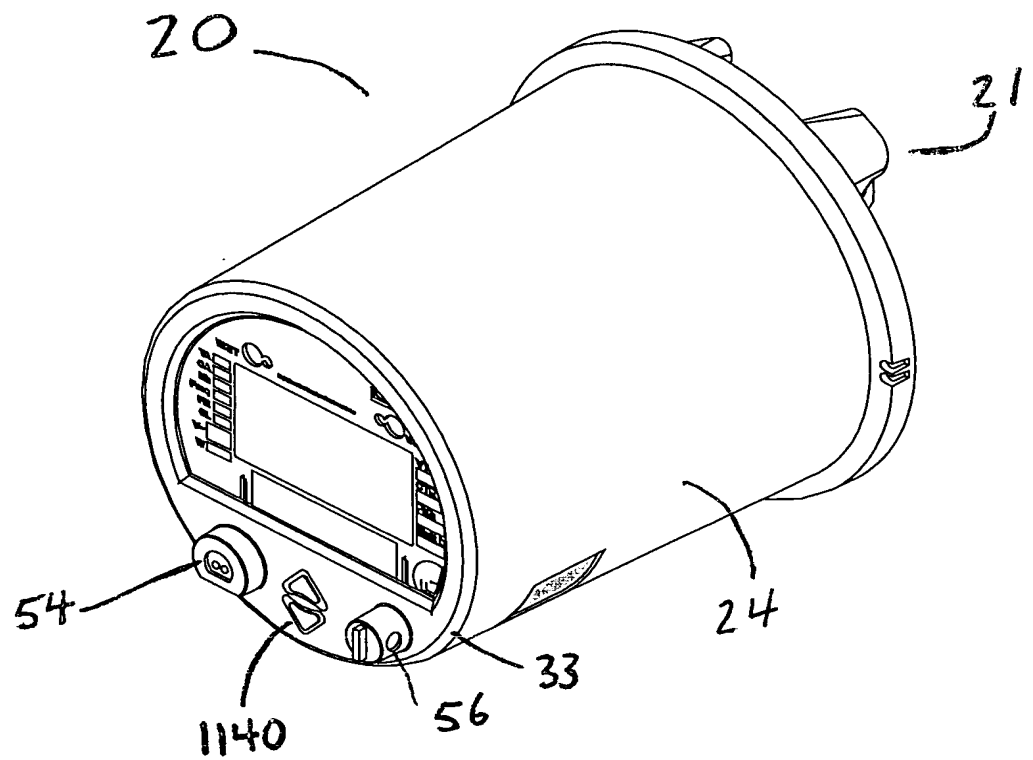


Fig. 11e

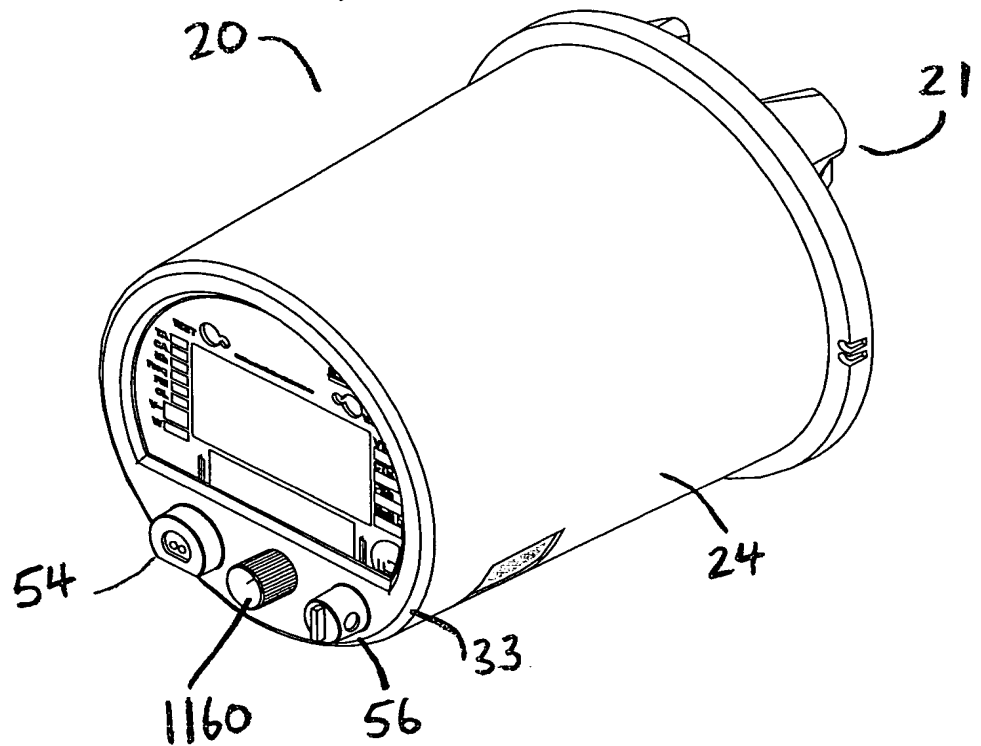


Fig. 11g

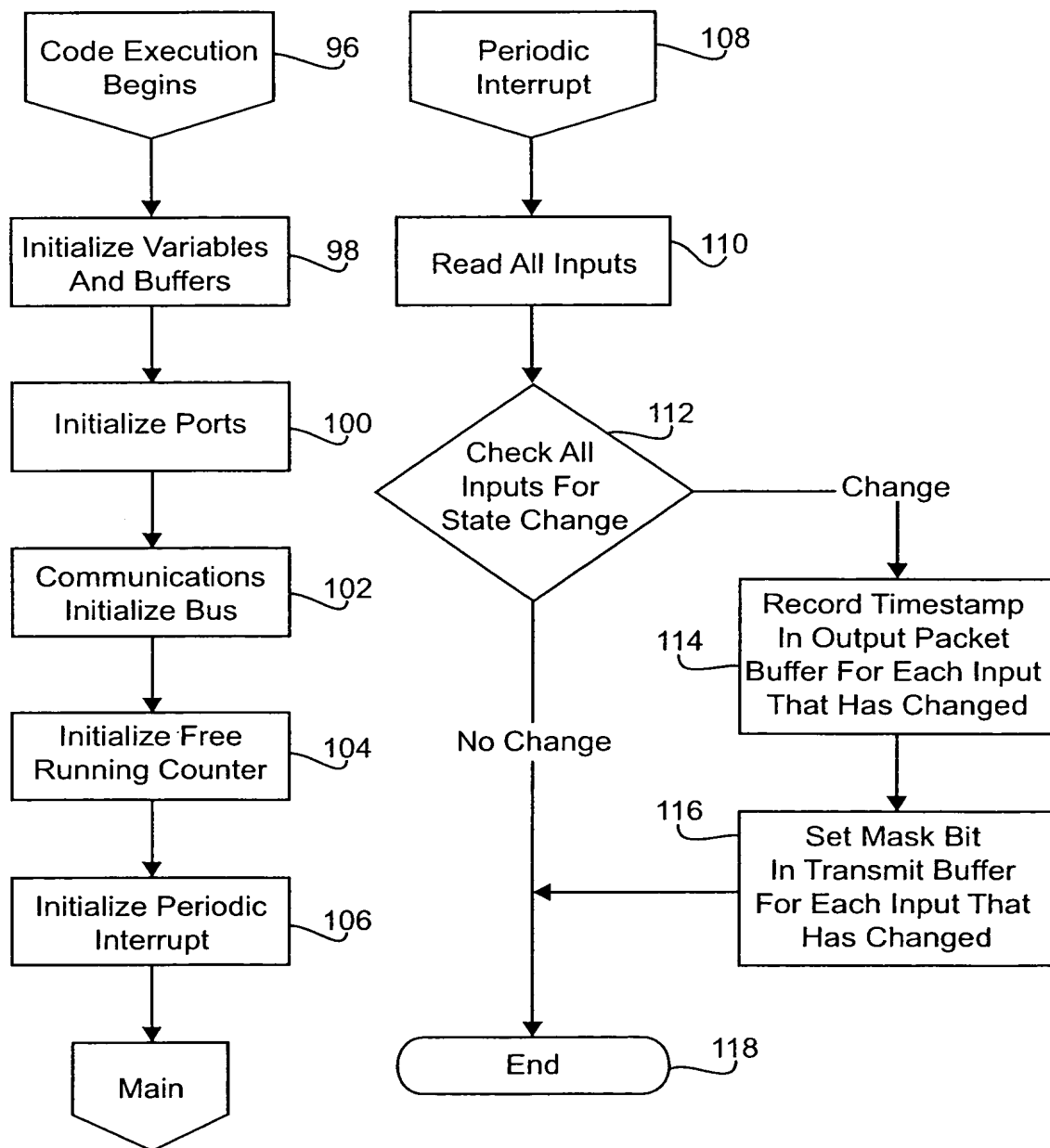


FIG. 12A

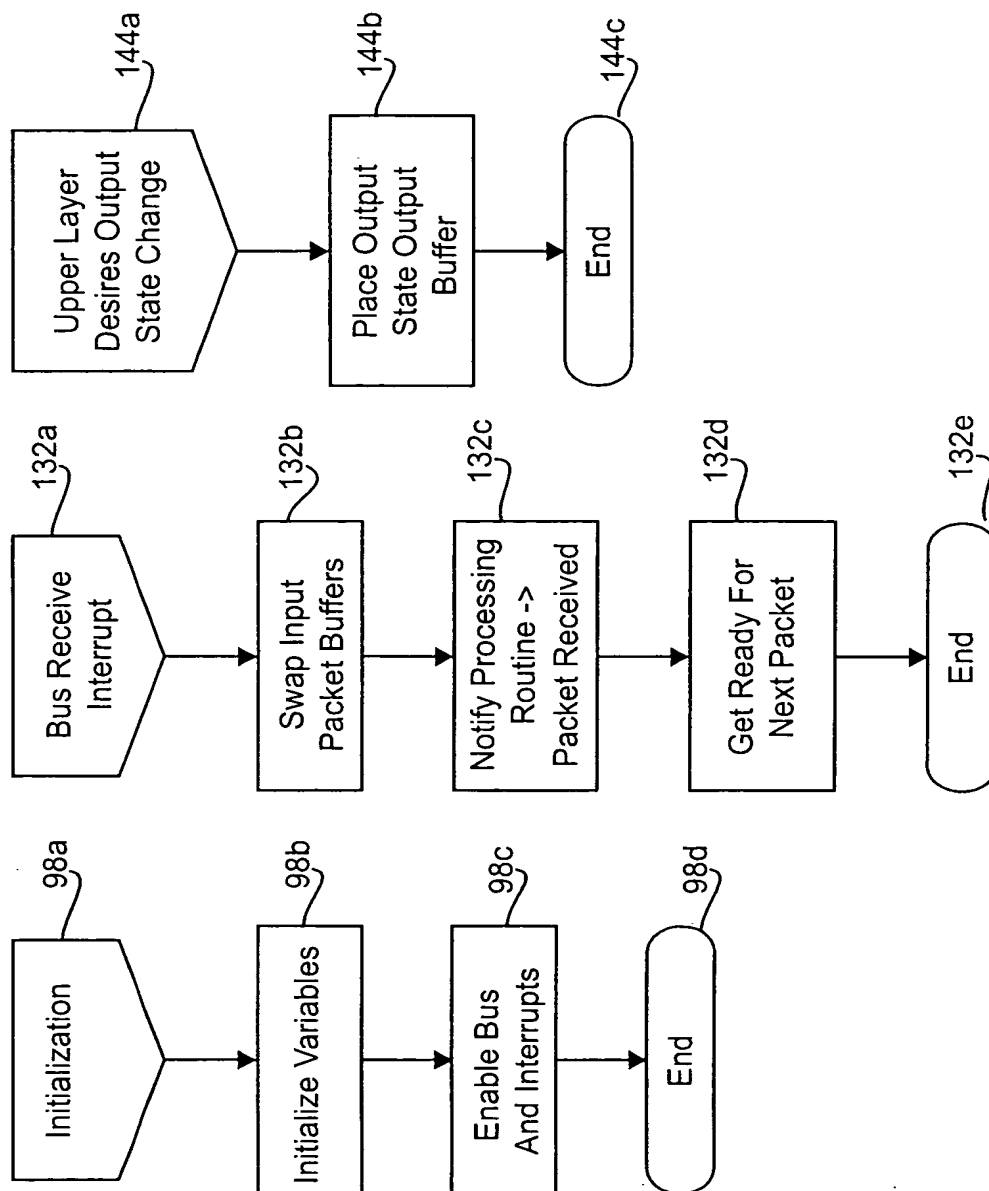


FIG. 12B

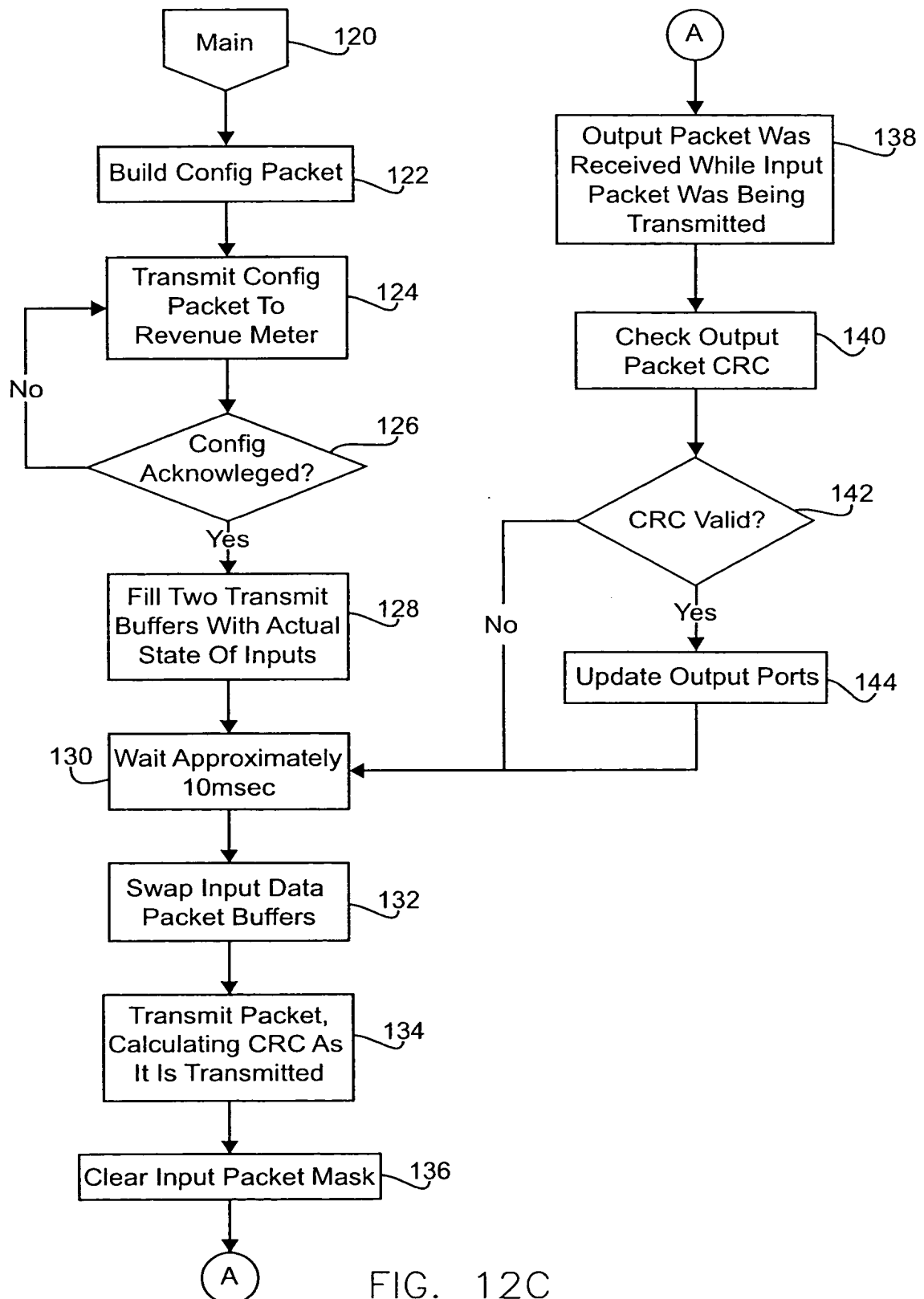


FIG. 12C

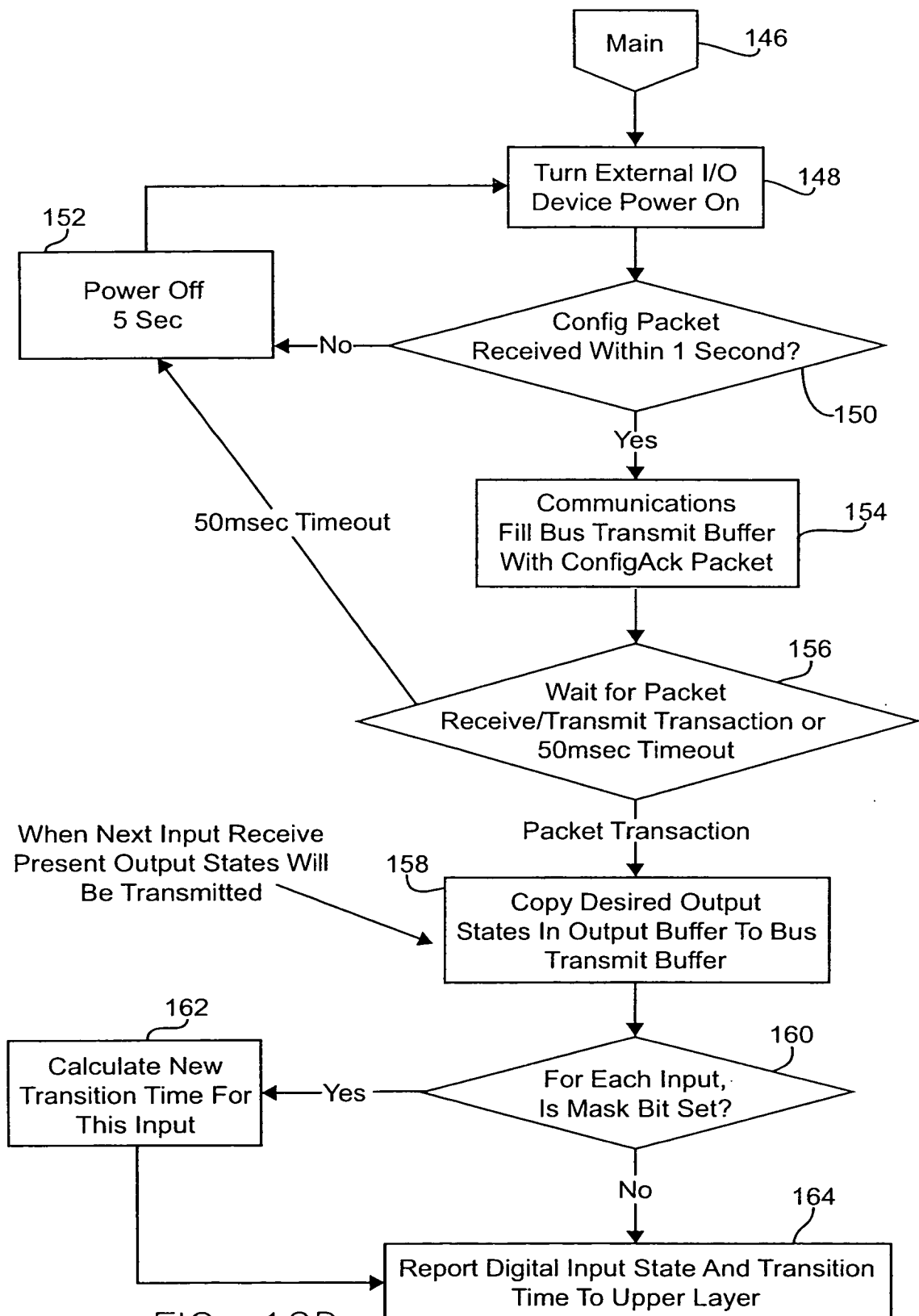


FIG. 12D

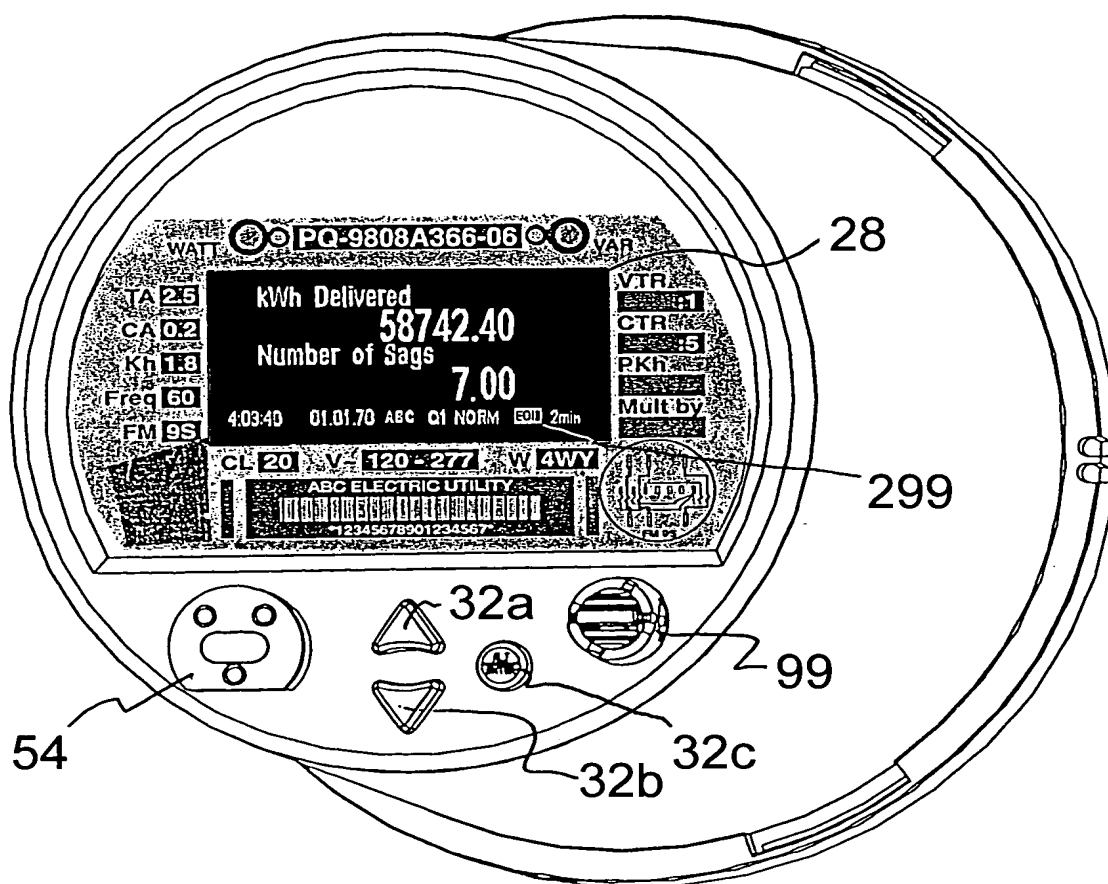


FIG. 13

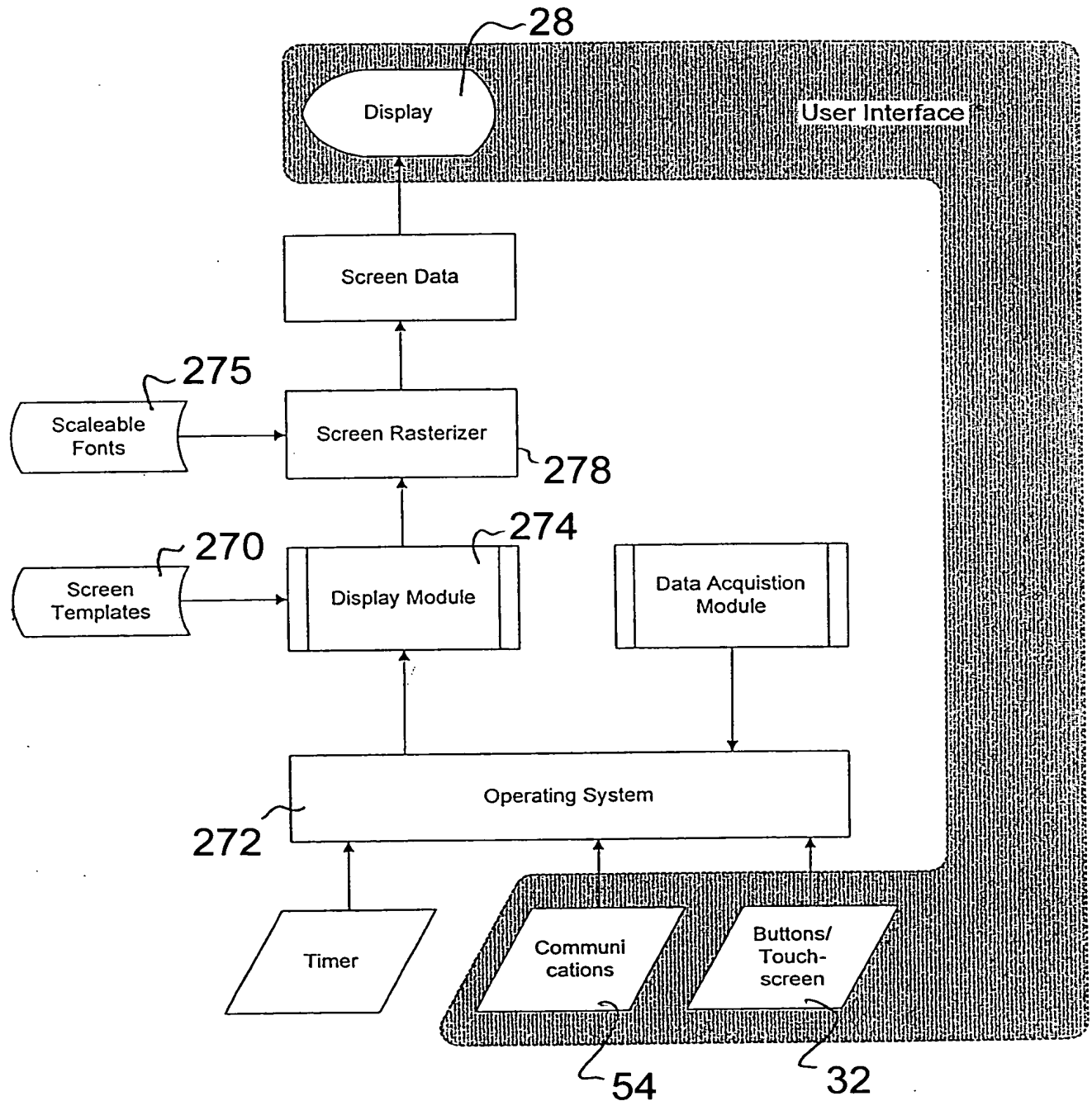


FIG. 14

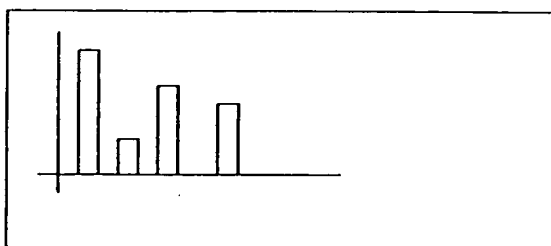


FIG. 15

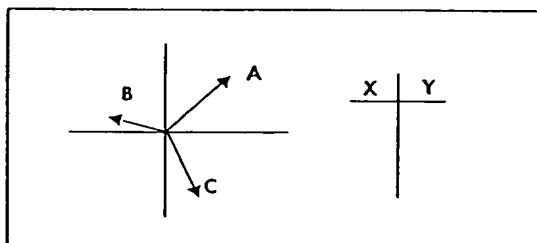


FIG. 16

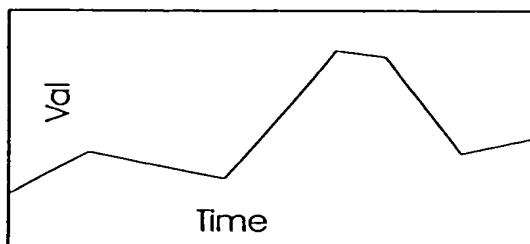


FIG. 17

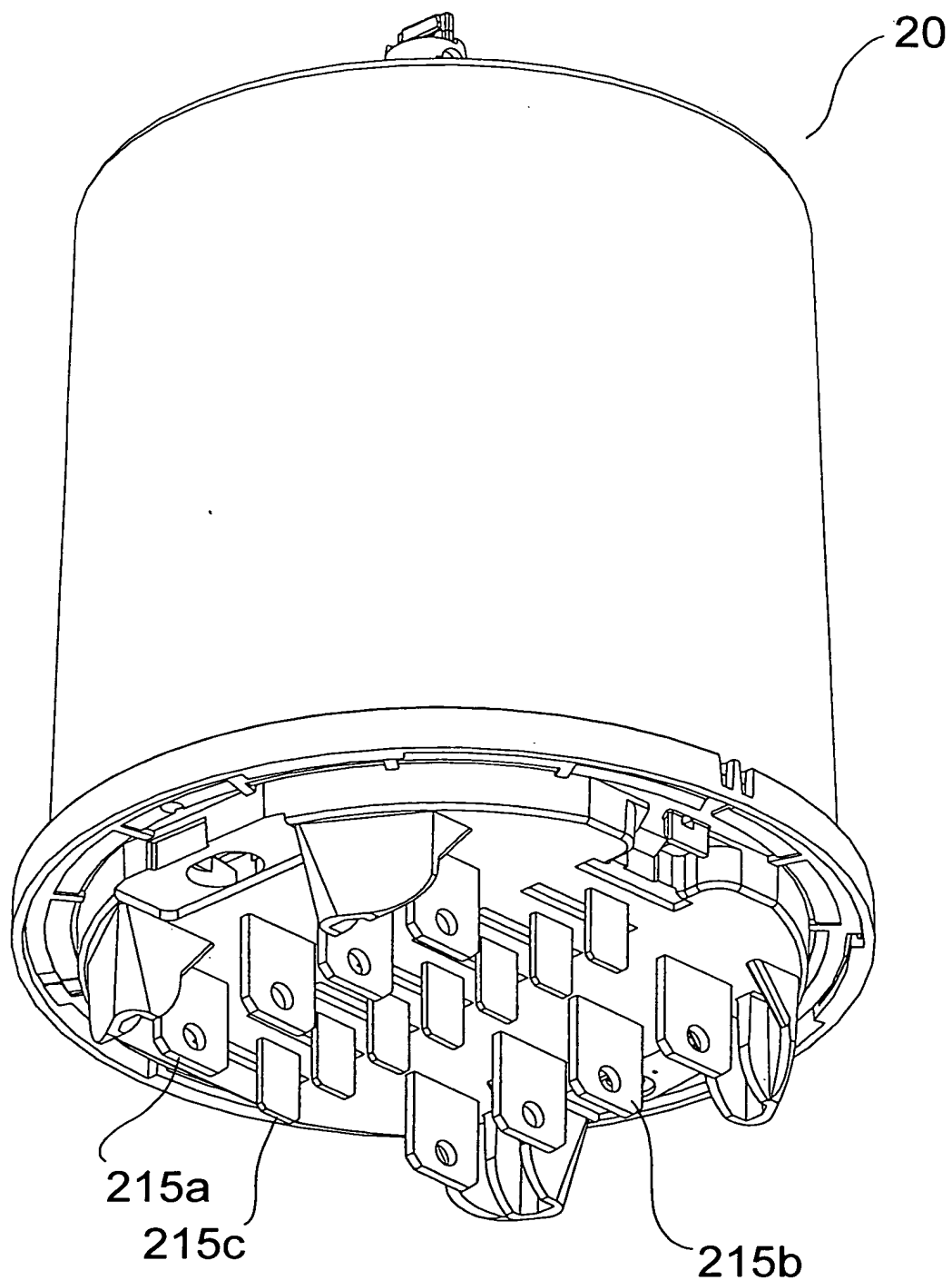


FIG. 18

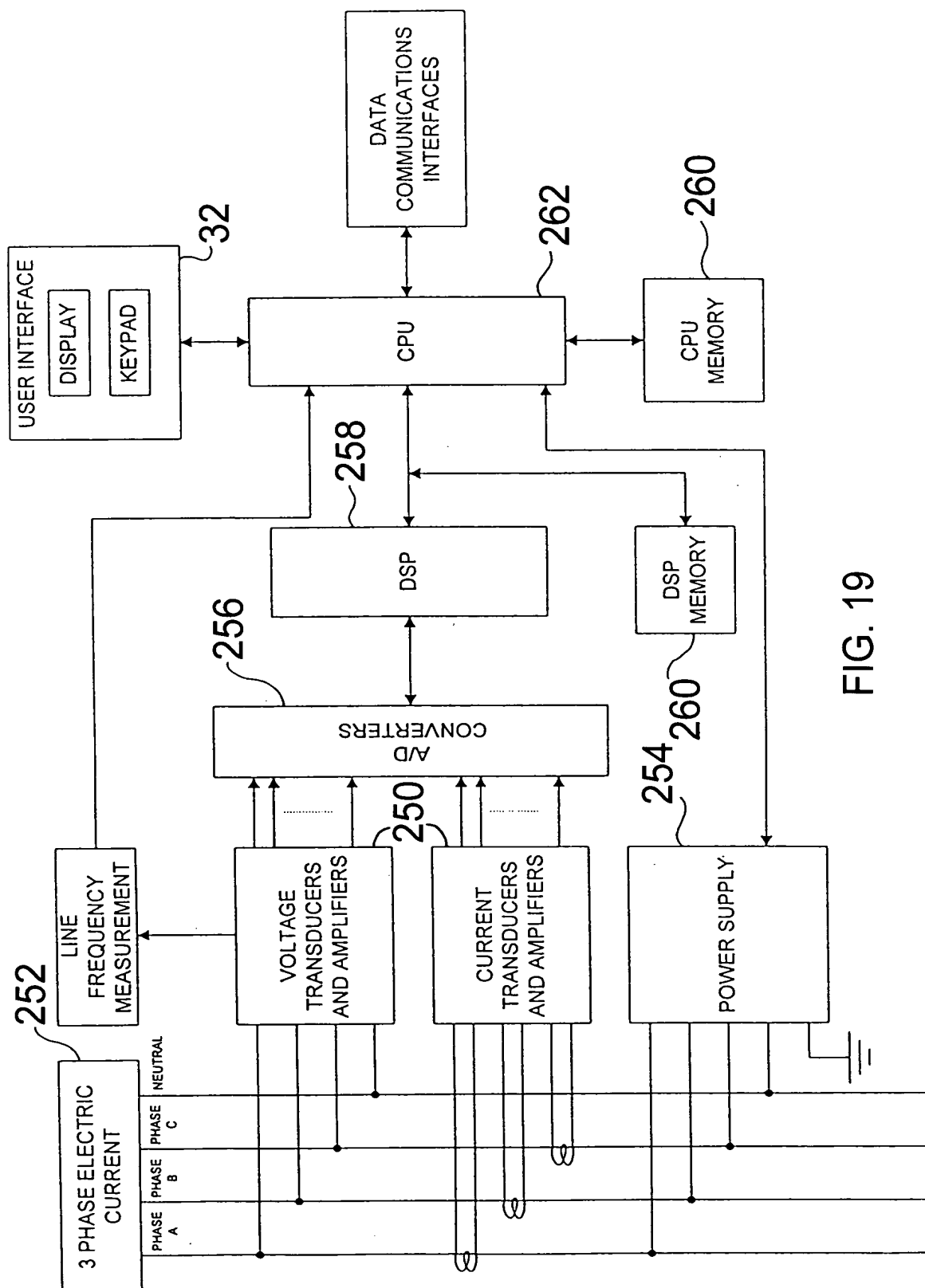
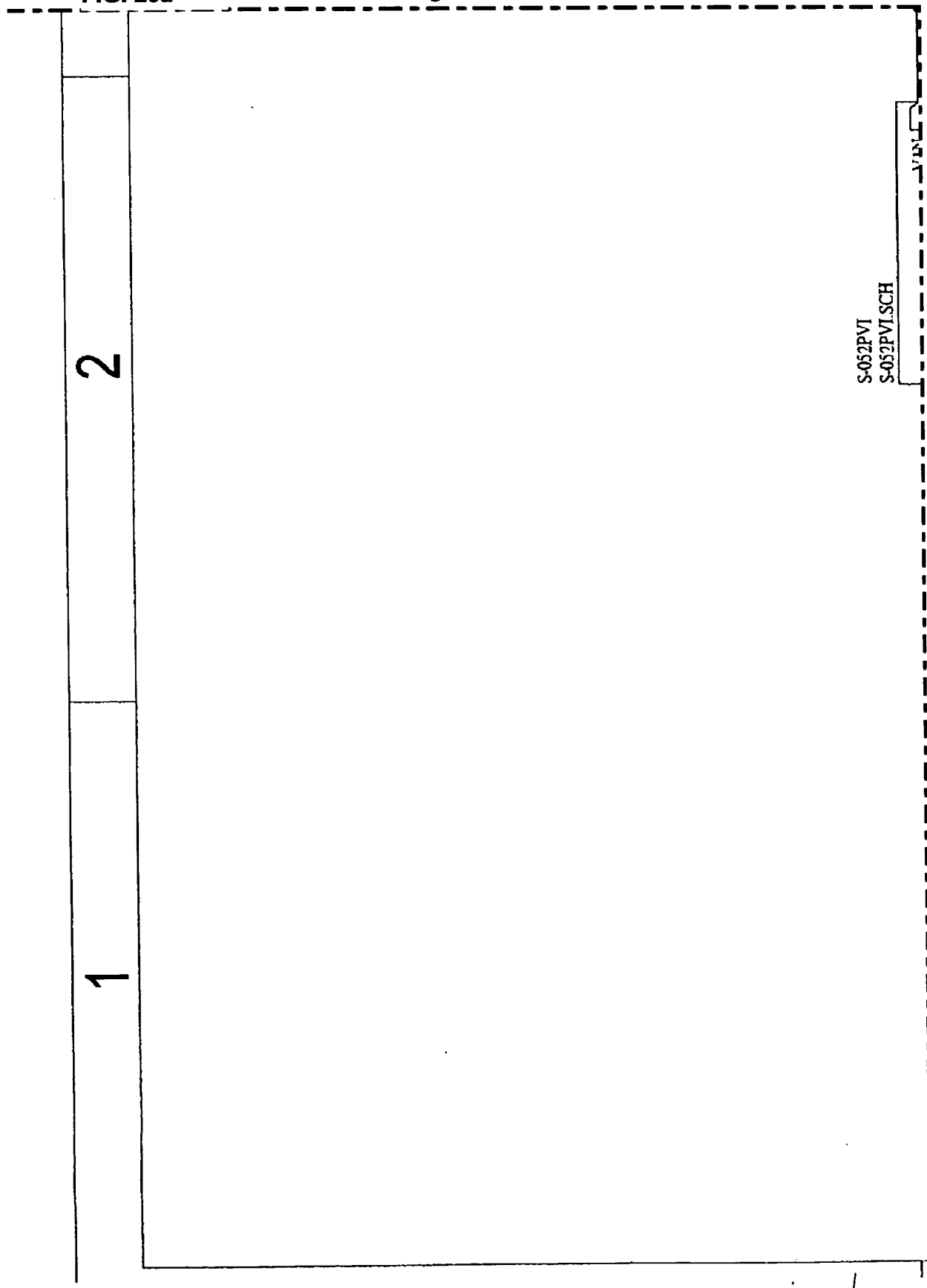


FIG. 19

FIG. 20a

To Fig. 20b



To Fig. 20d

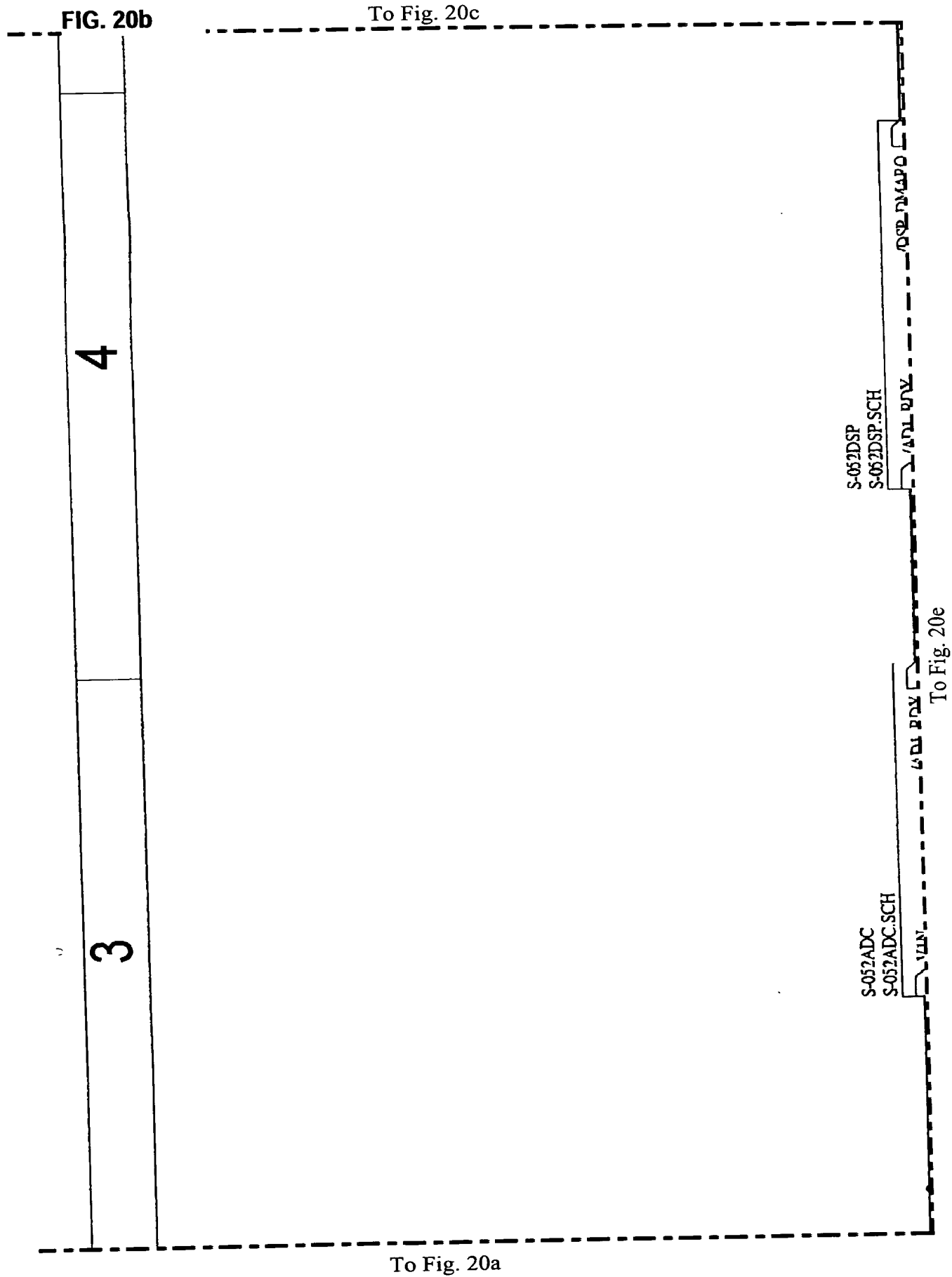
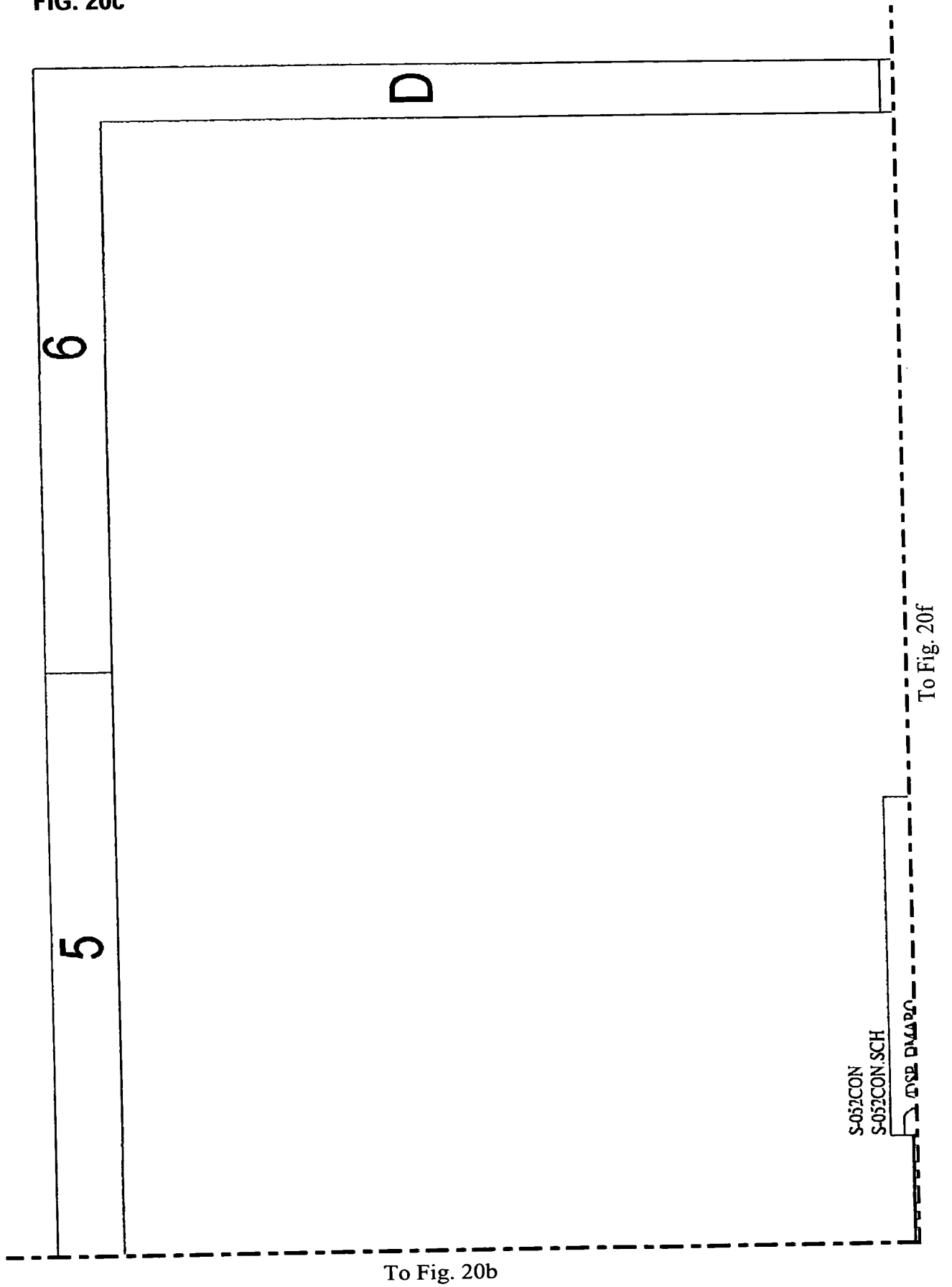
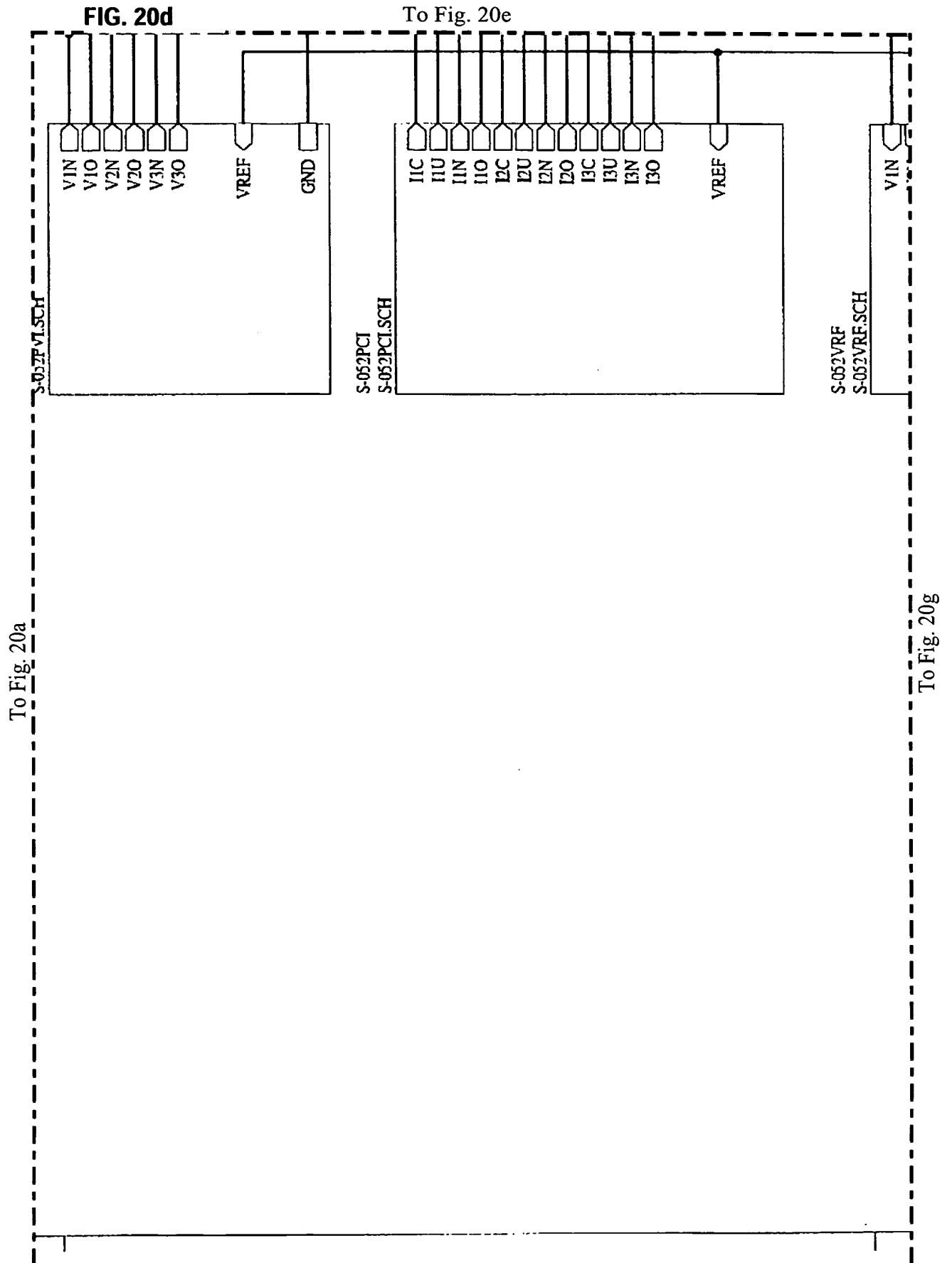


FIG. 20c





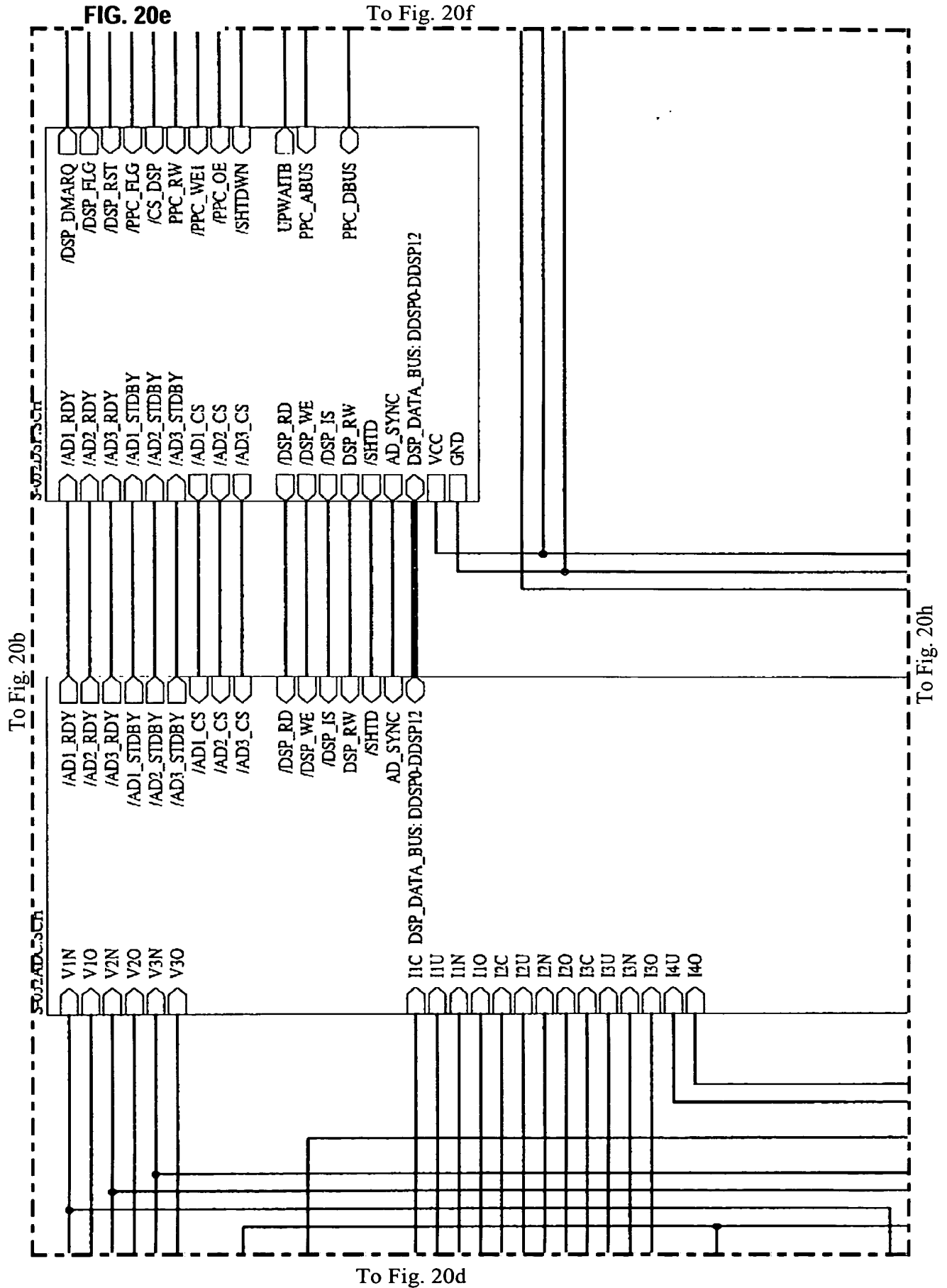
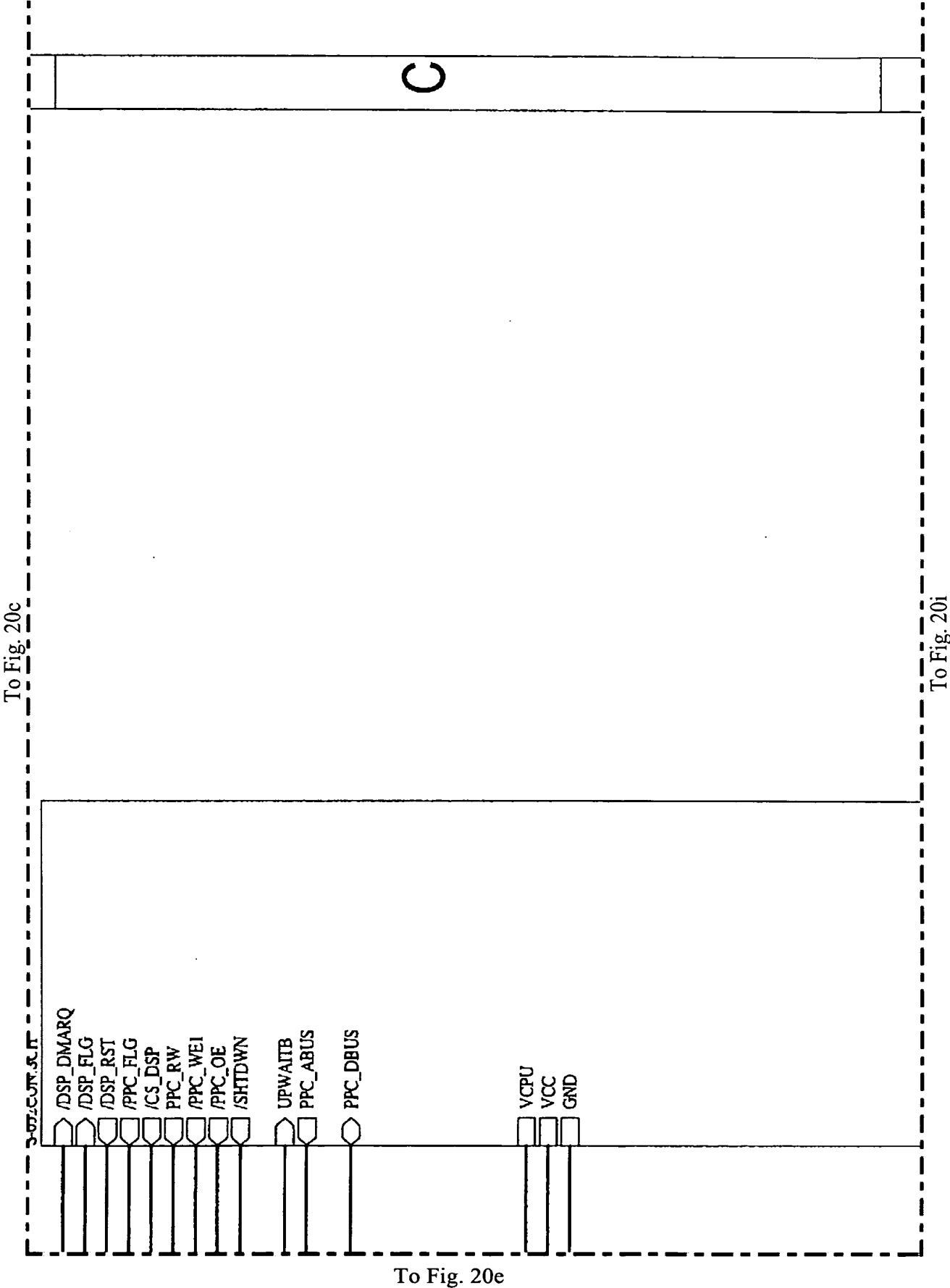
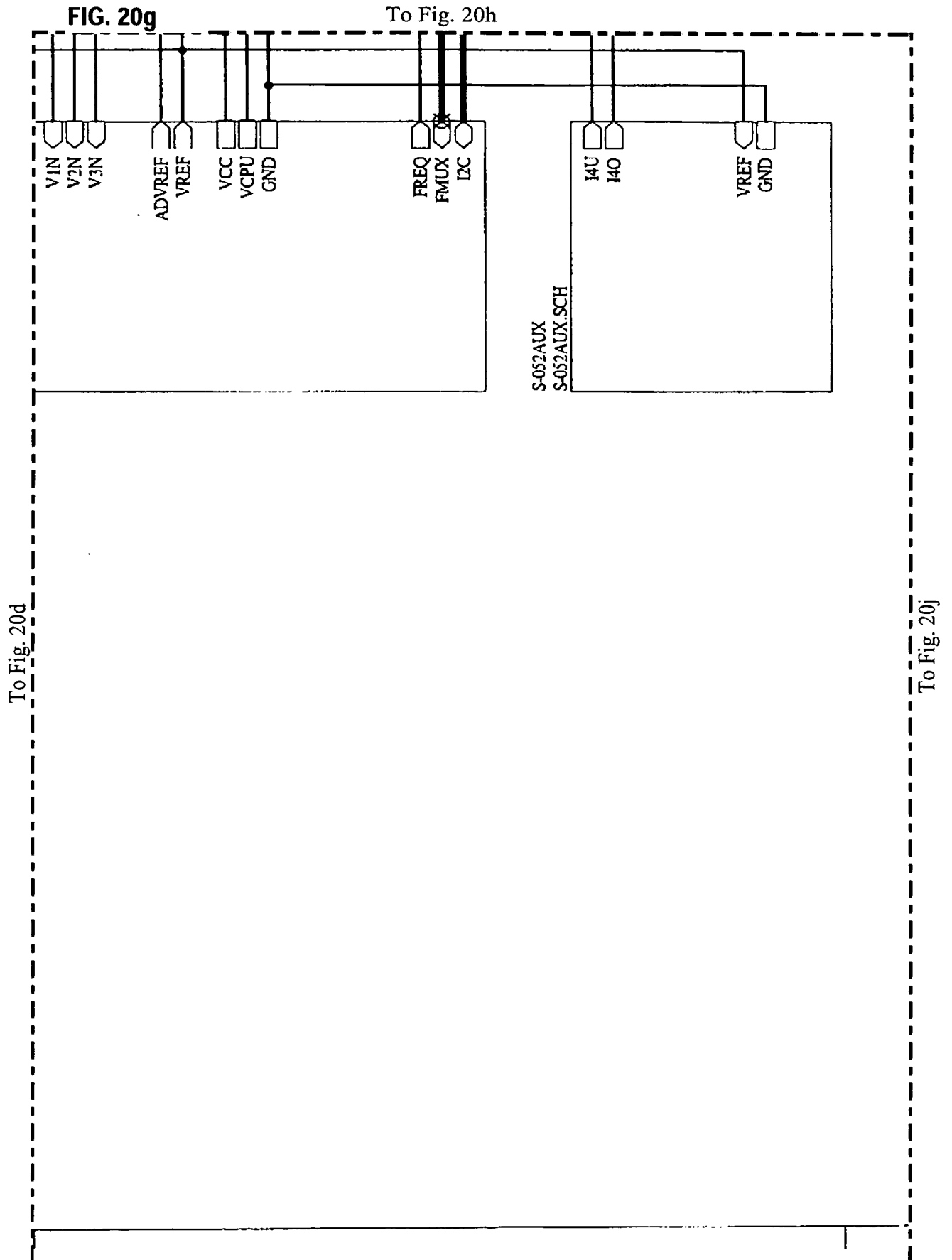


FIG. 20f





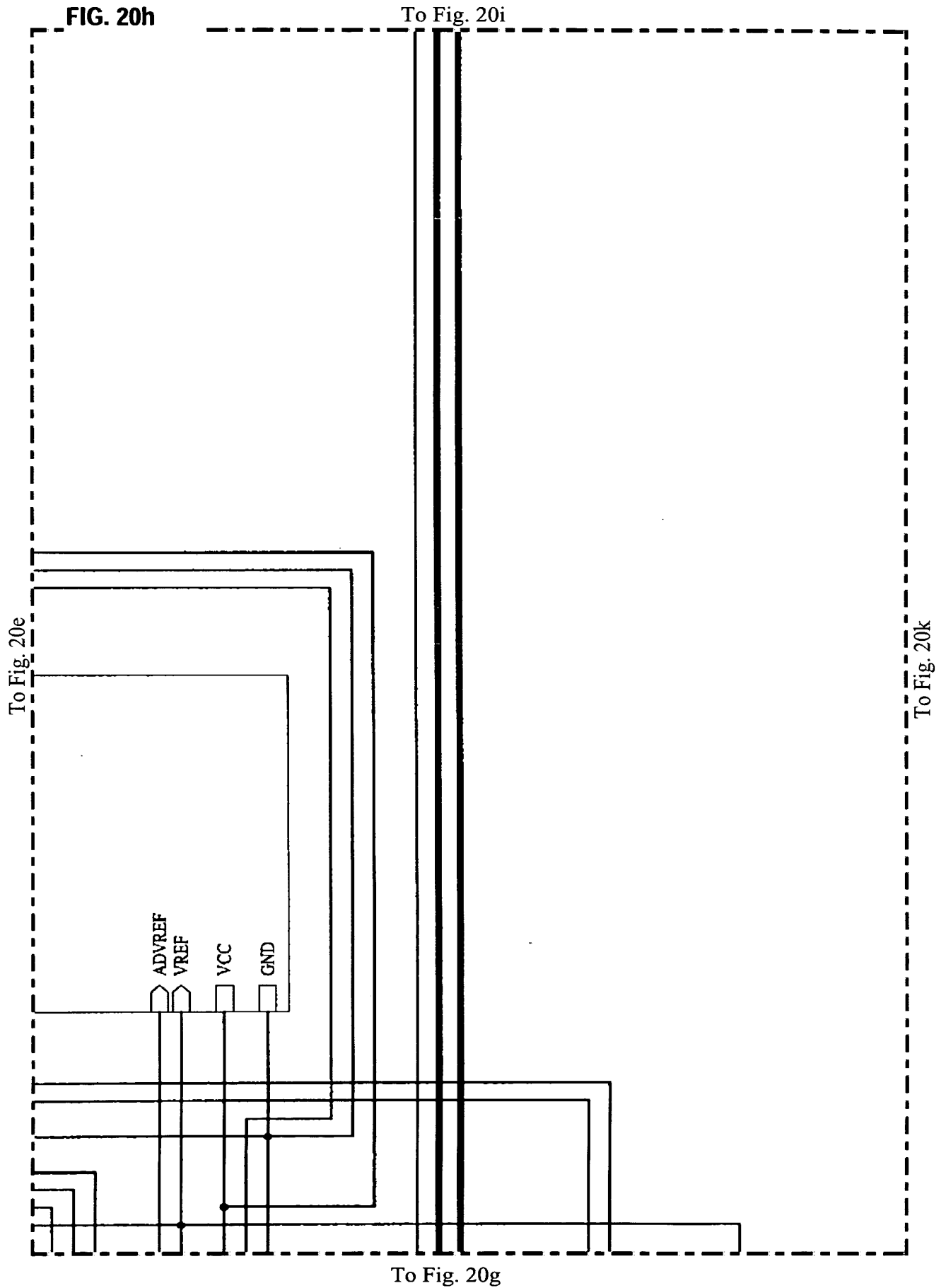


FIG. 20i

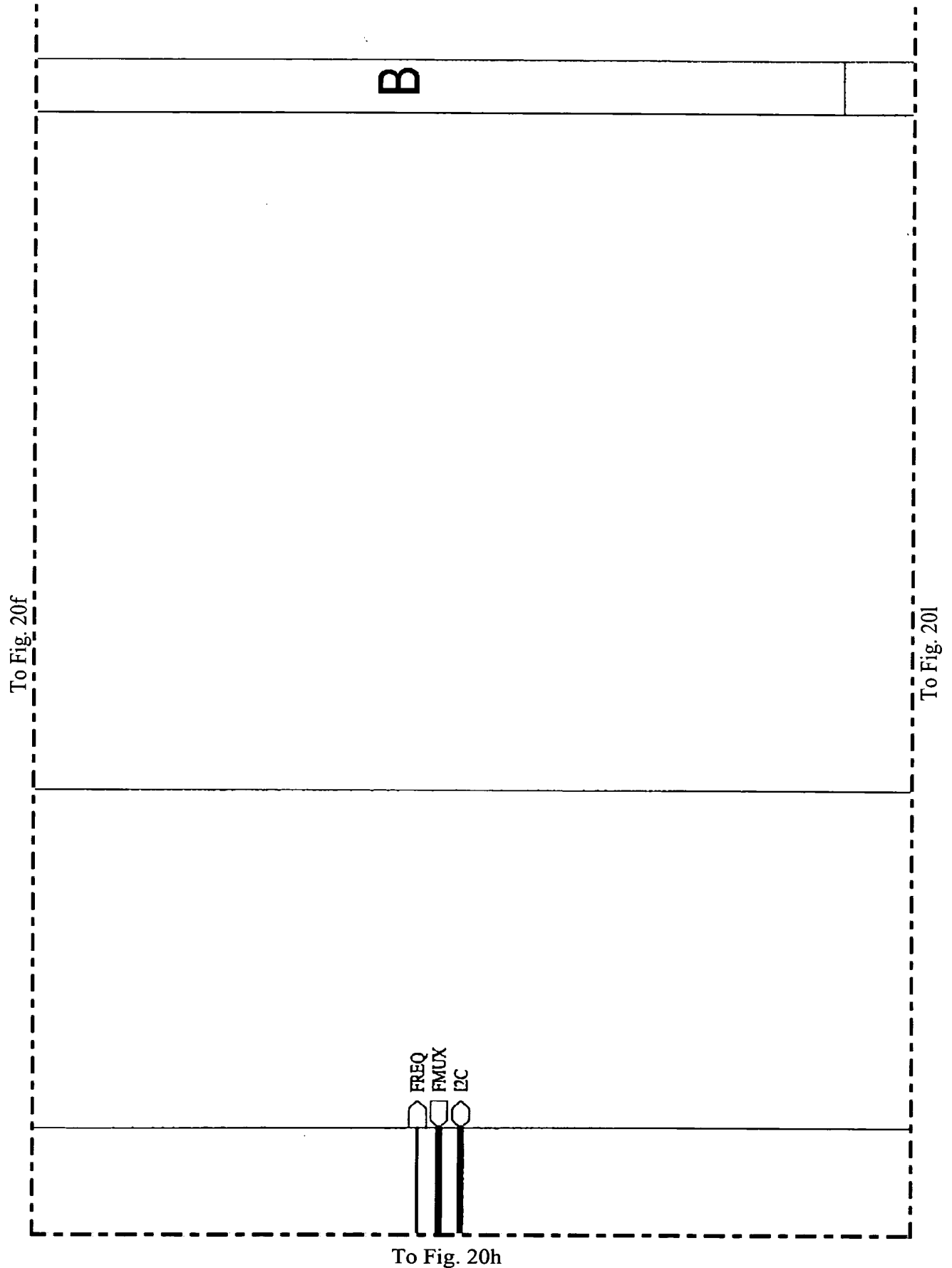


FIG. 20j

To Fig. 20k

To Fig. 20g

2

1

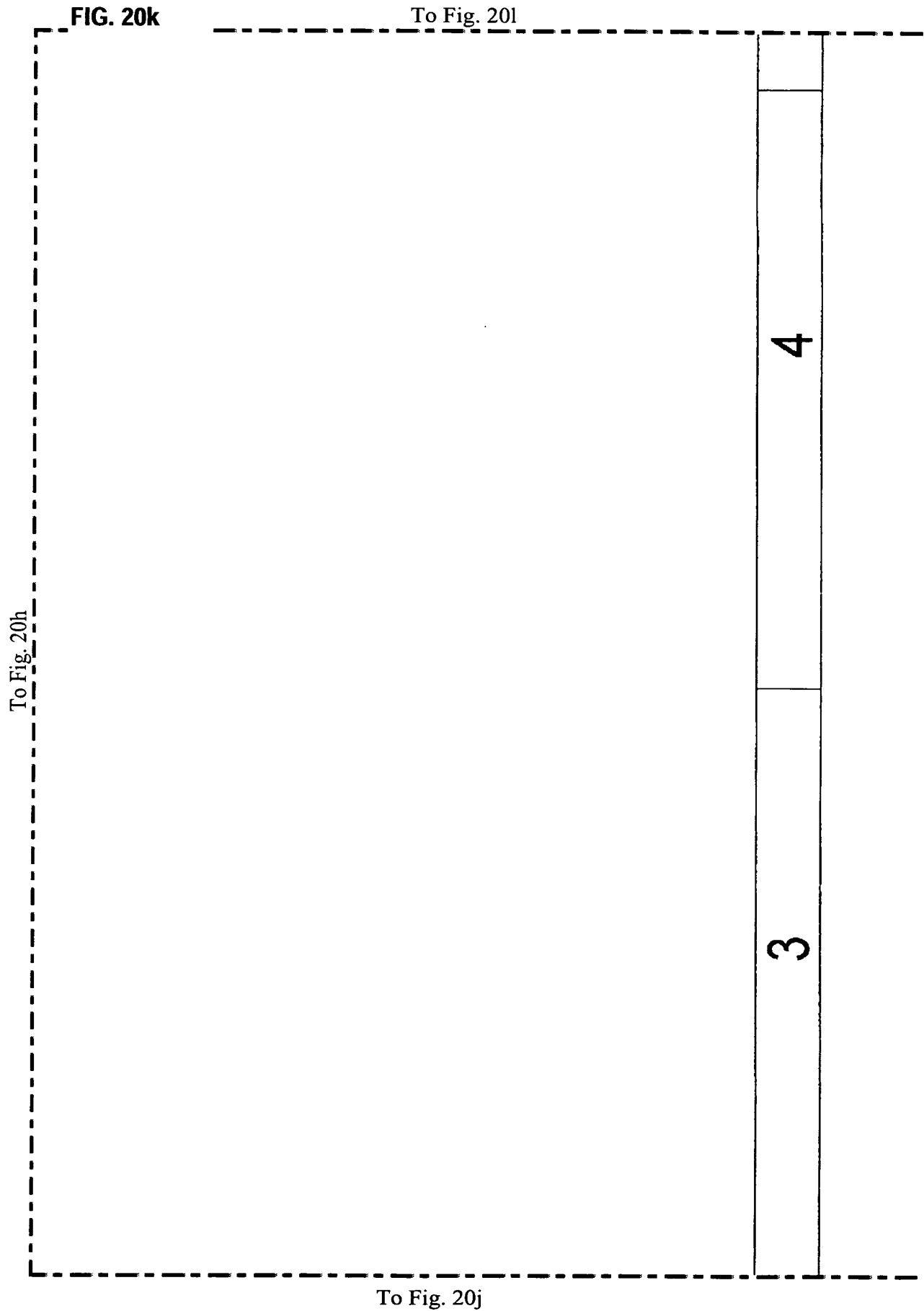
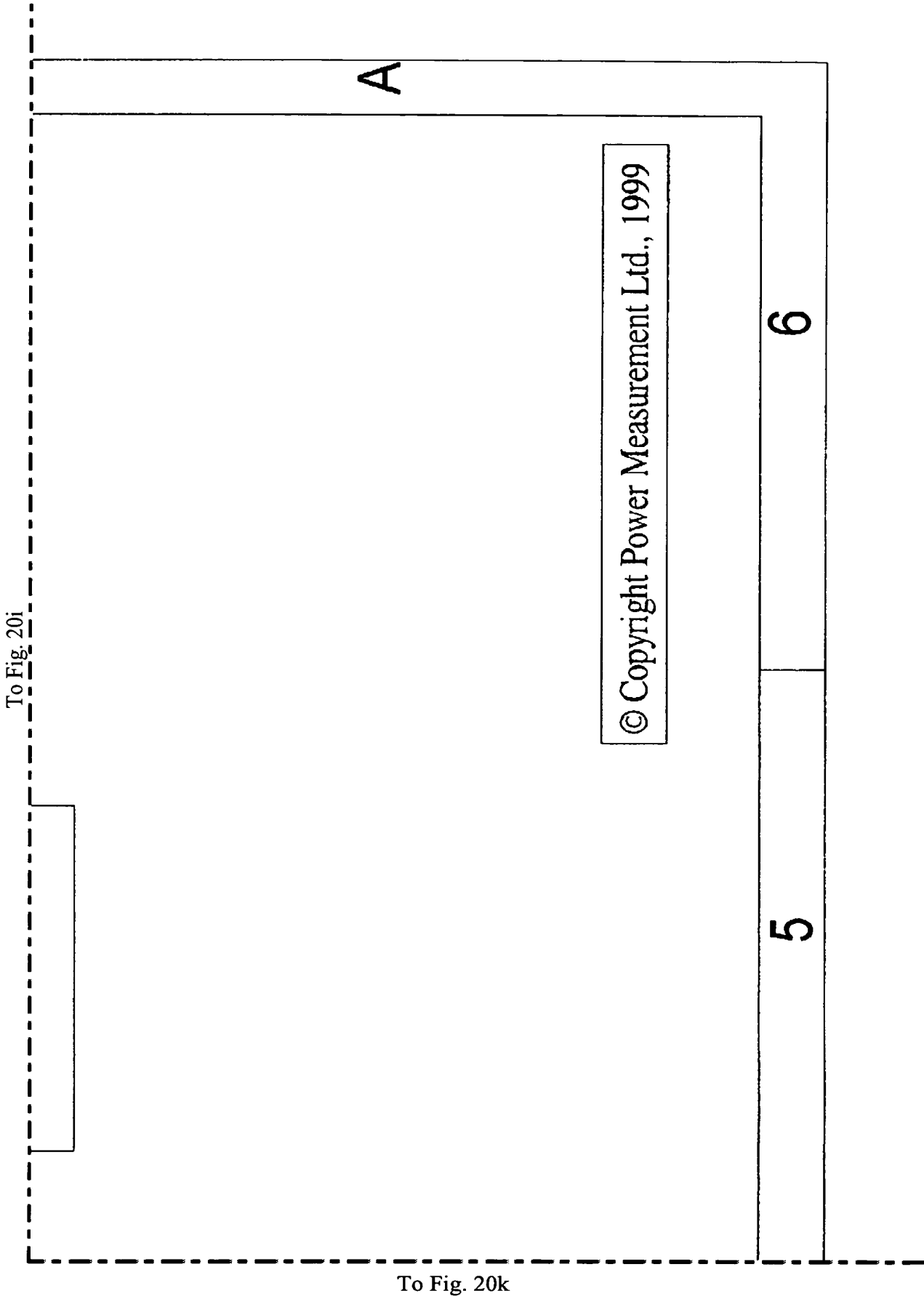


FIG. 20i



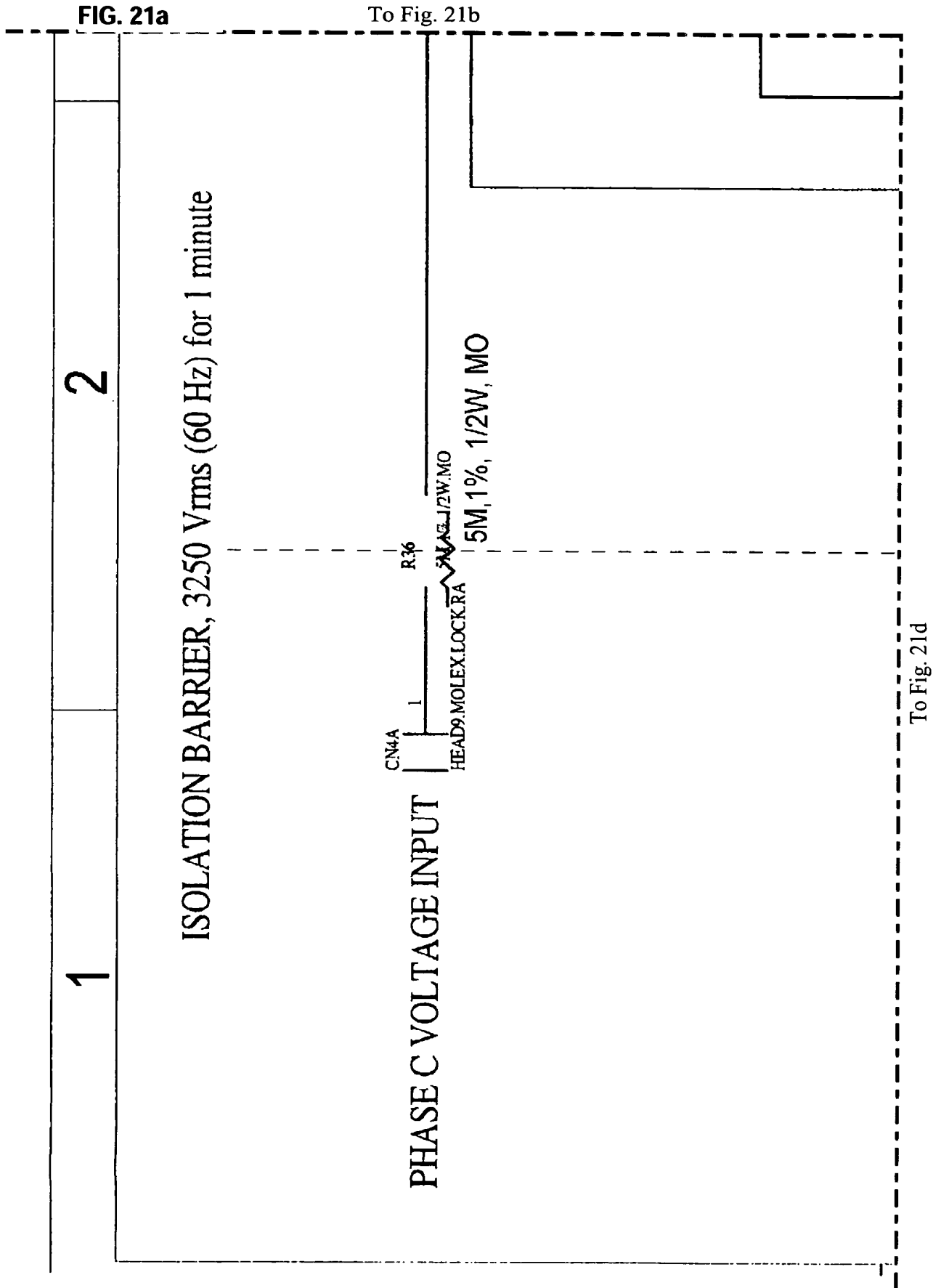


FIG. 21b

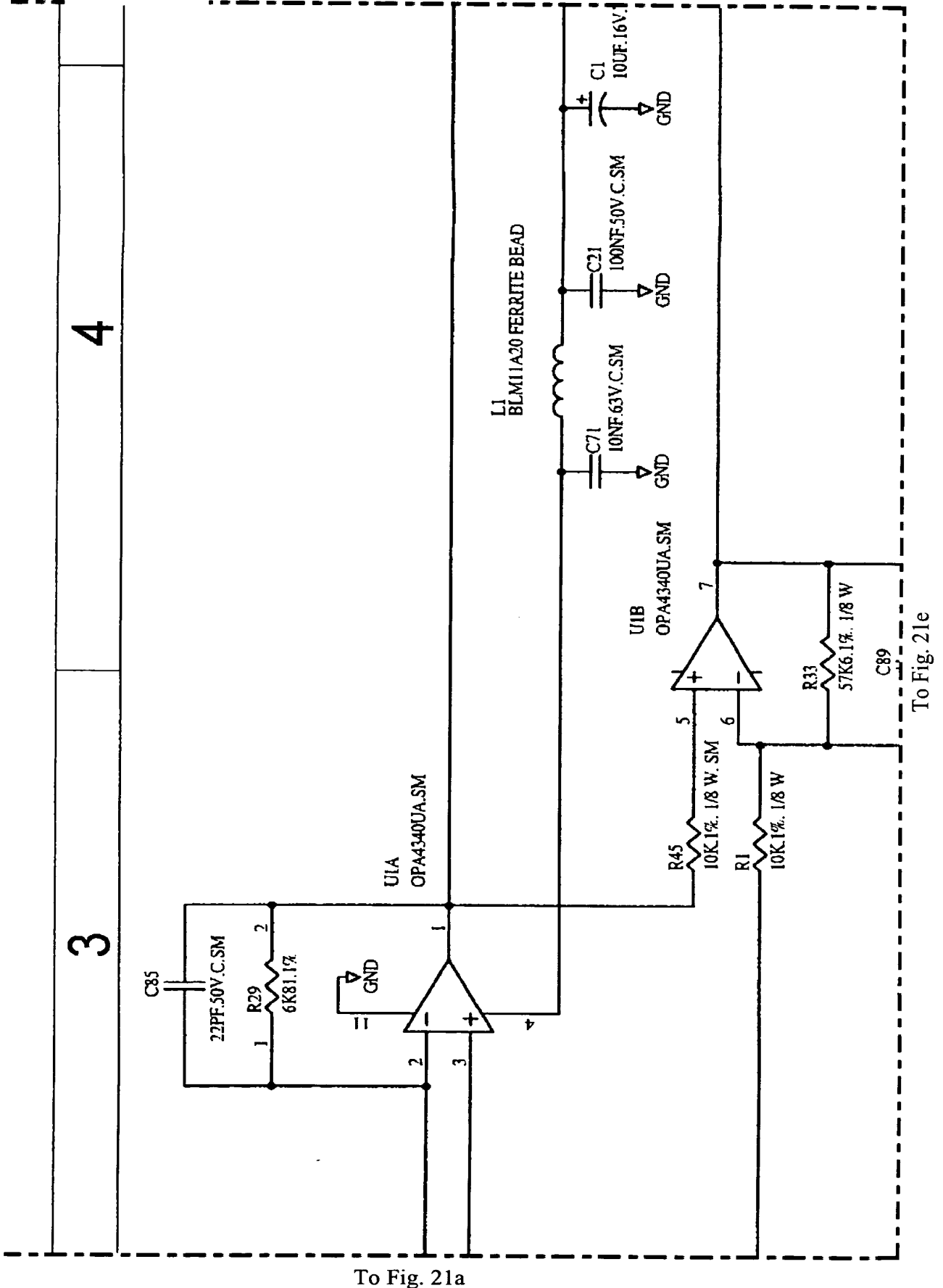
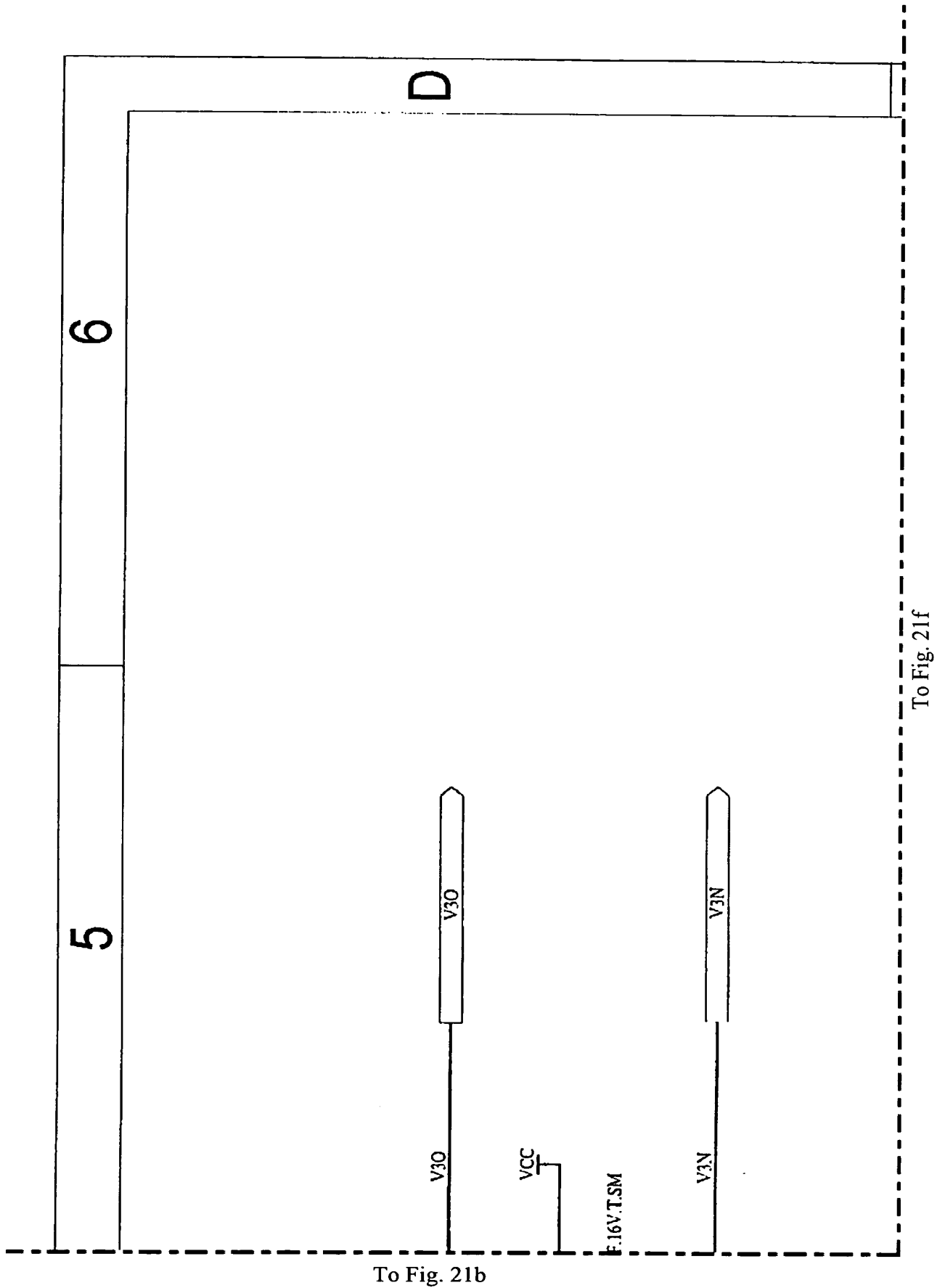
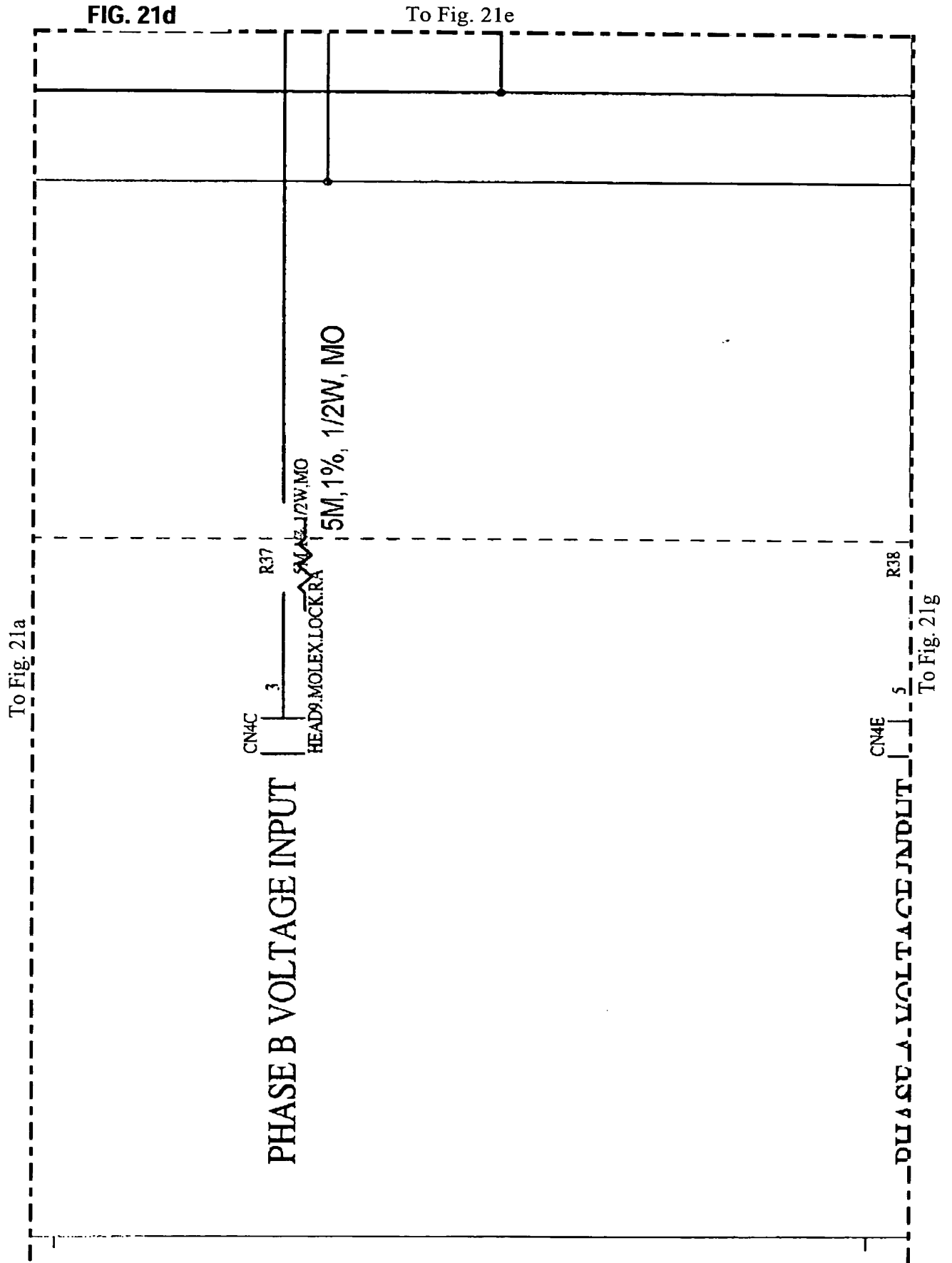


FIG. 21c





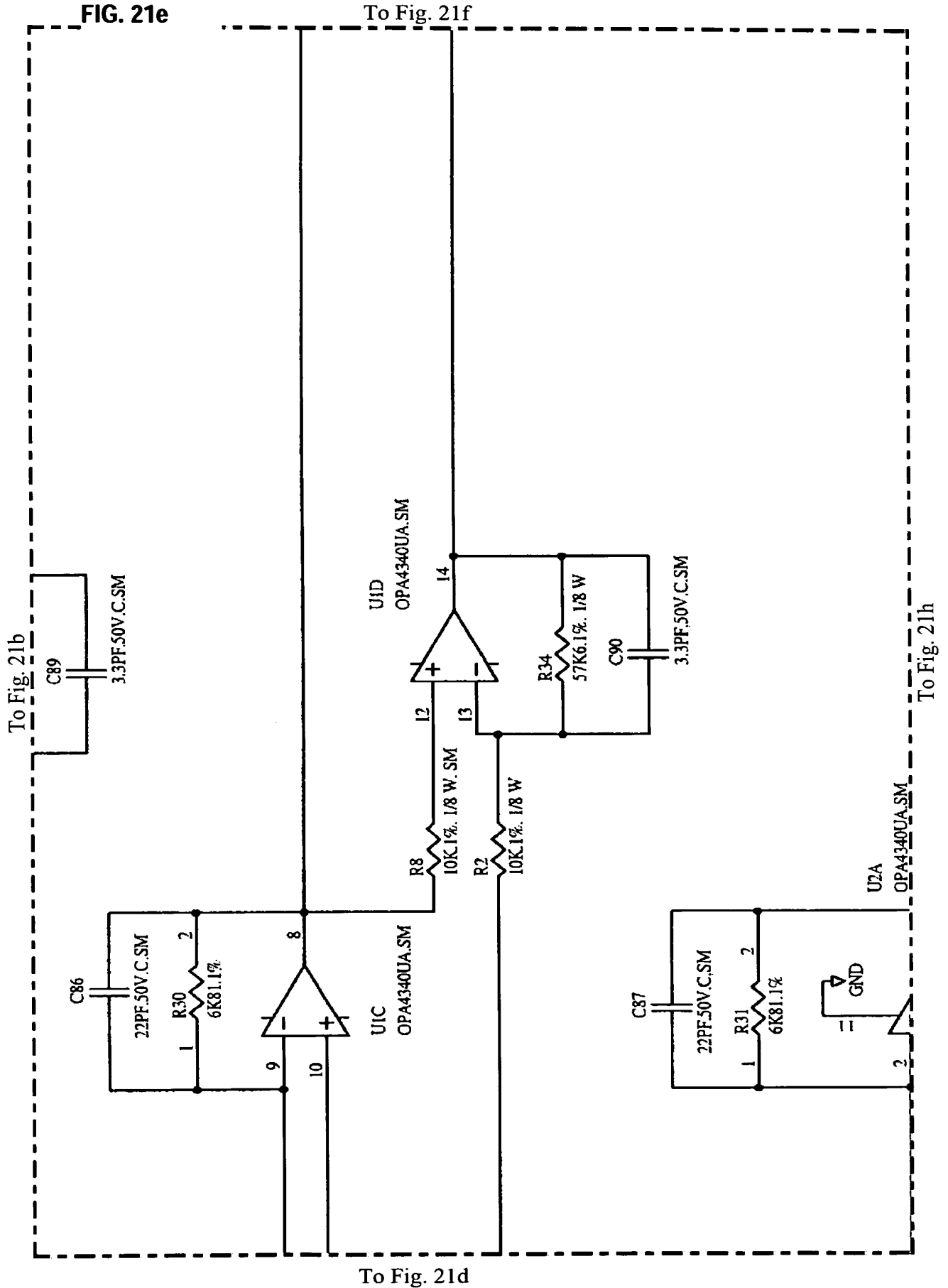
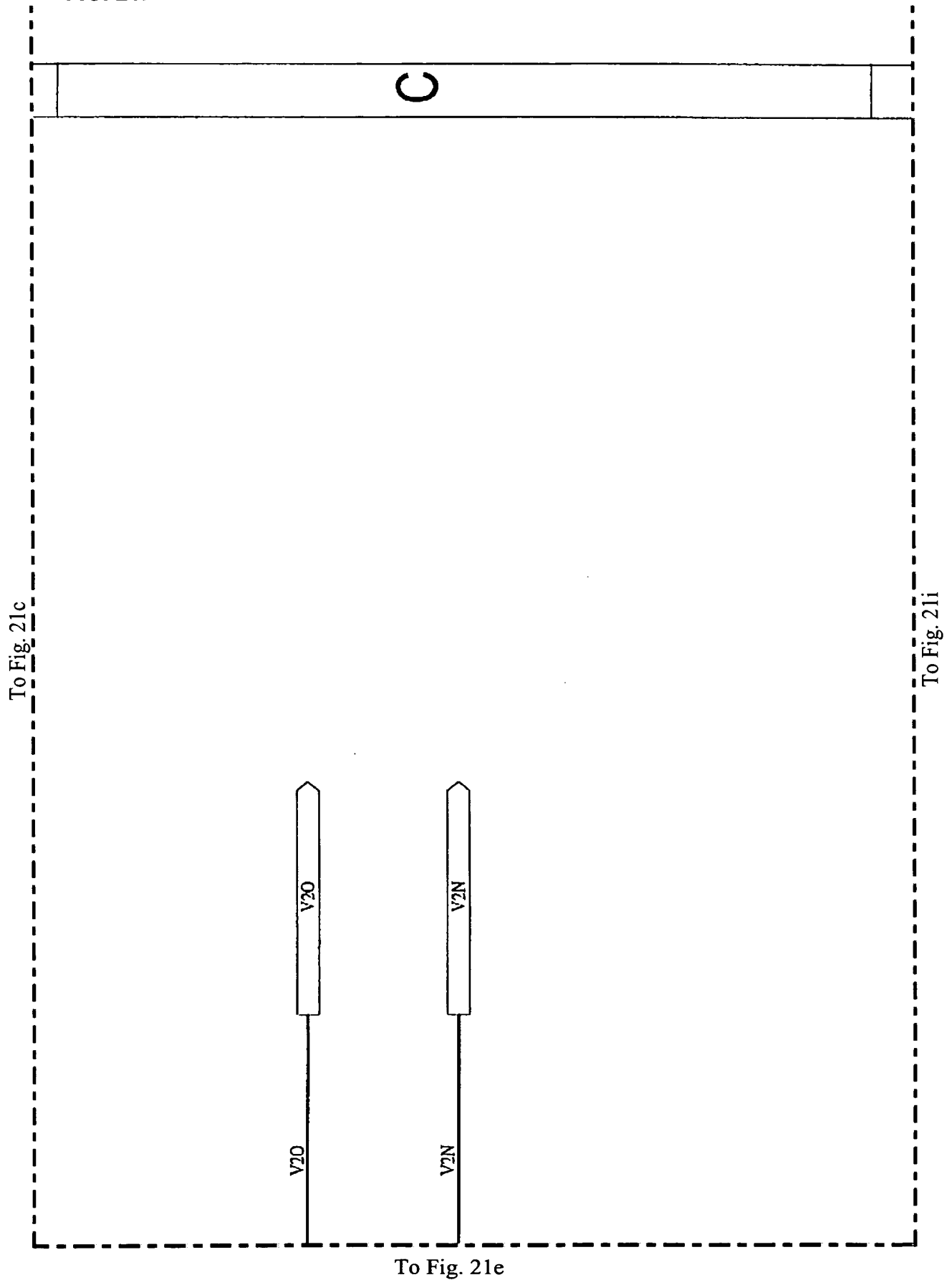
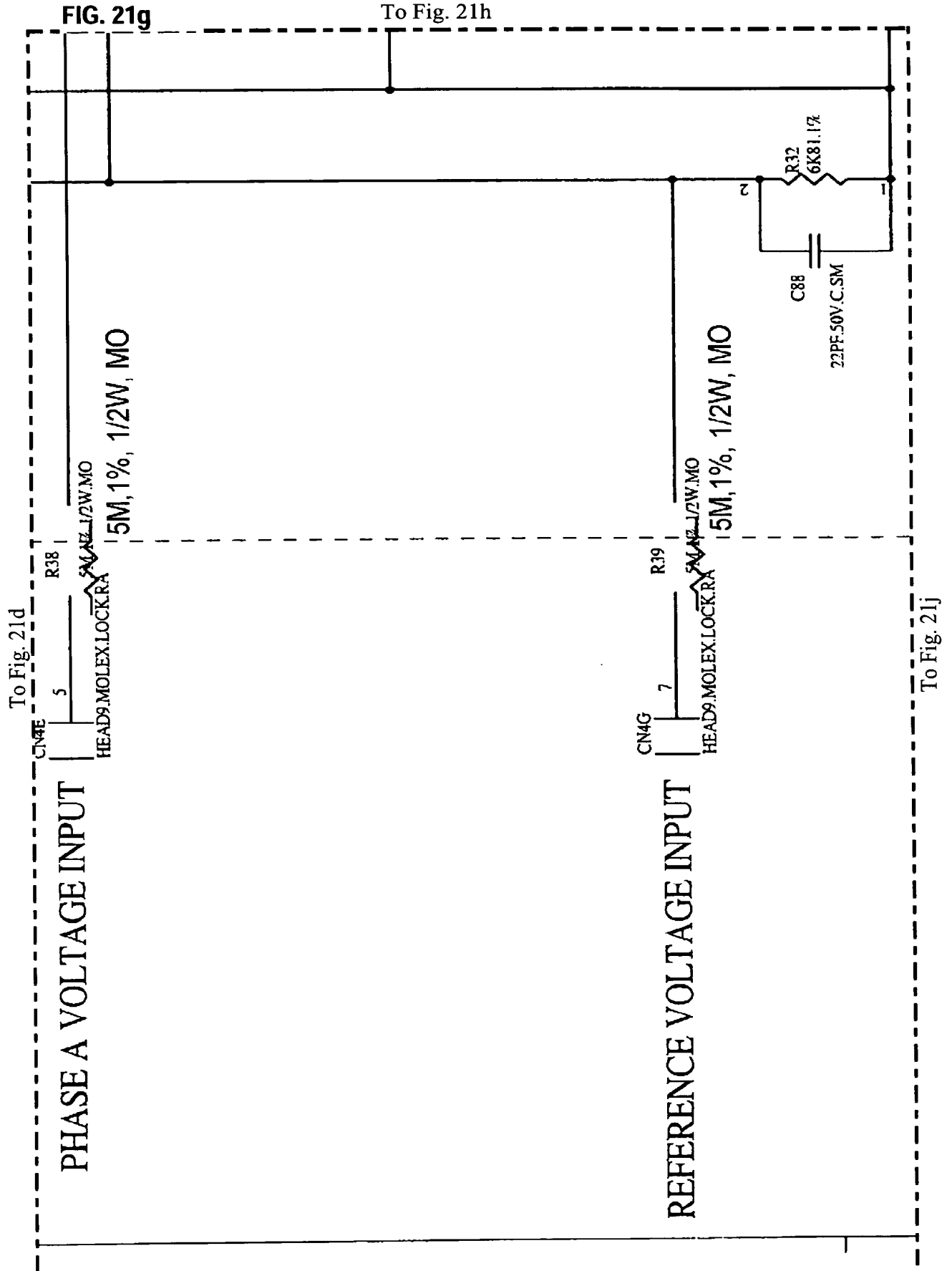


FIG. 21f





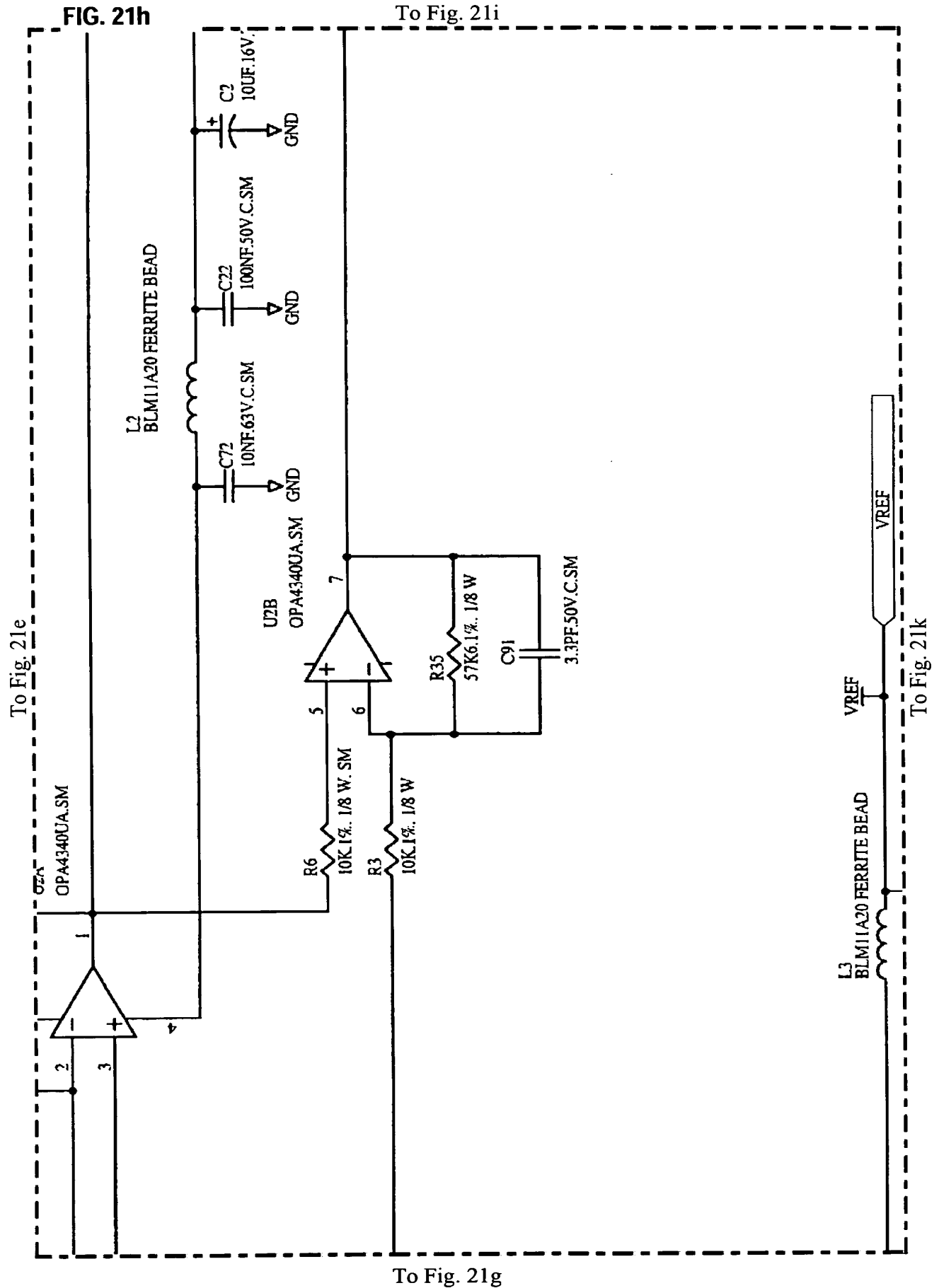
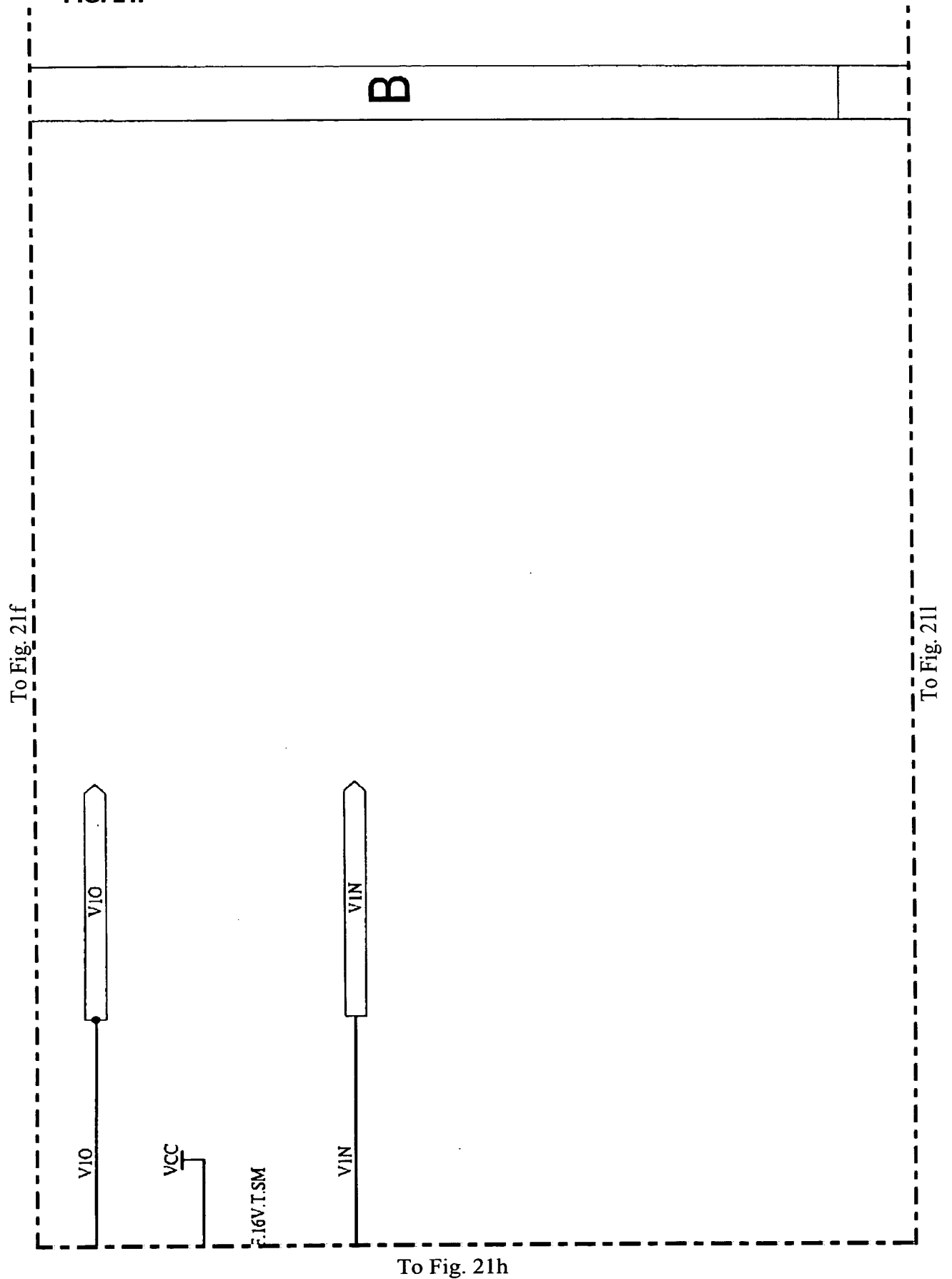
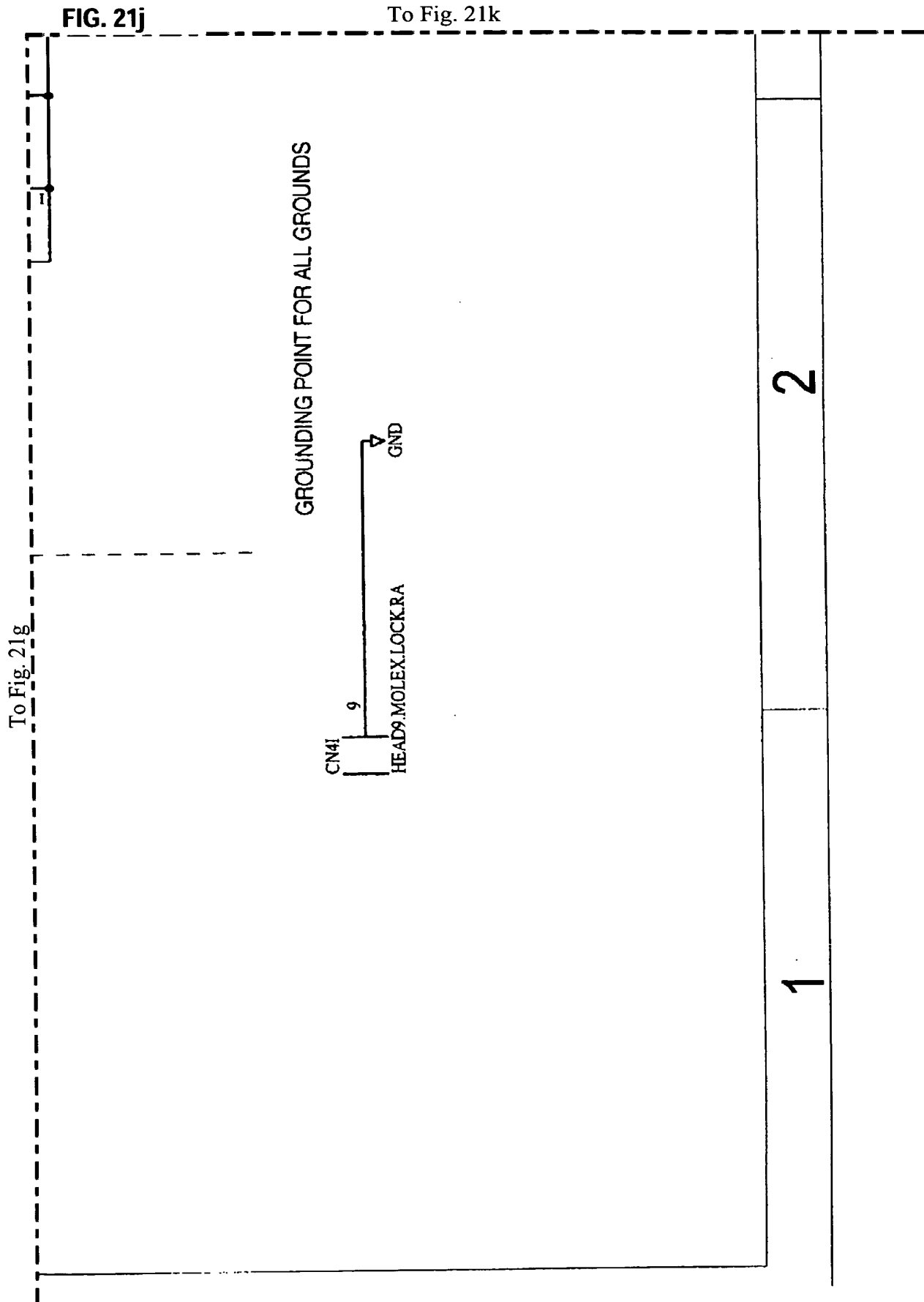


FIG. 21i





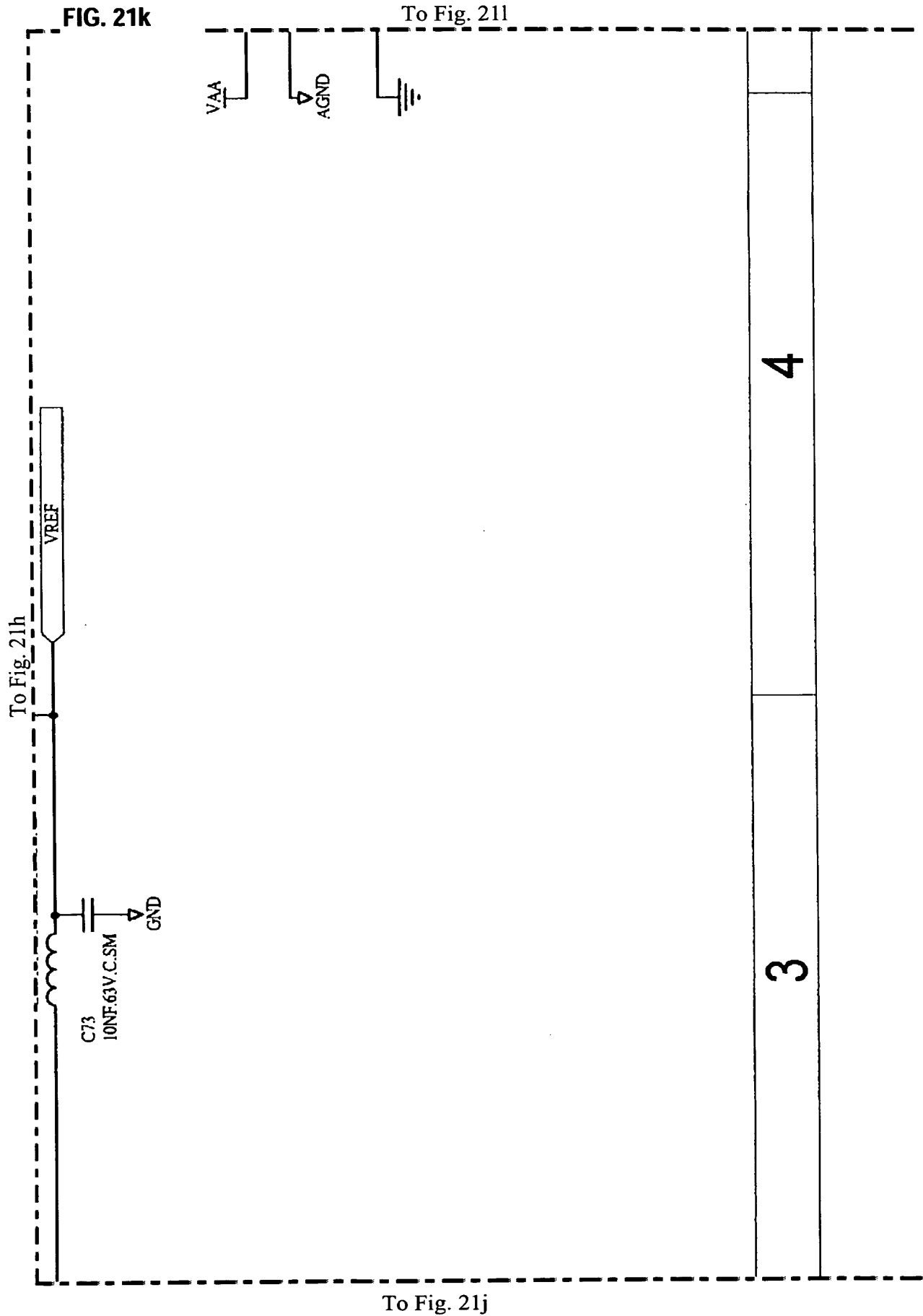


FIG. 21I

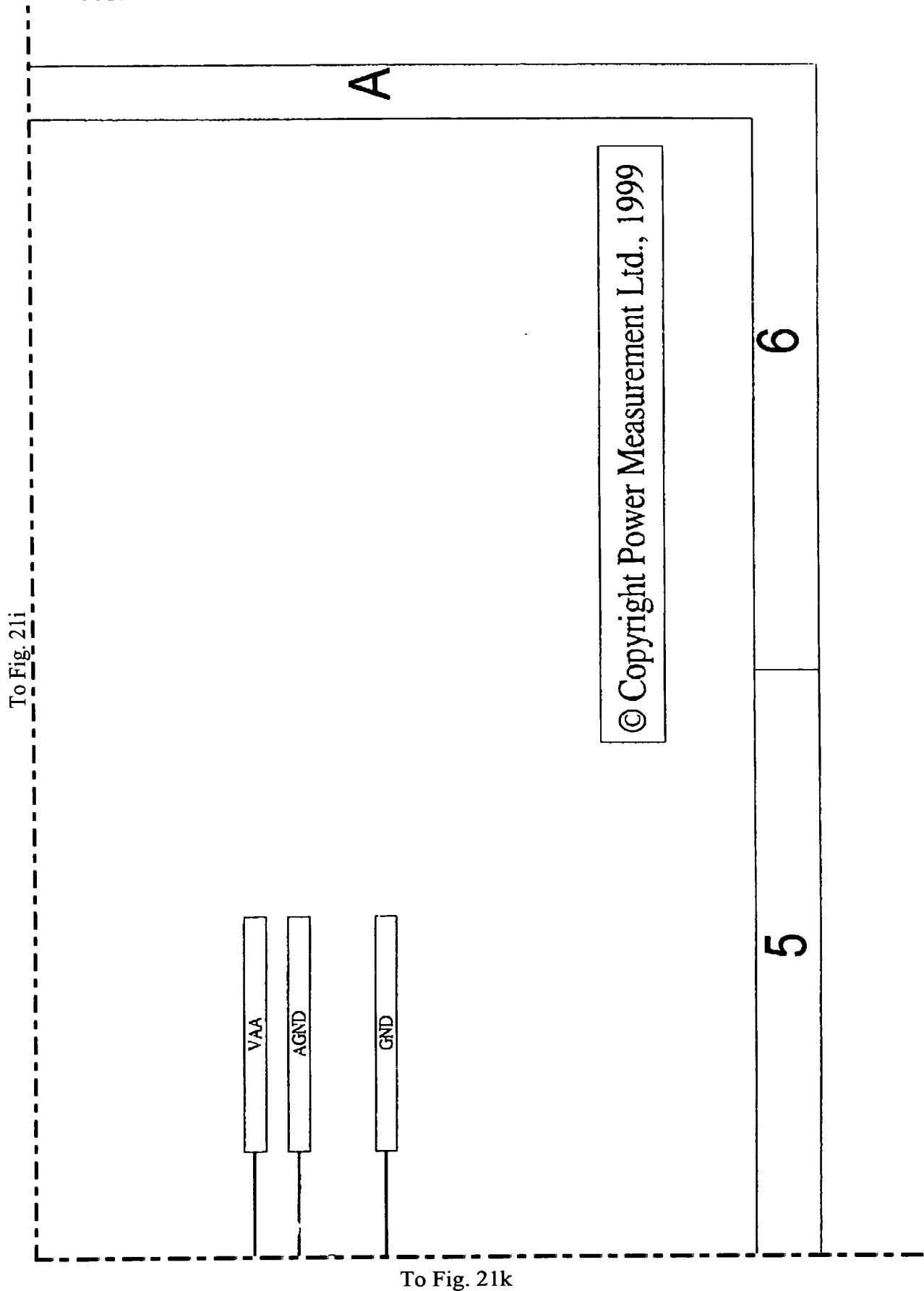
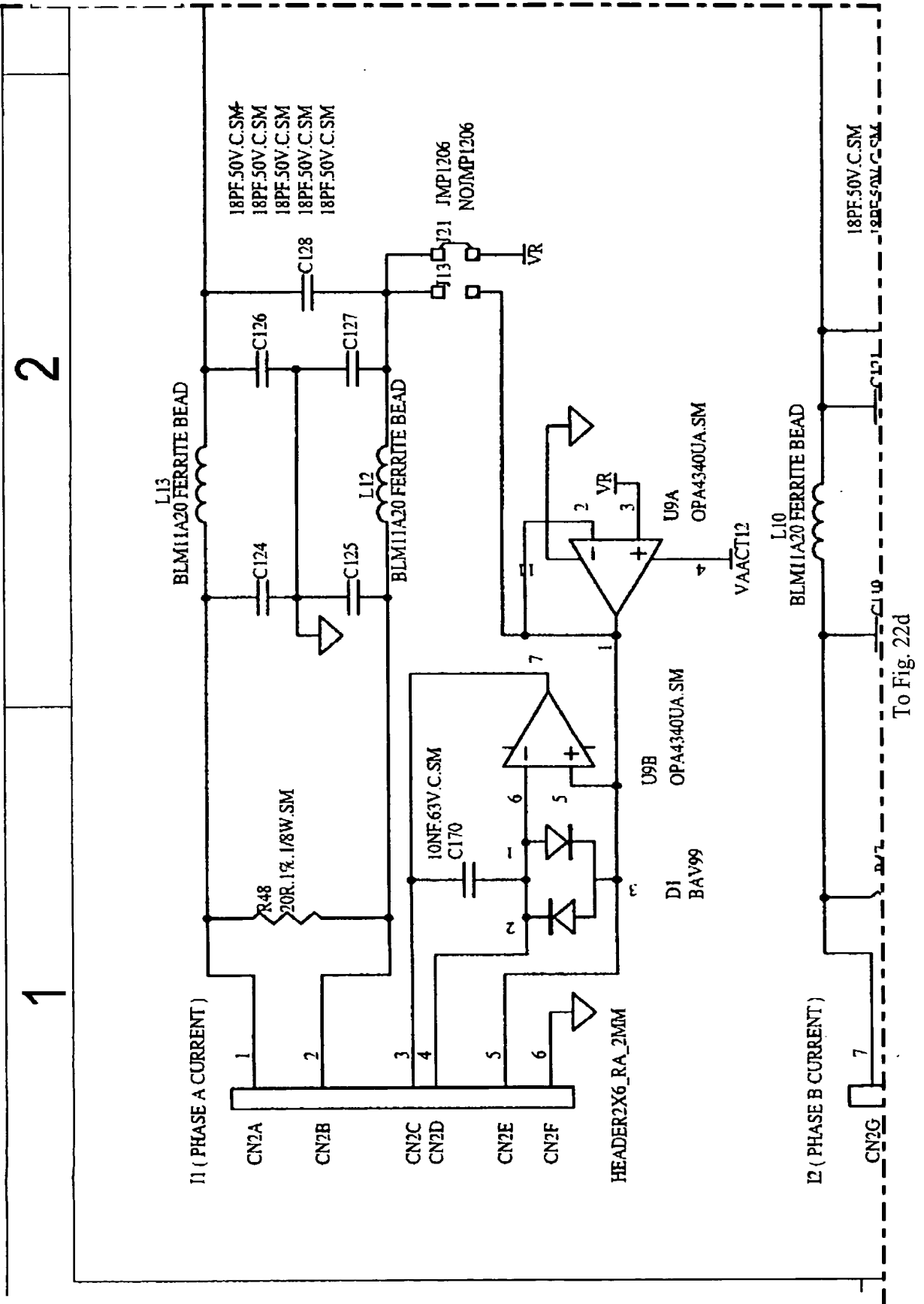


FIG. 22a

To Fig. 22b



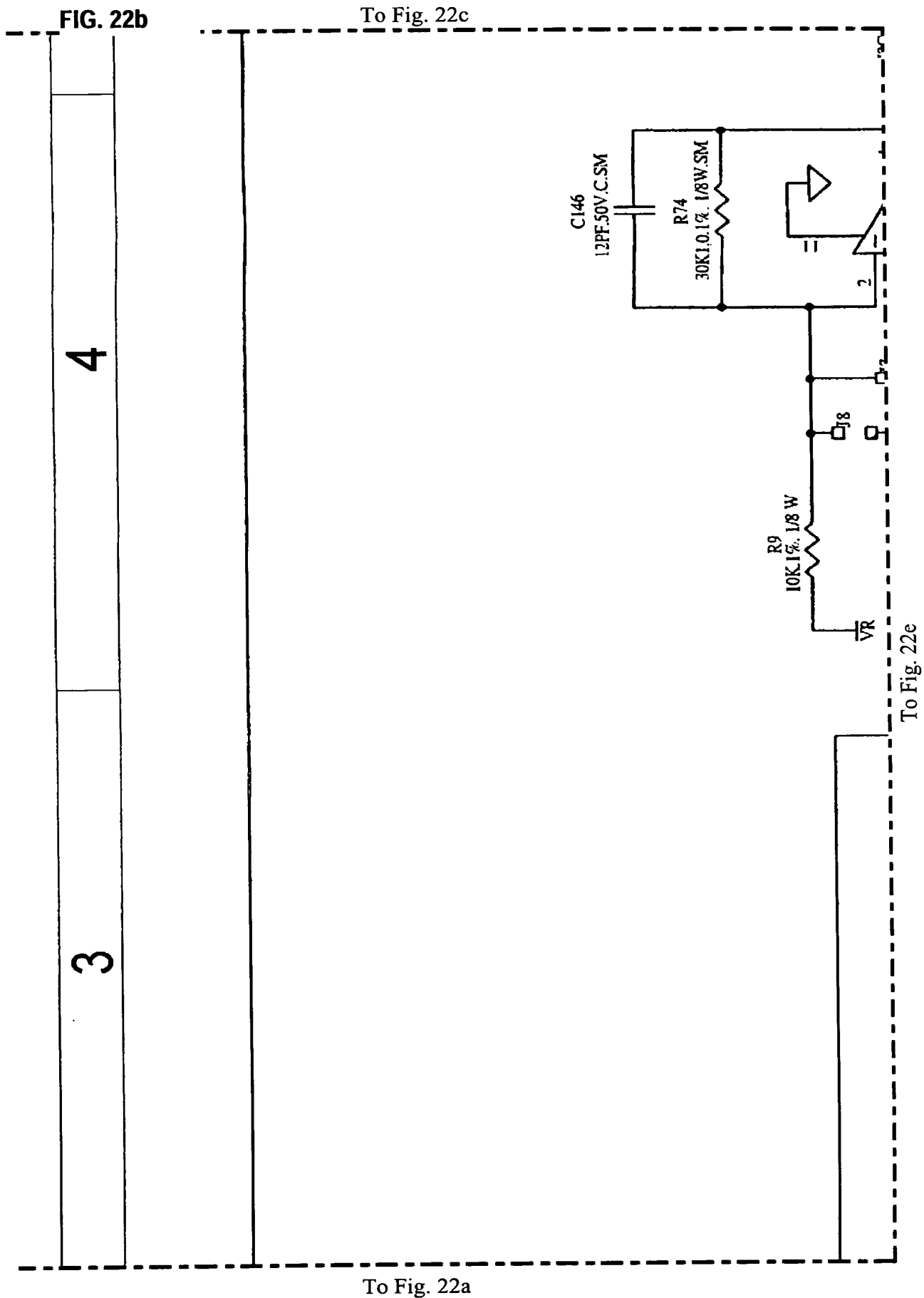
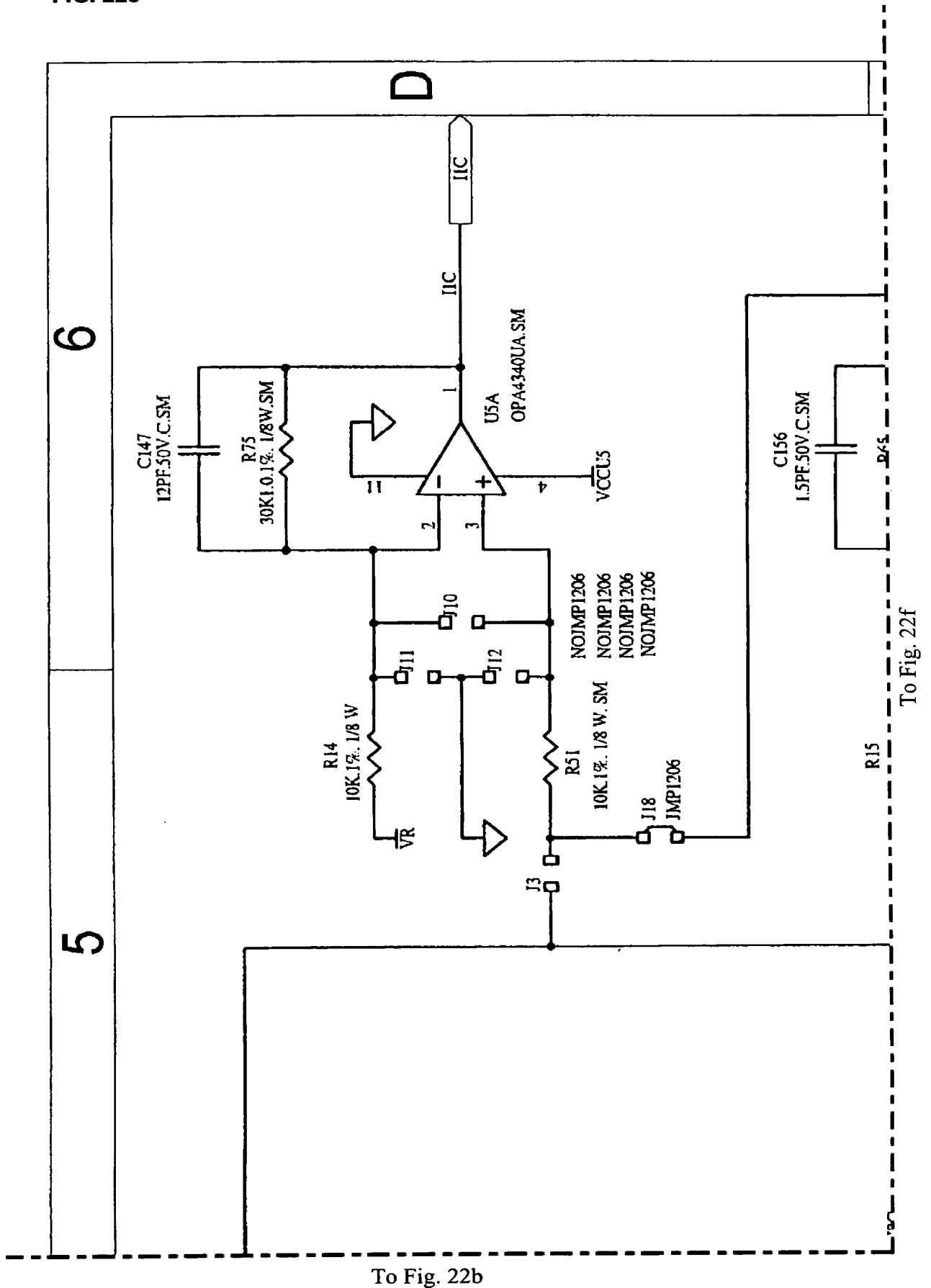
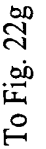


FIG. 22c



To Fig. 22e



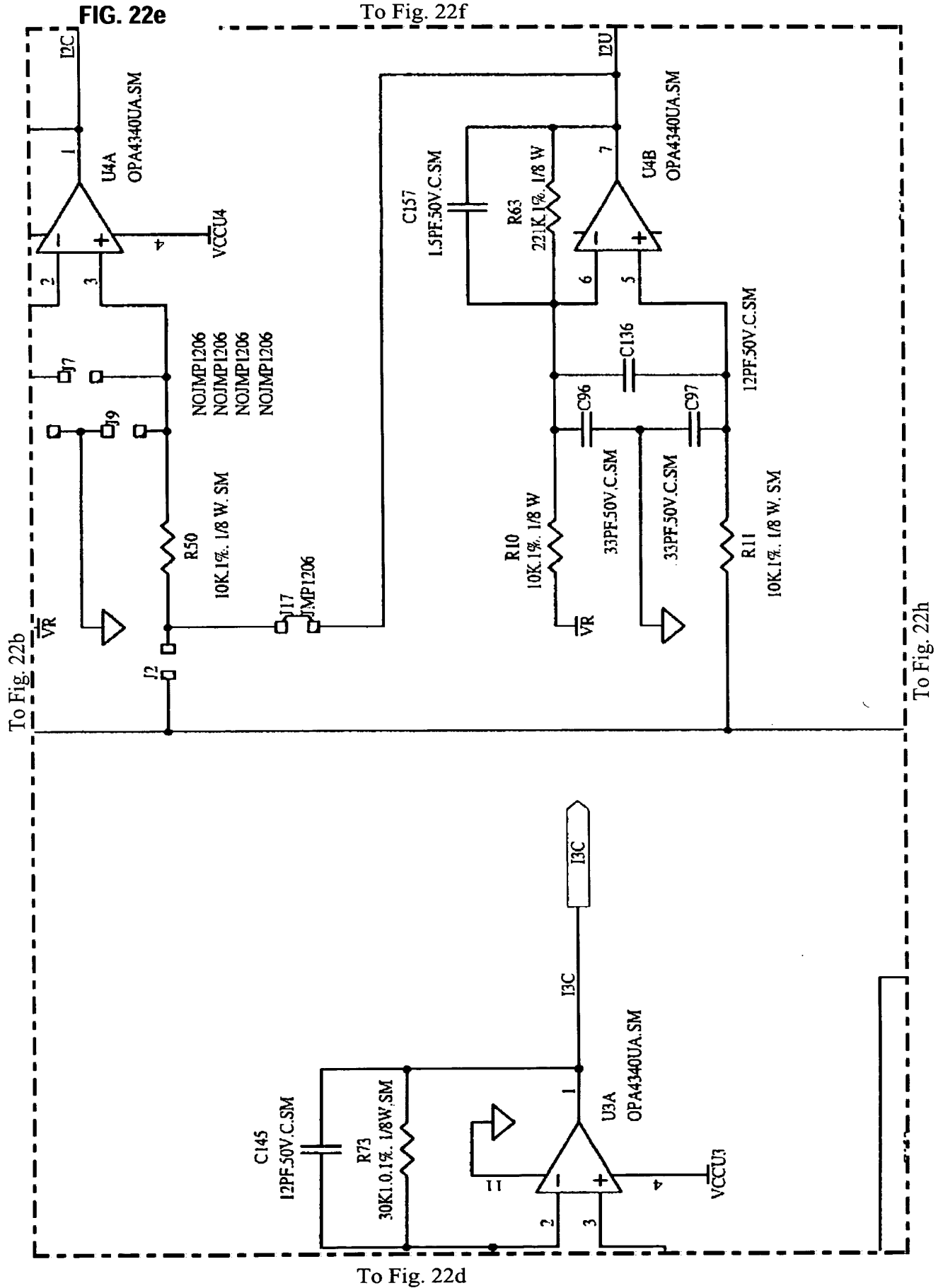
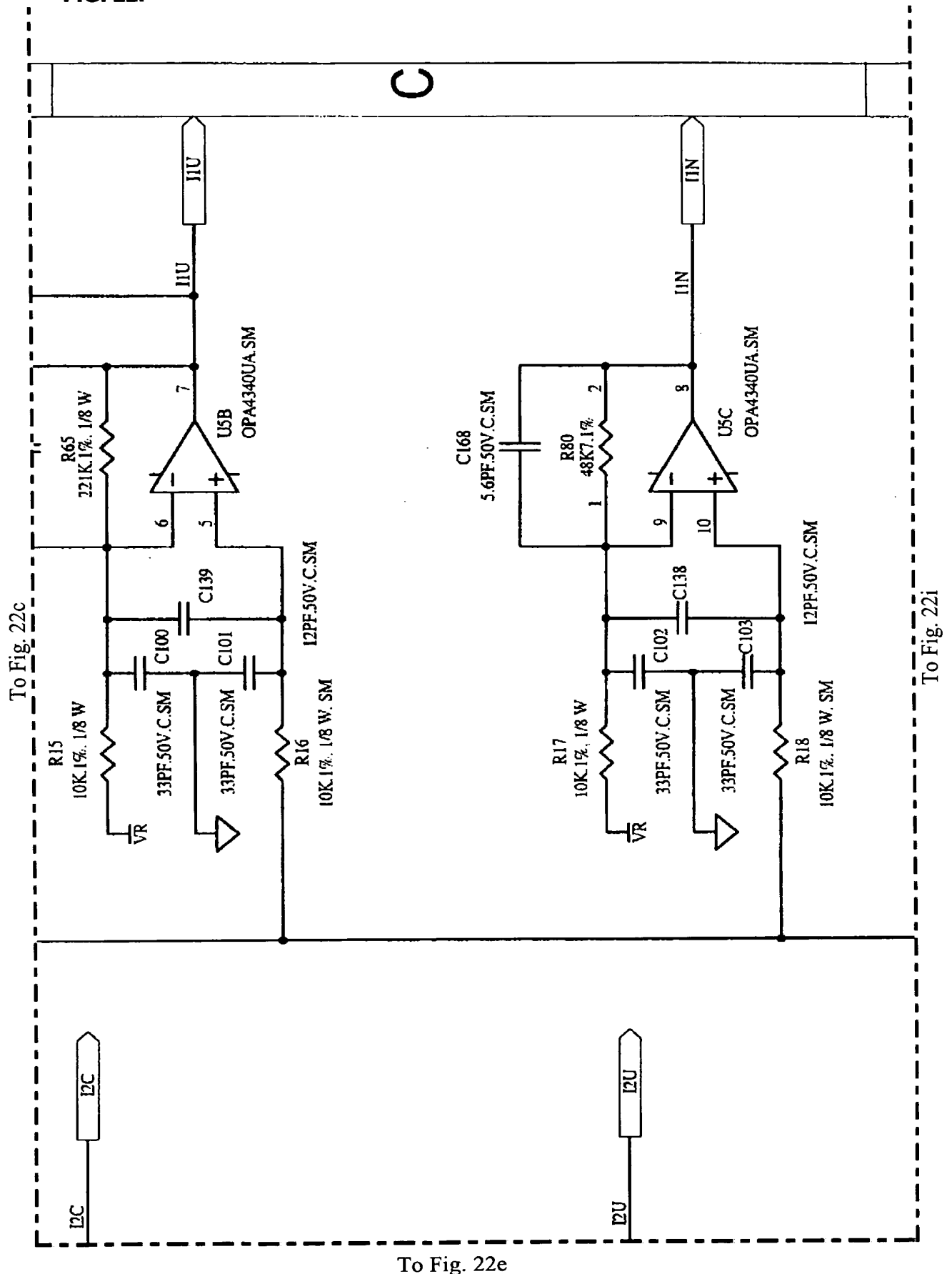
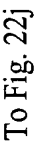


FIG. 22f





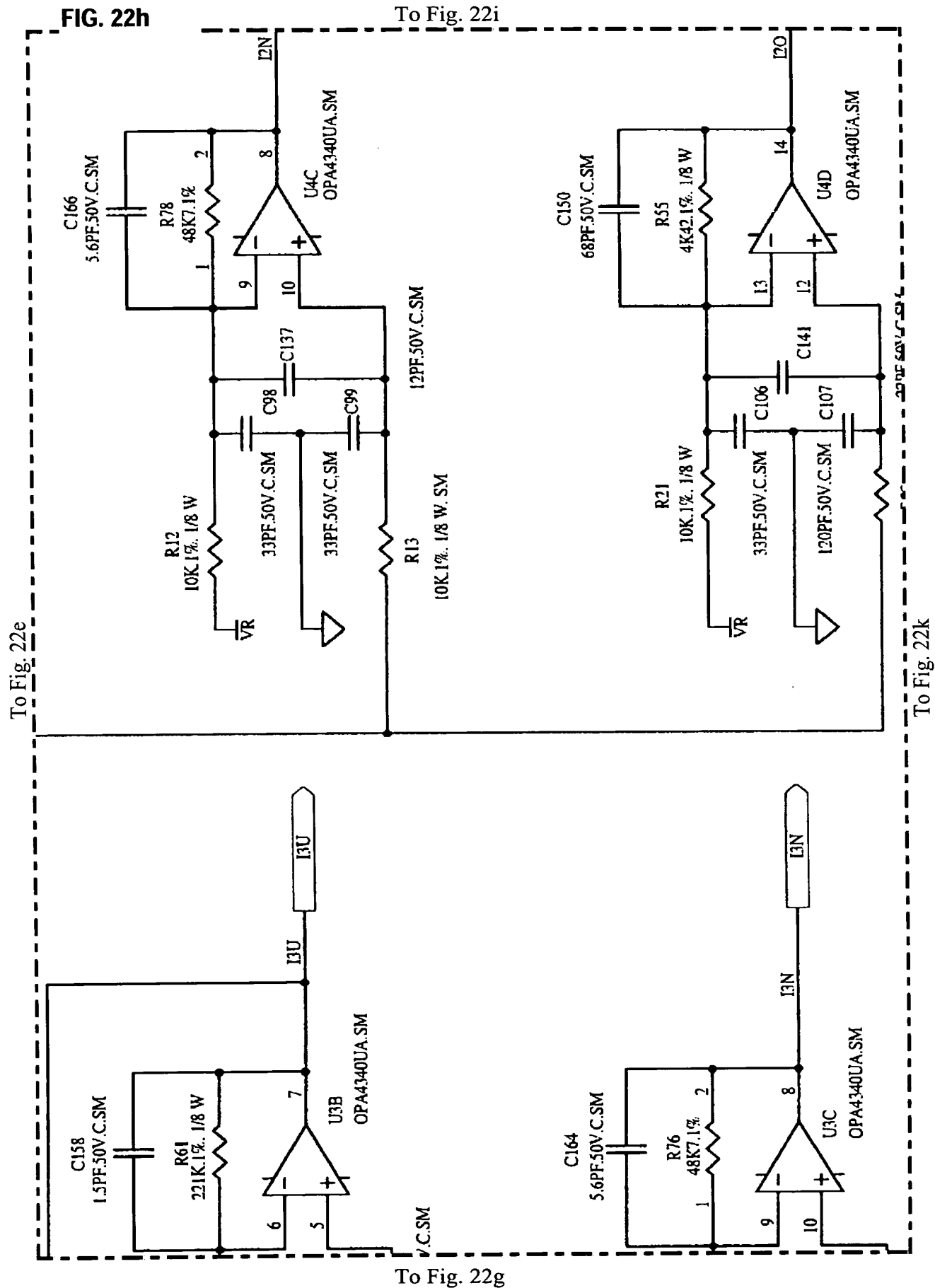
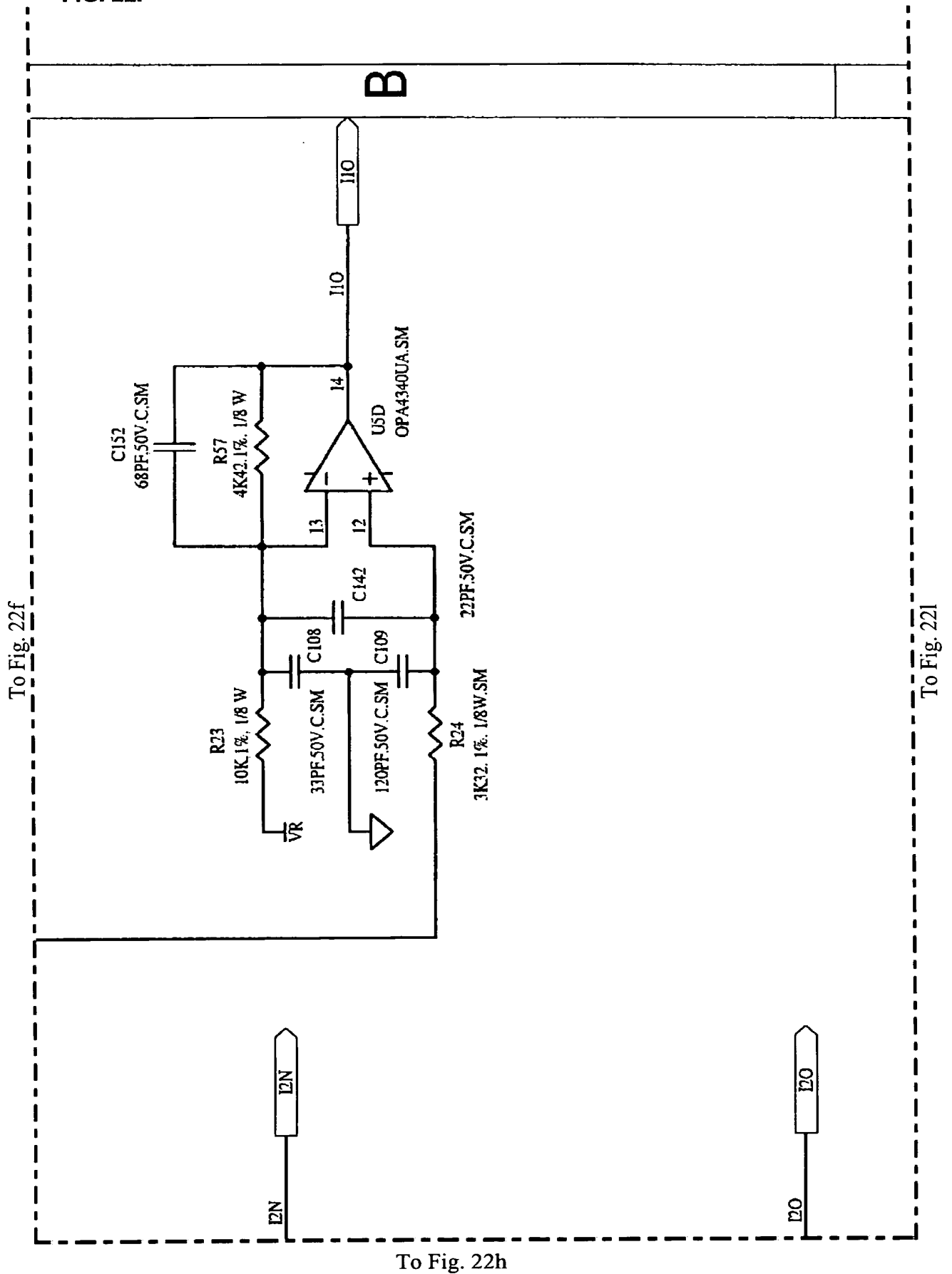
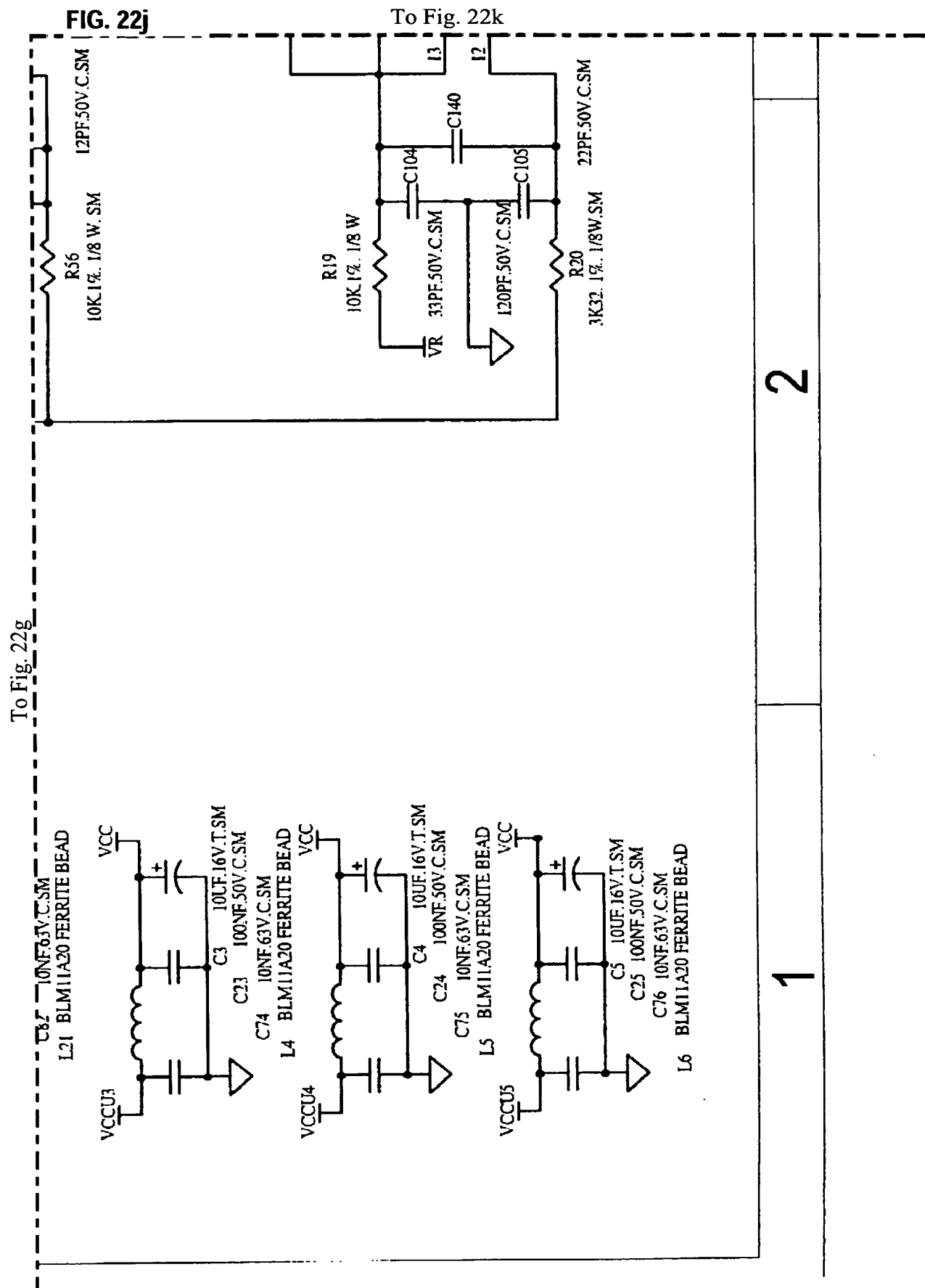


FIG. 22i





To Fig. 221

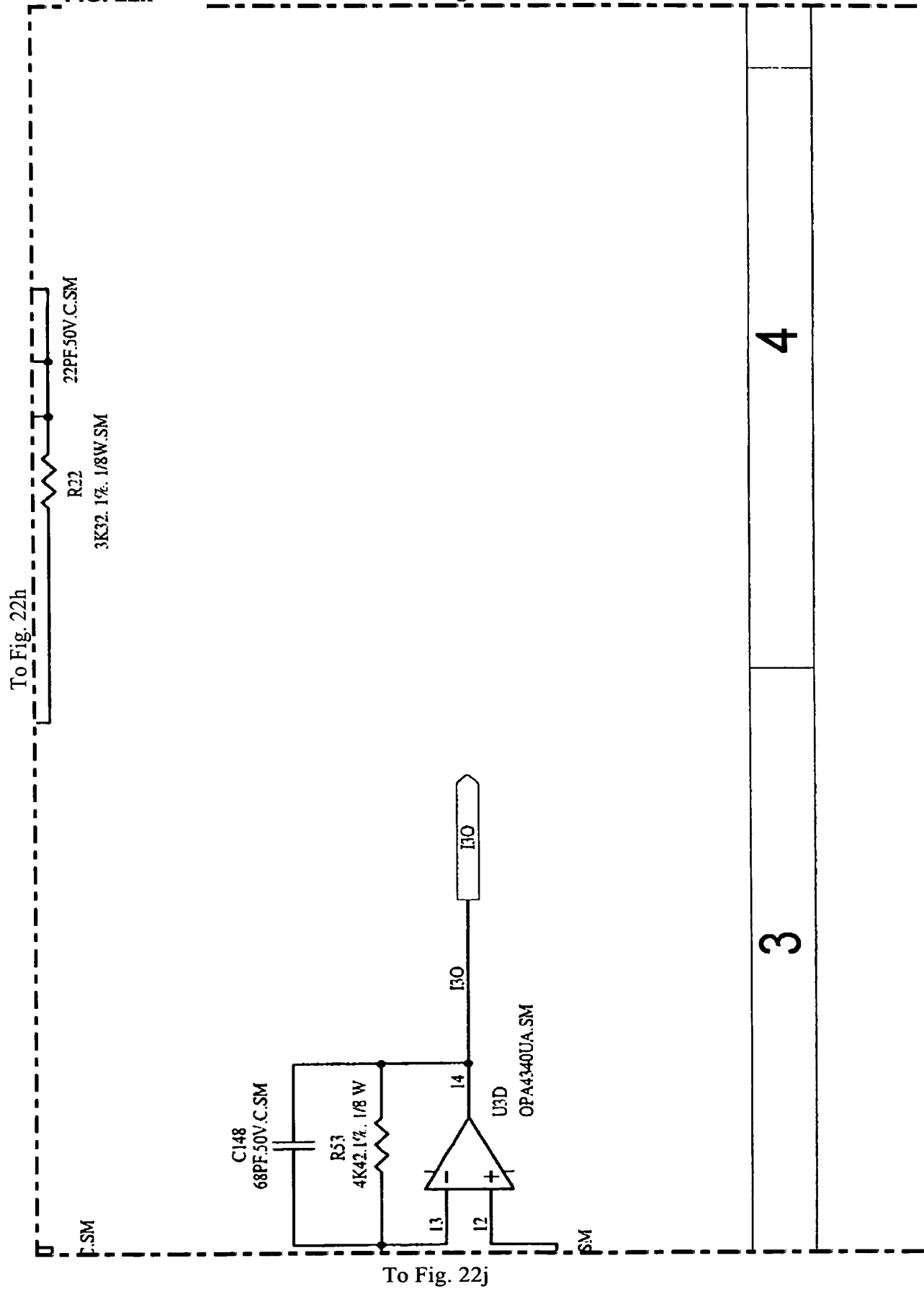
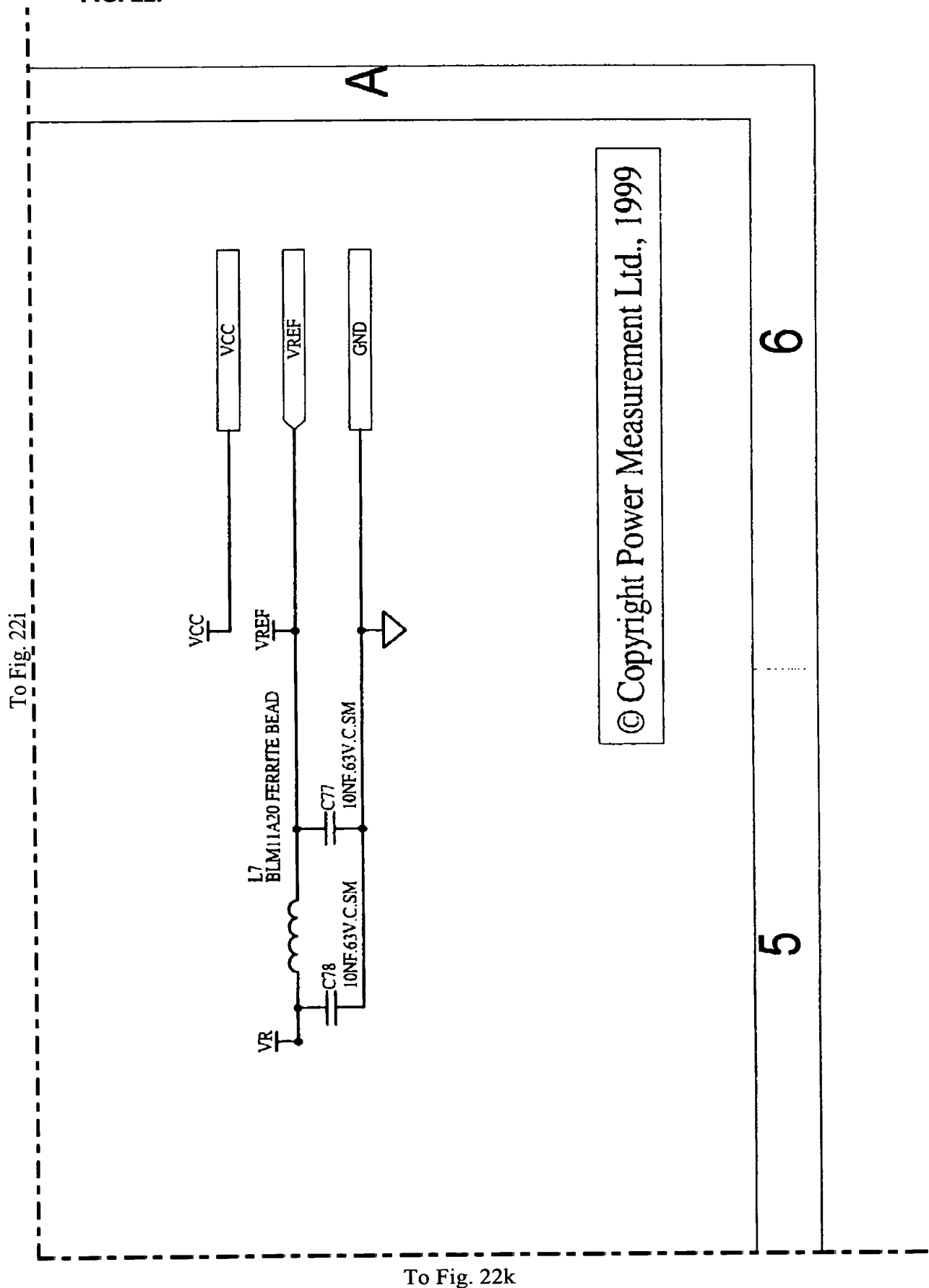
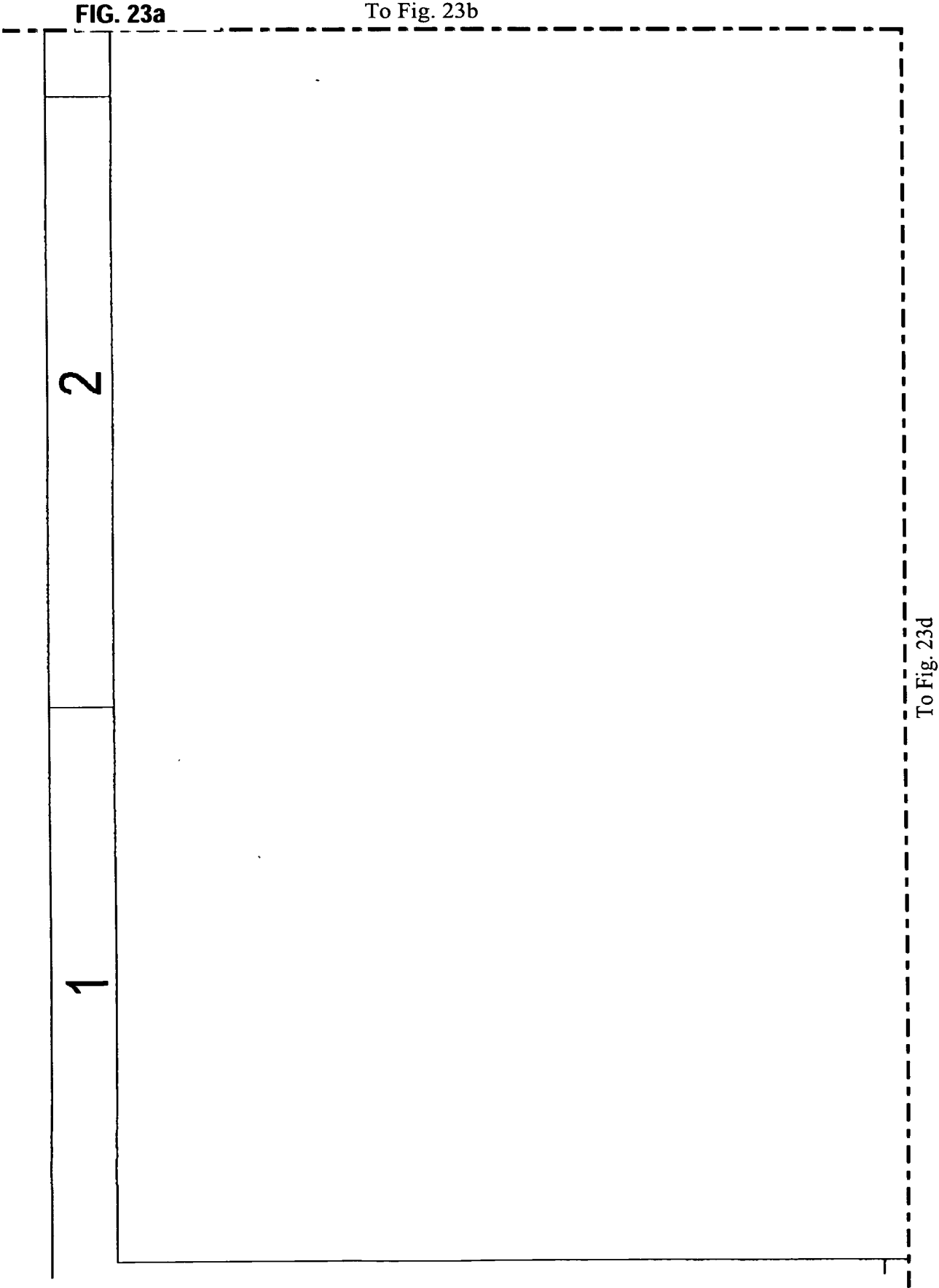


FIG. 22I





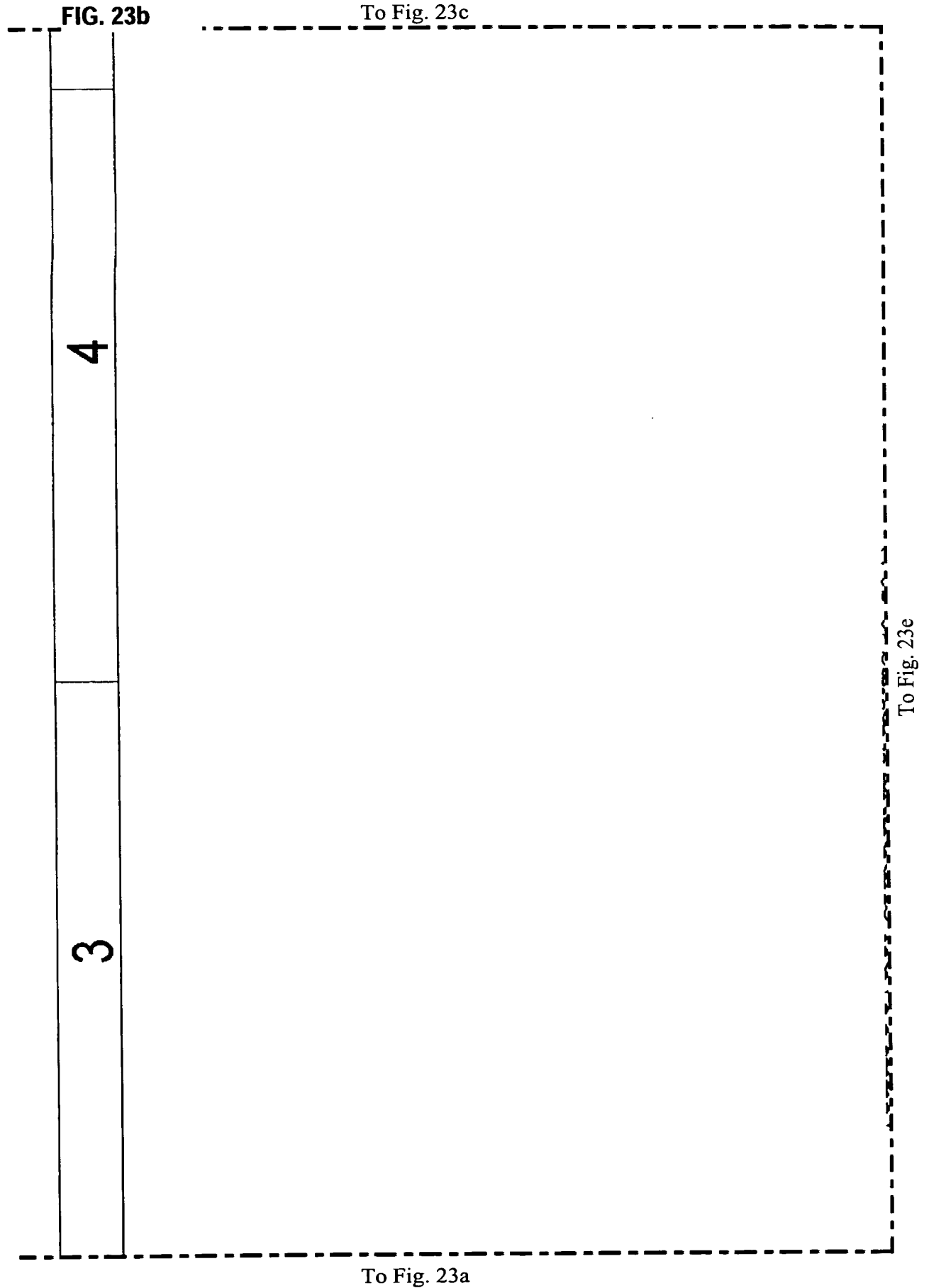
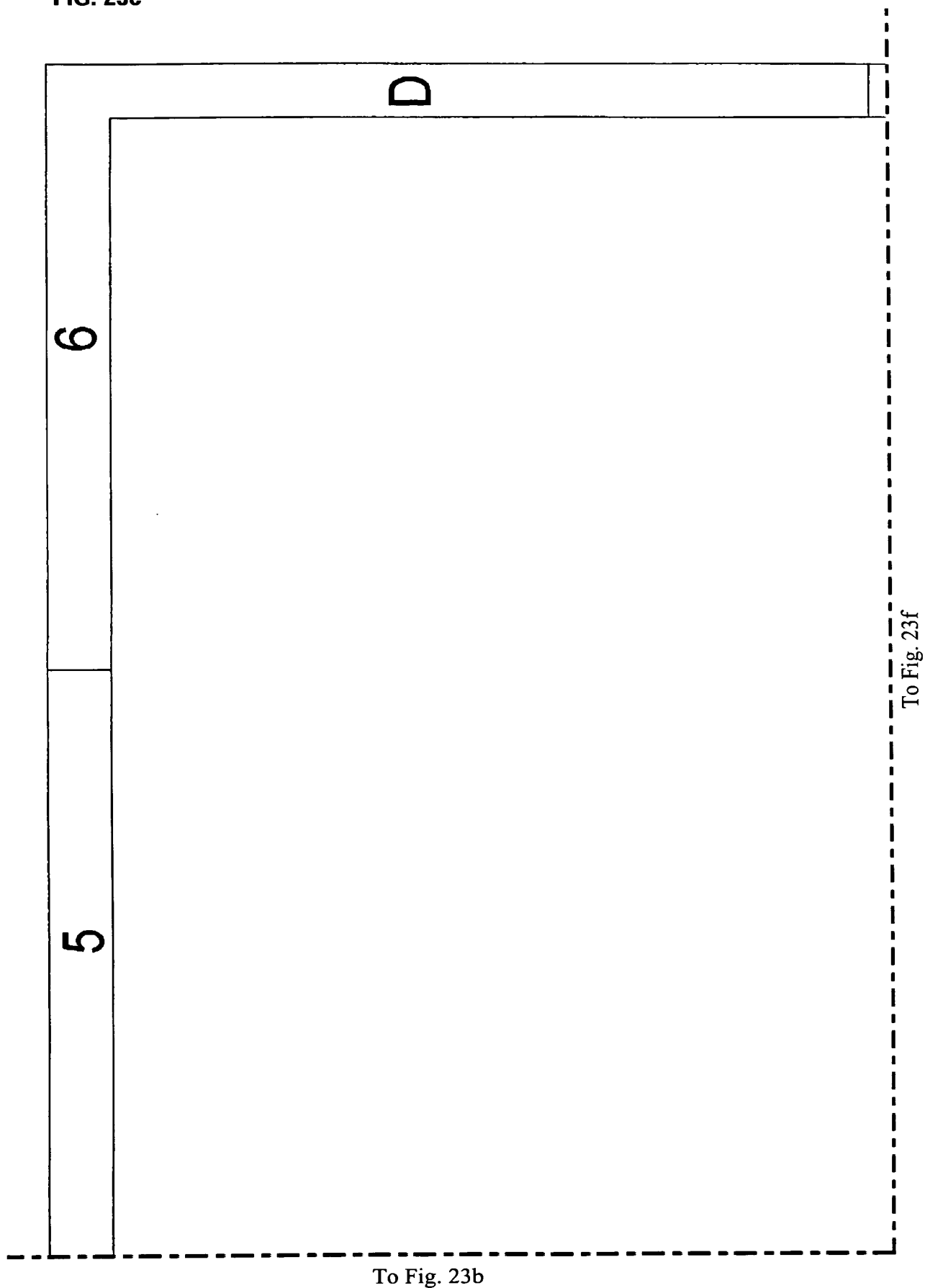
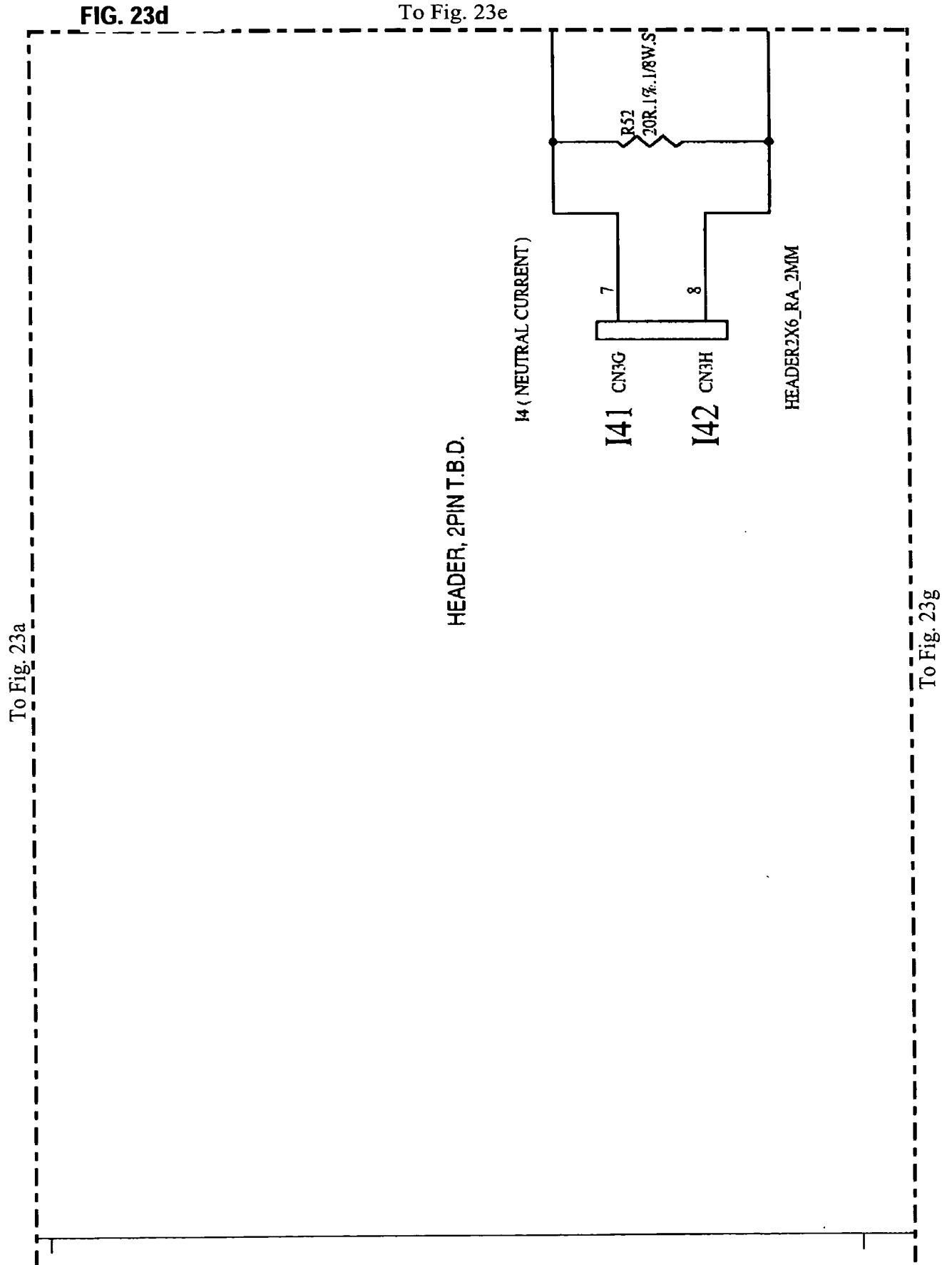


FIG. 23c



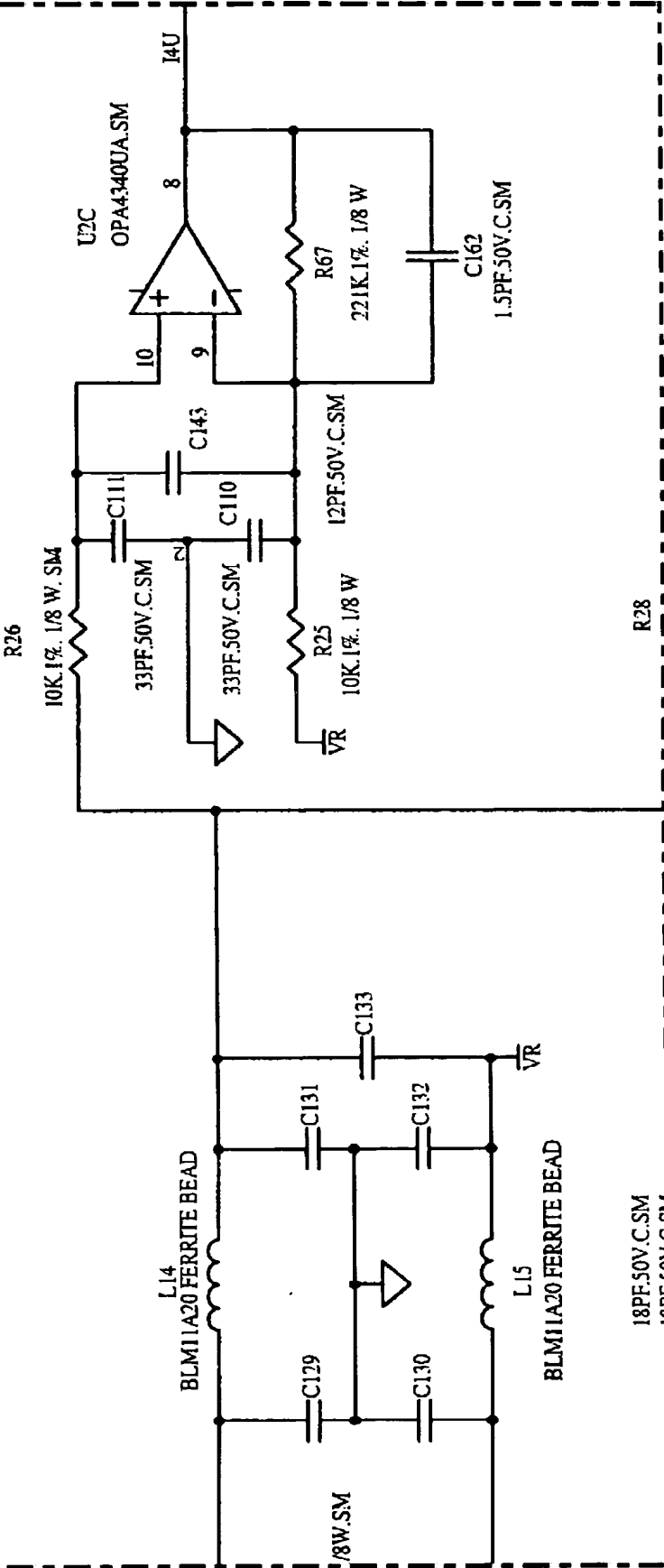


To Fig. 23b

AUXILIARY CURRENT INPUTS (0-50A)

FIG. 23e

To Fig. 23f



To Fig. 23h

To Fig. 23d

FIG. 23f

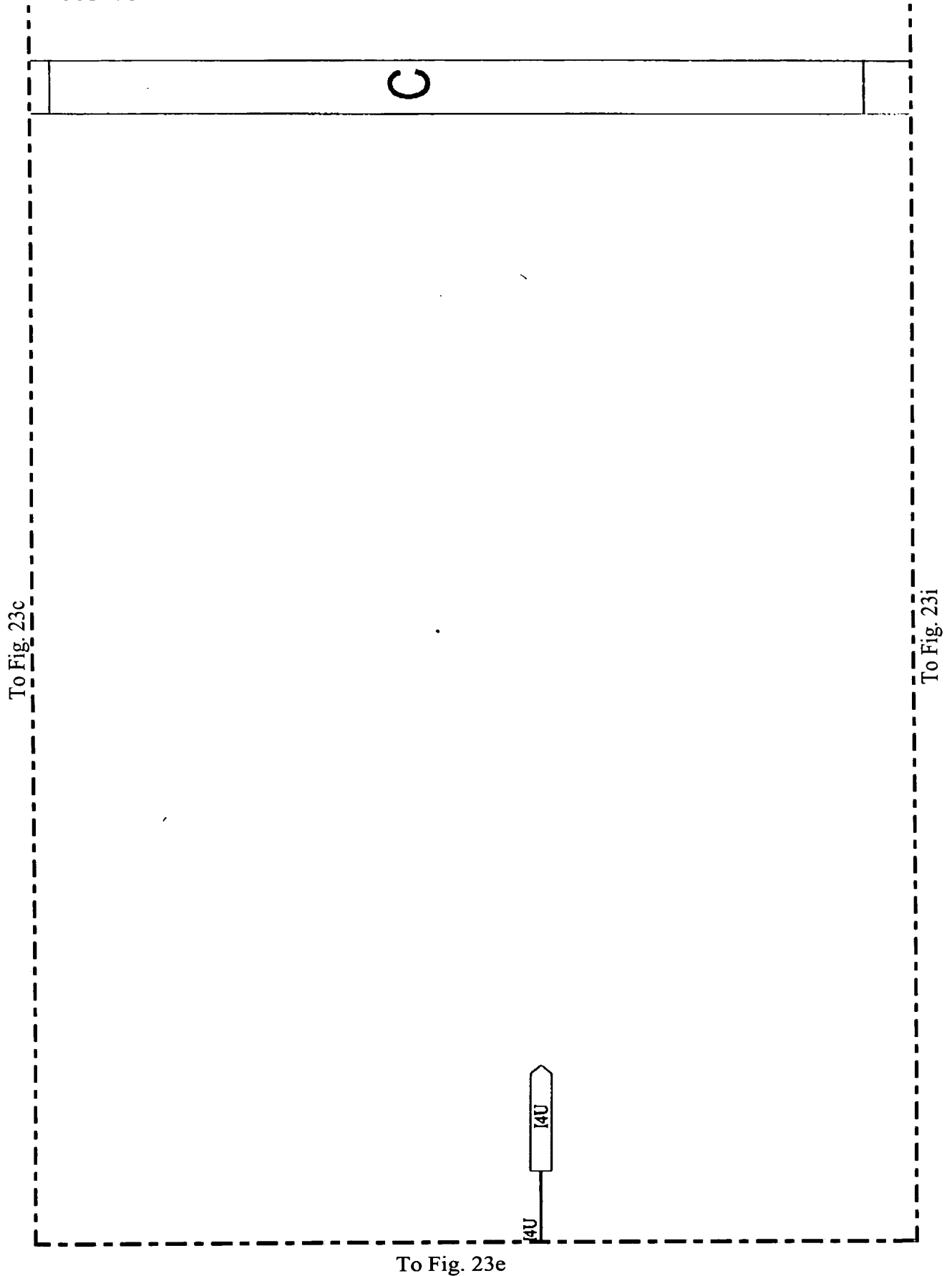


FIG. 23g

To Fig. 23h

To Fig. 23d

To Fig. 23j

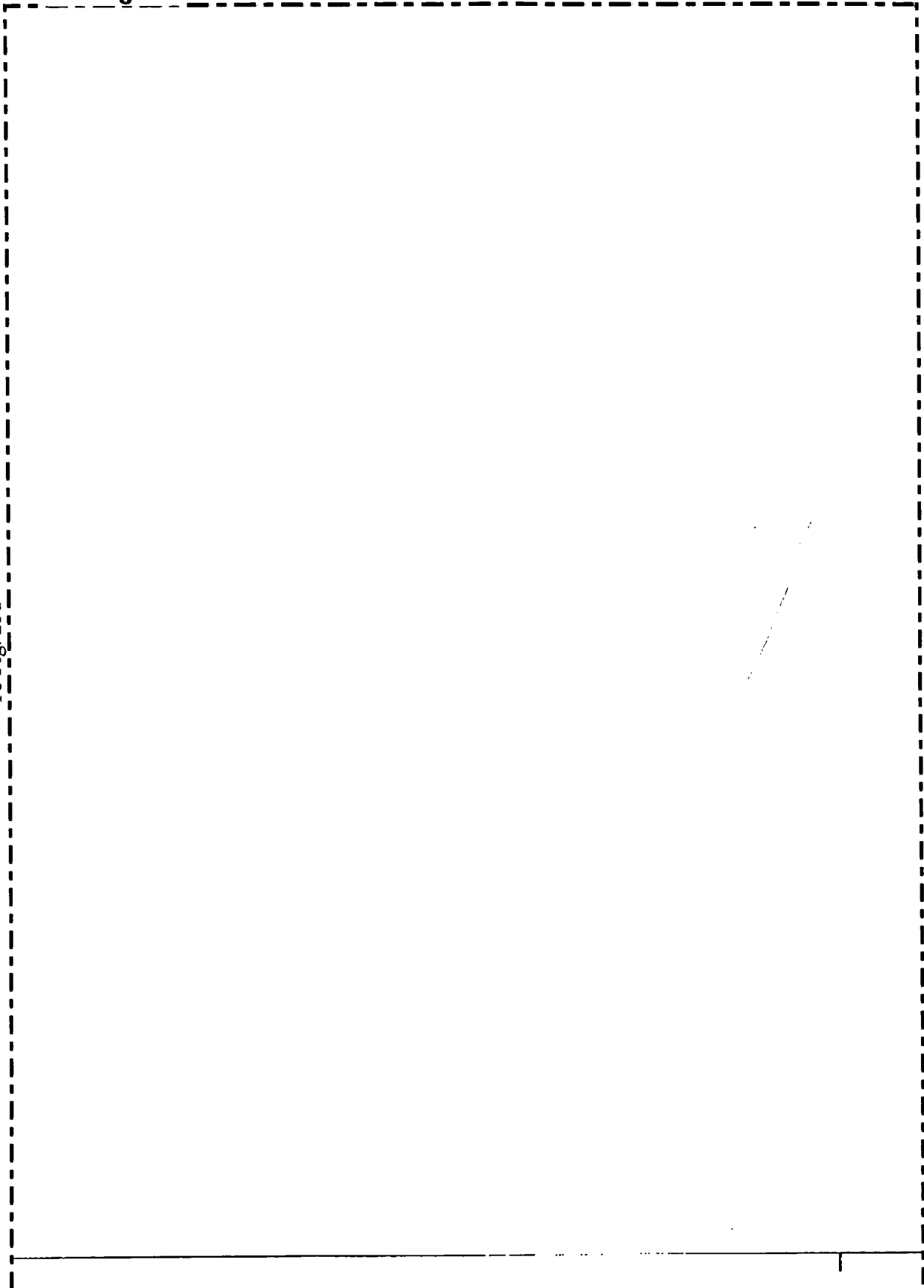
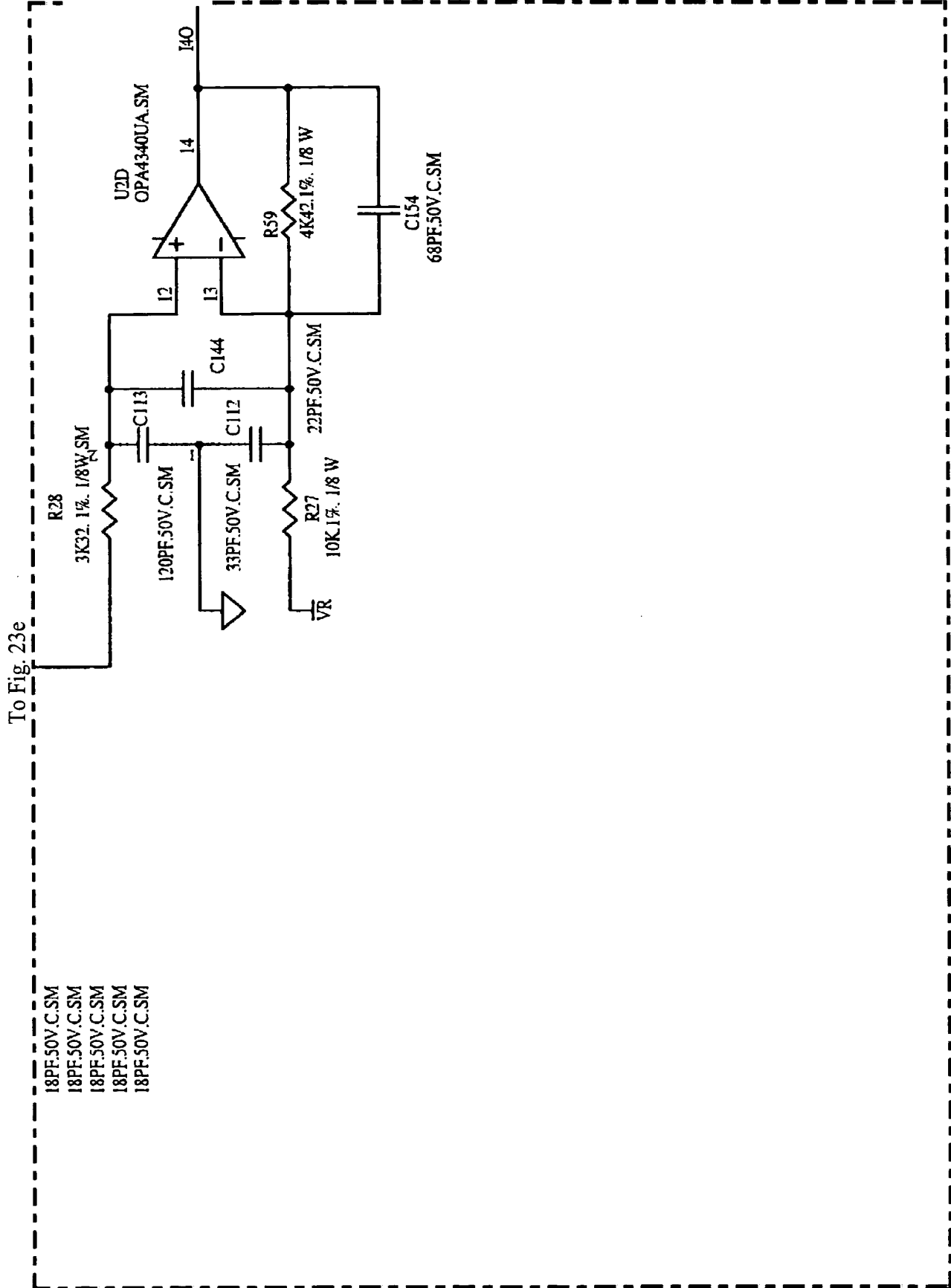


FIG. 23h

To Fig. 23i



To Fig. 23g

To Fig. 23k

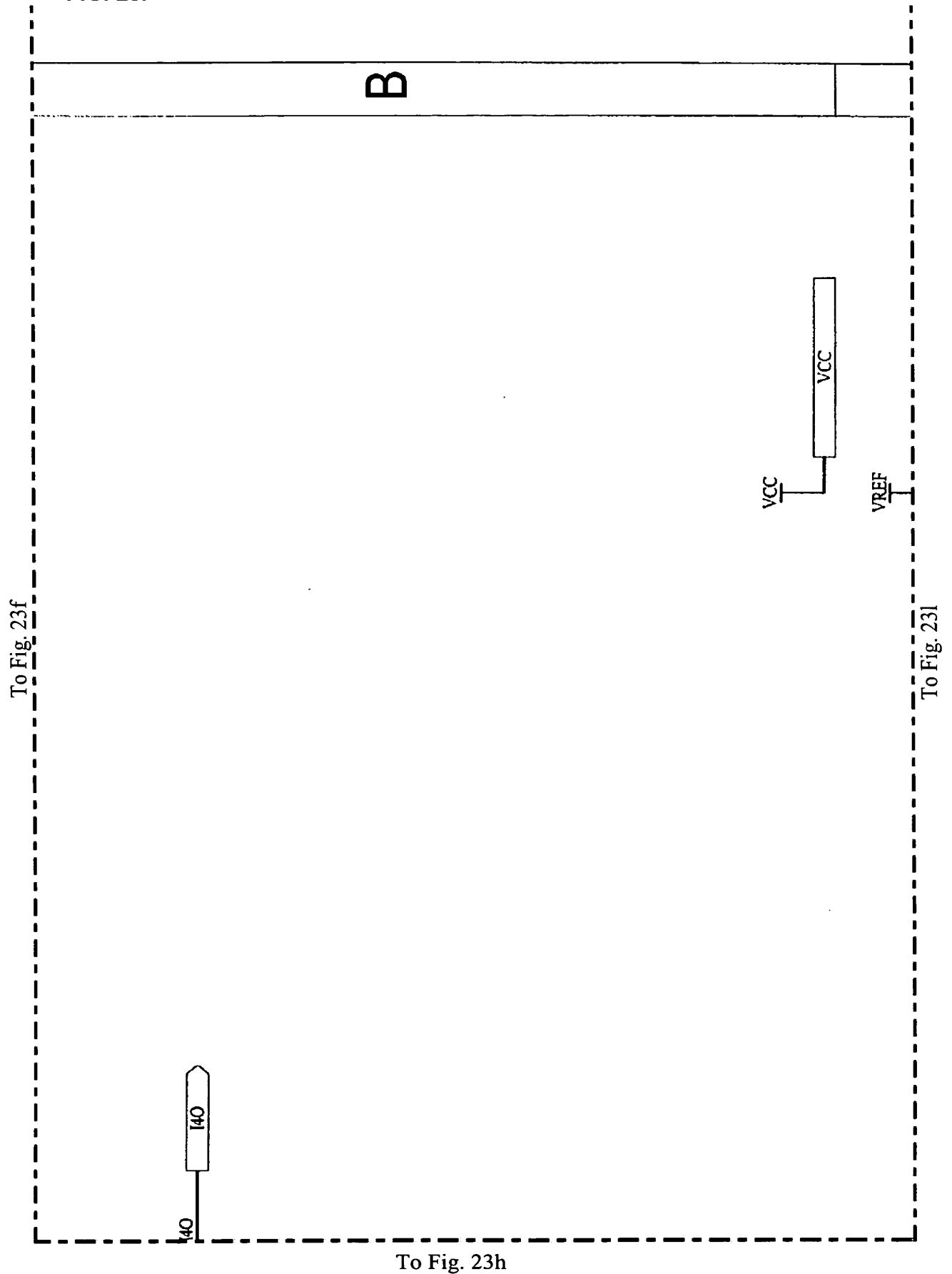


FIG. 23j

To Fig. 23k

To Fig. 23g

2

1

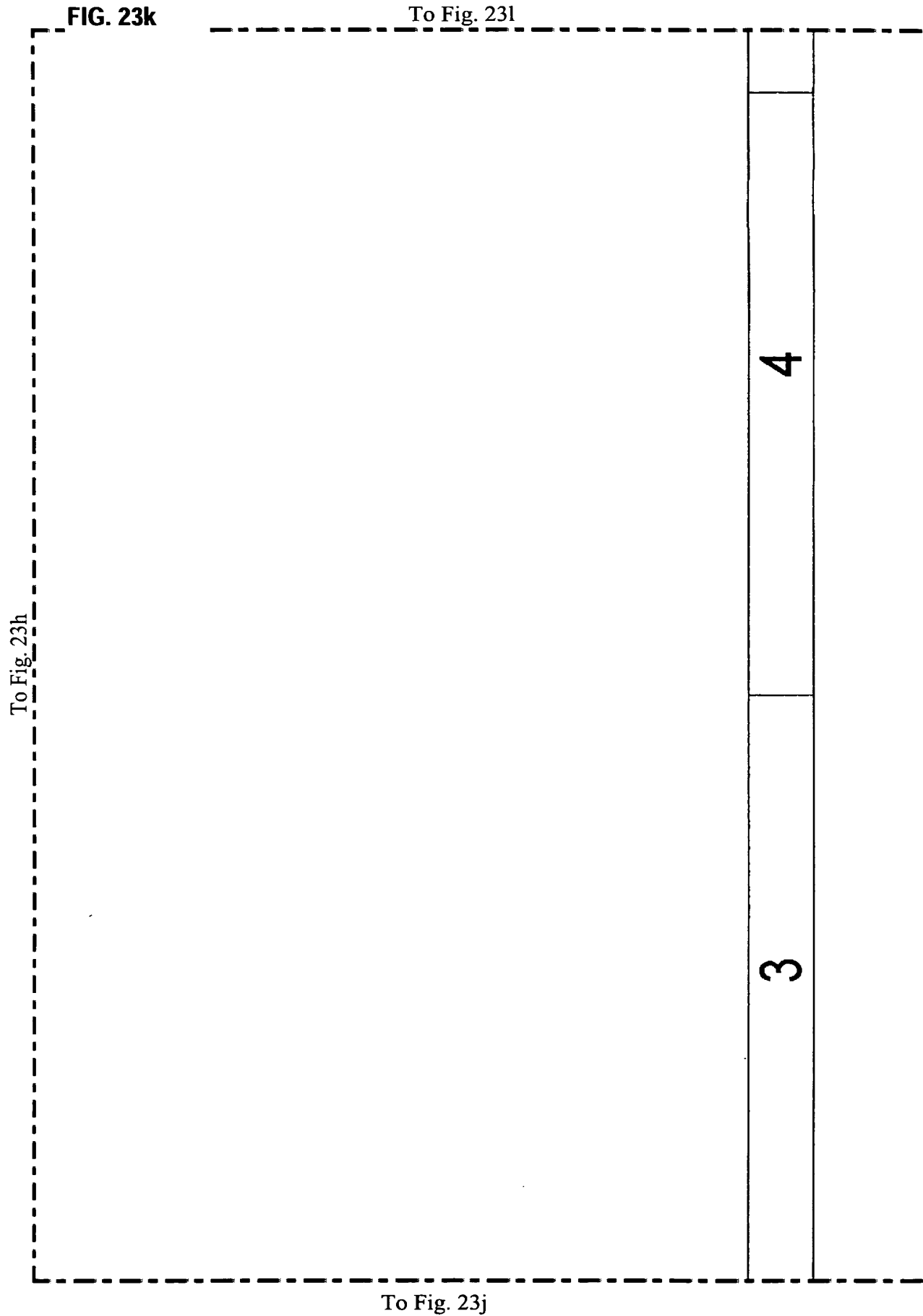


FIG. 23l

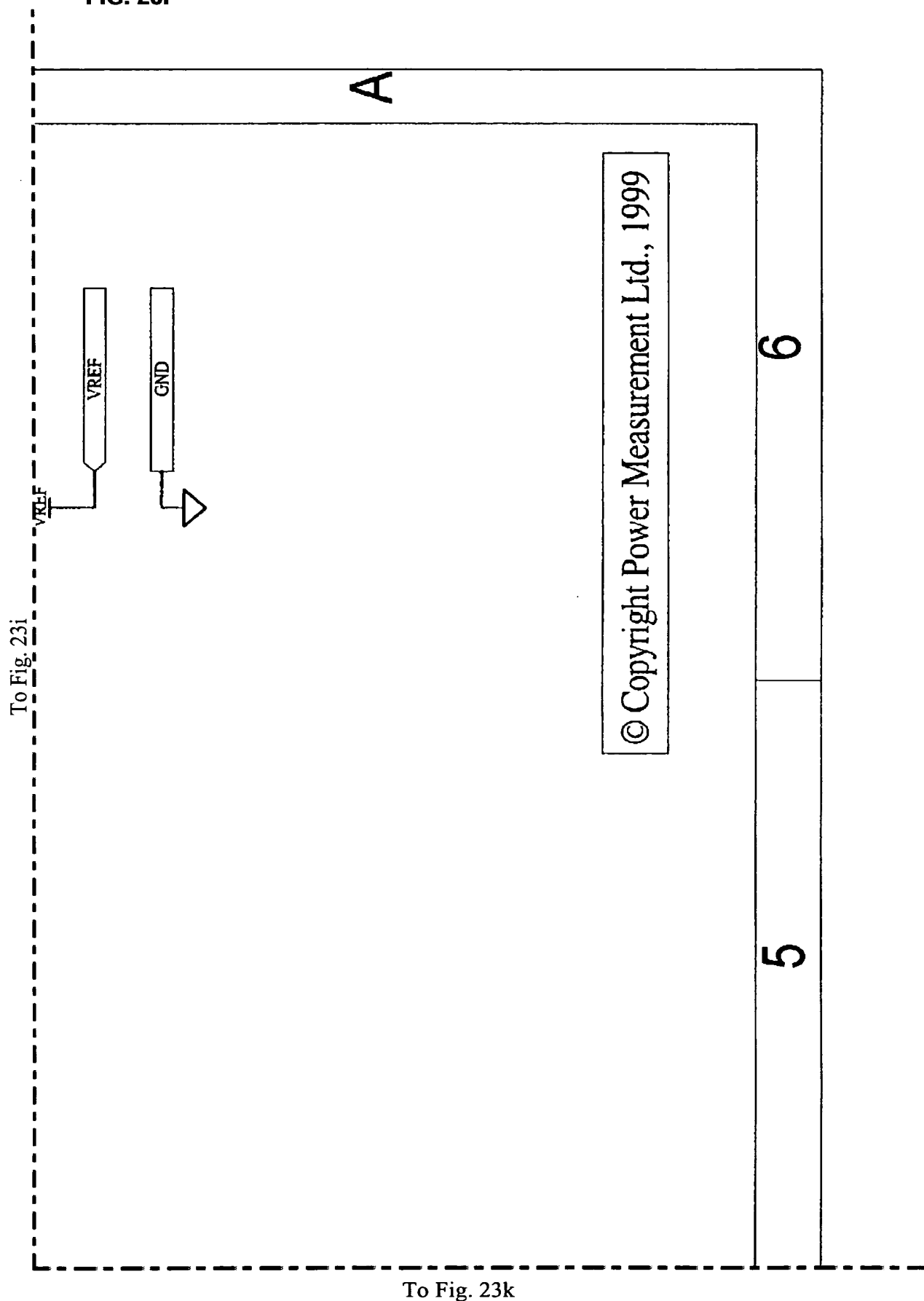
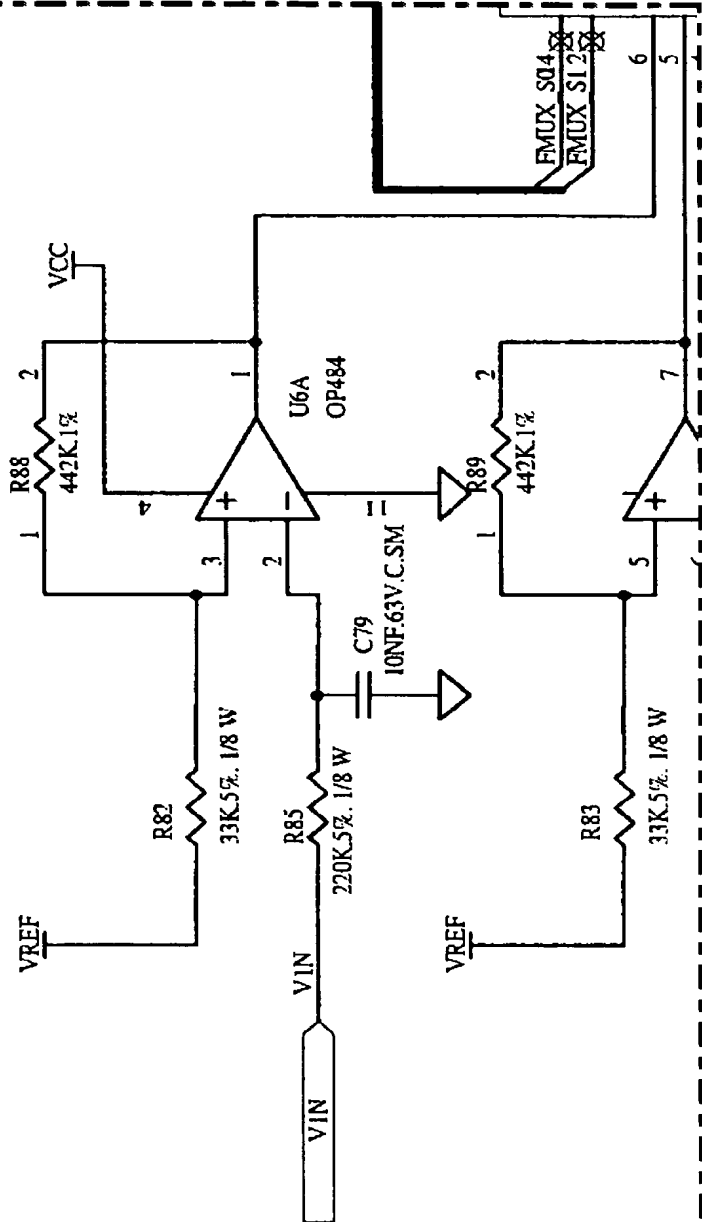


FIG. 24a

To Fig. 24b

2

LINE FREQUENCY DETECTOR



To Fig. 24d

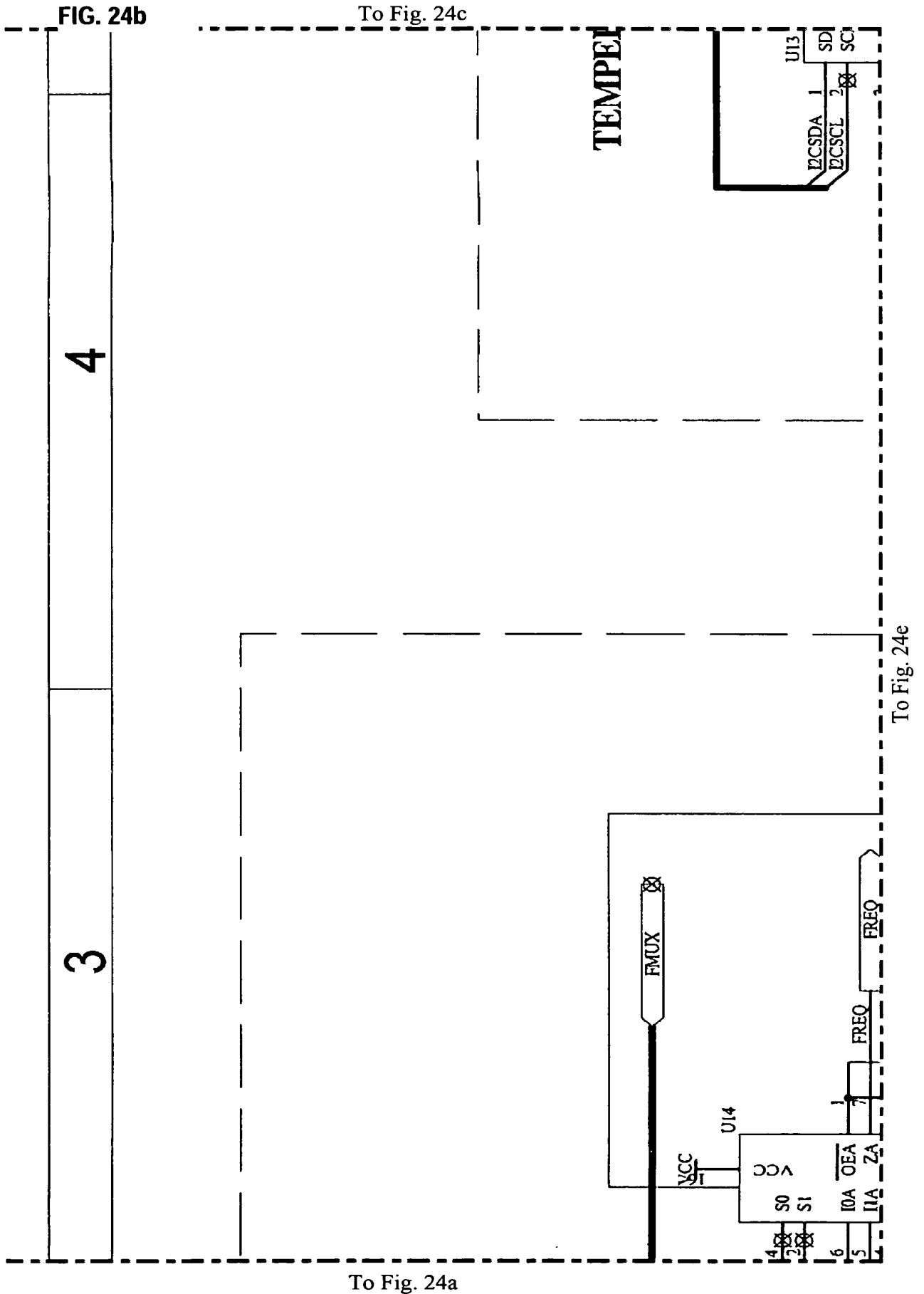
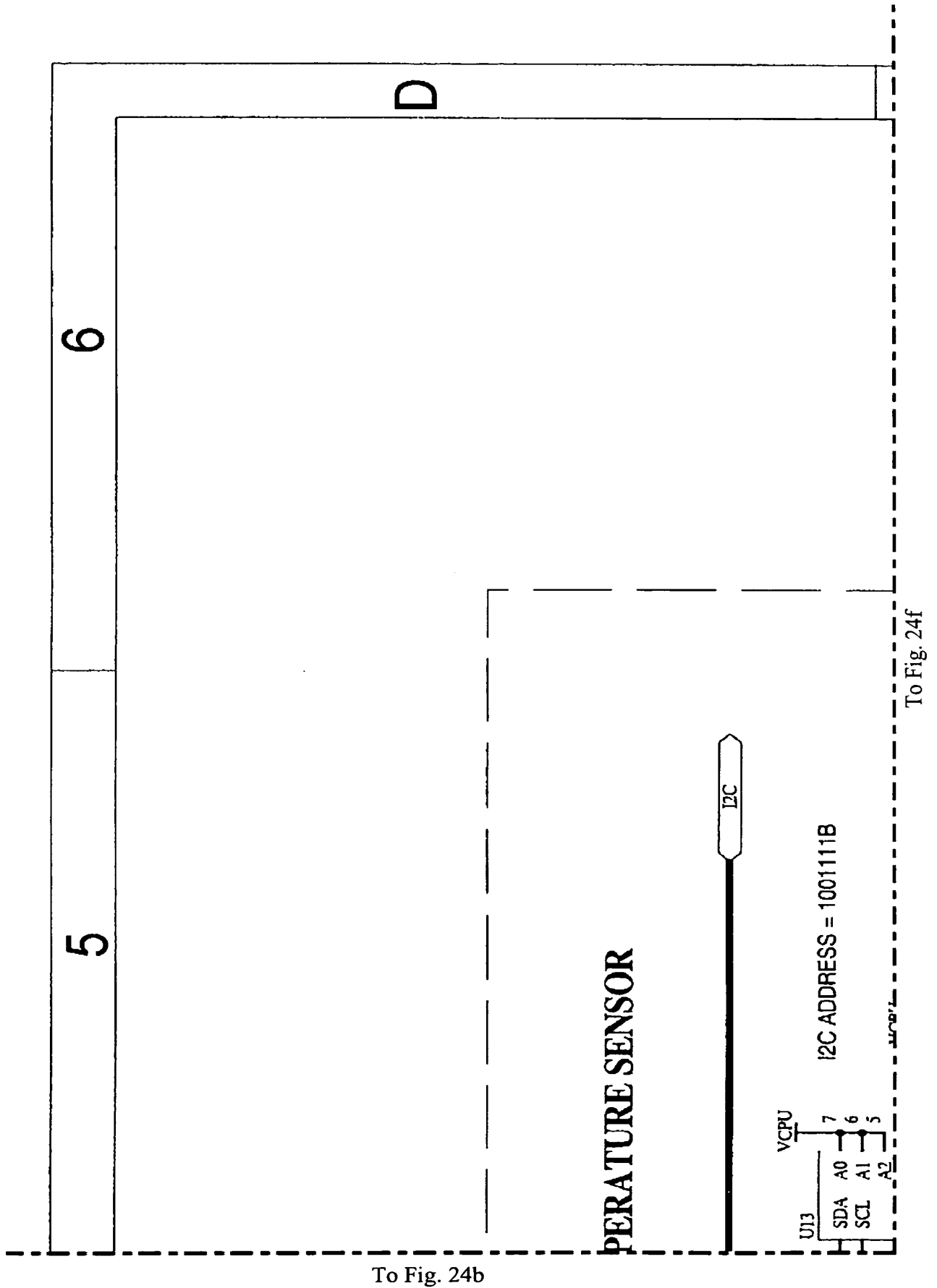
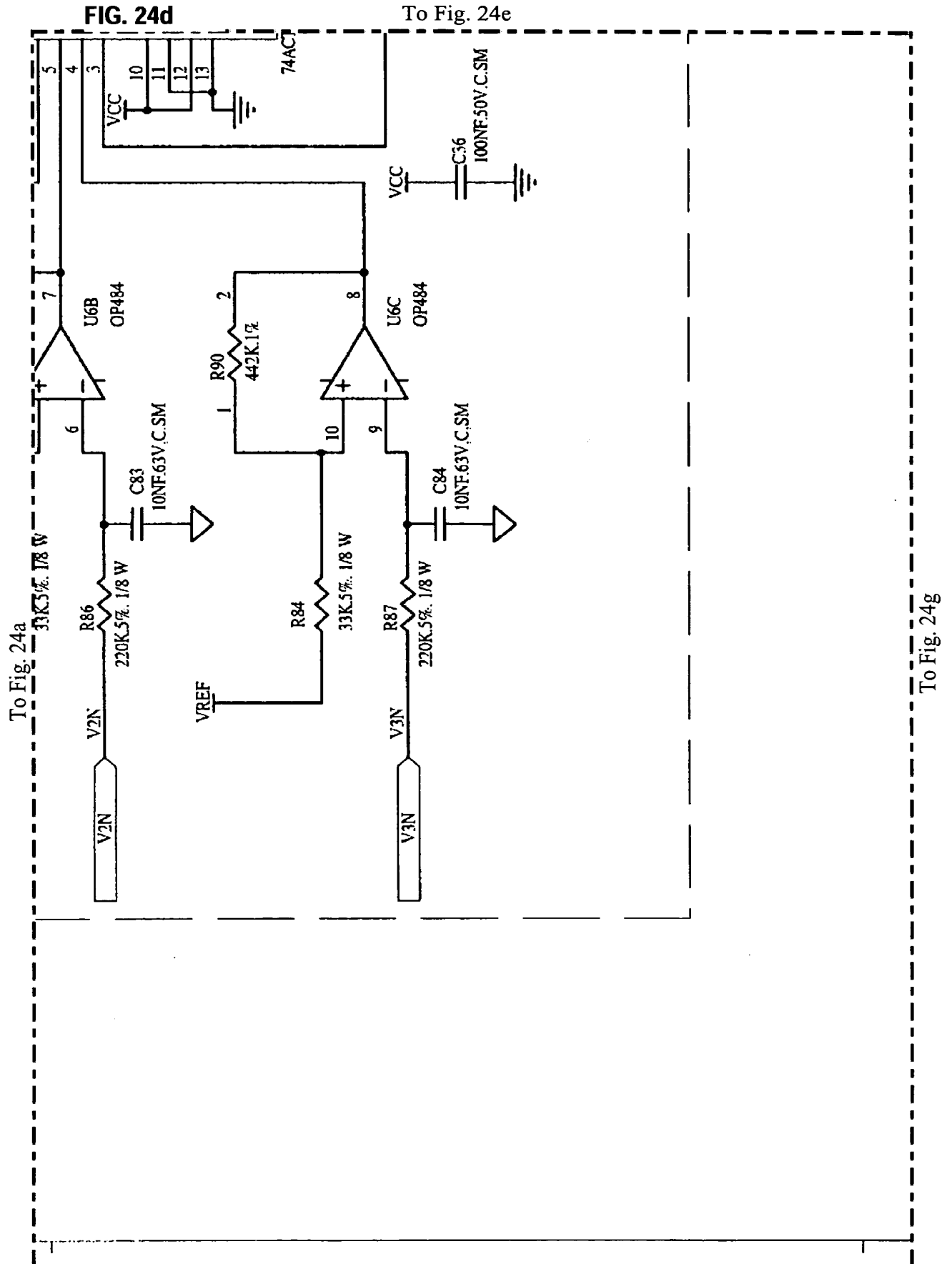


FIG. 24c





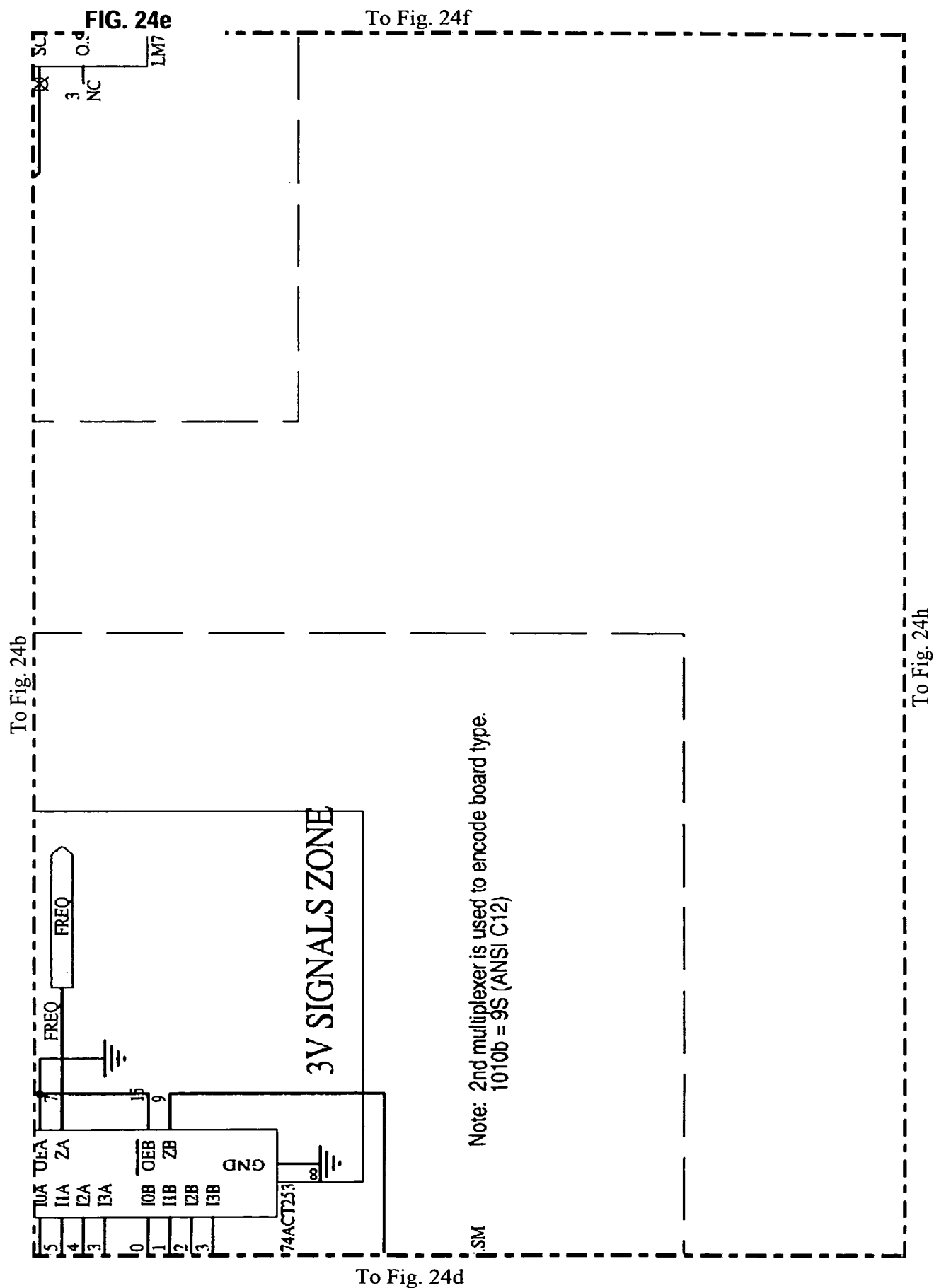


FIG. 24f

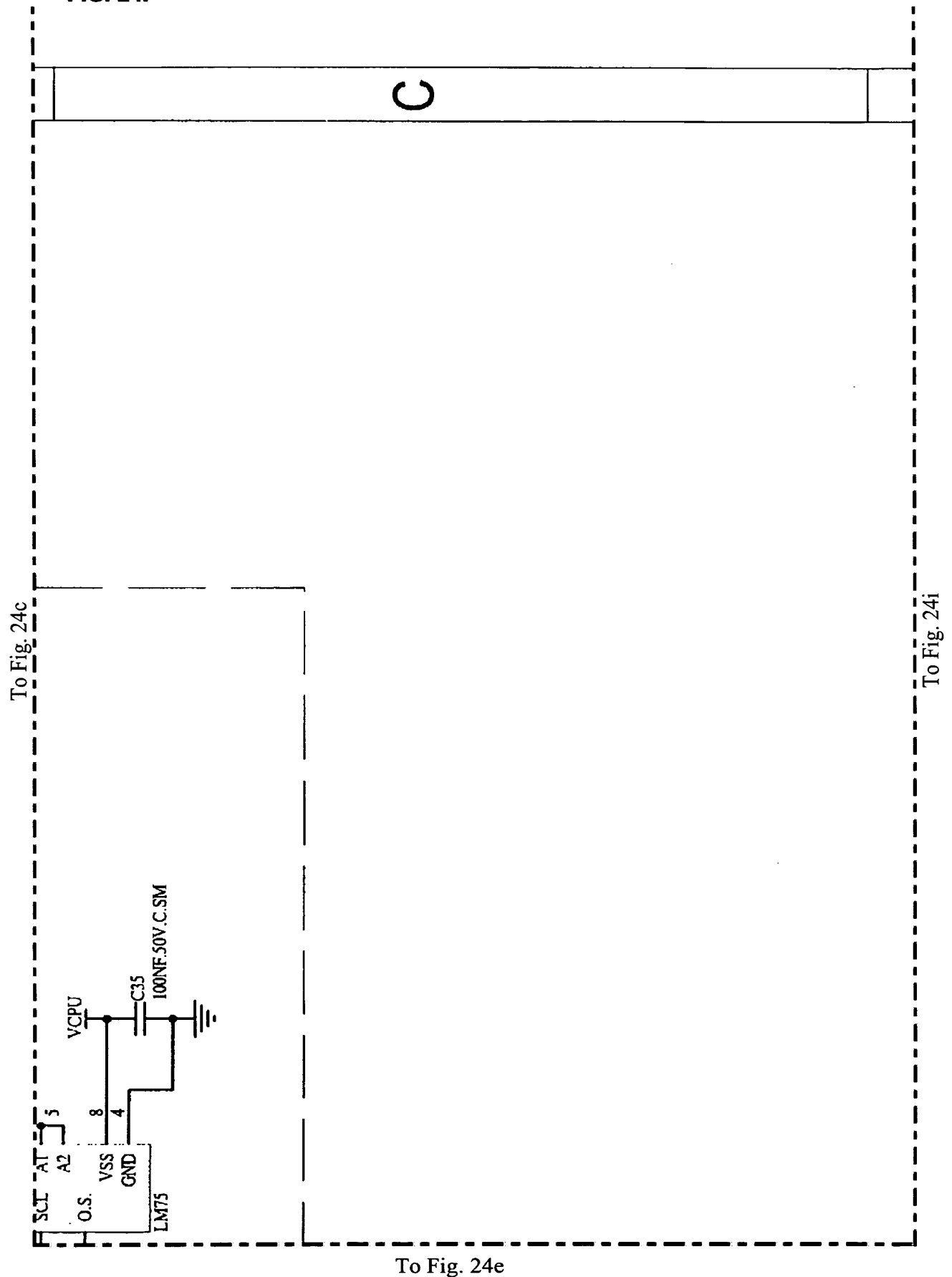


FIG. 24g

To Fig. 24h

To Fig. 24d

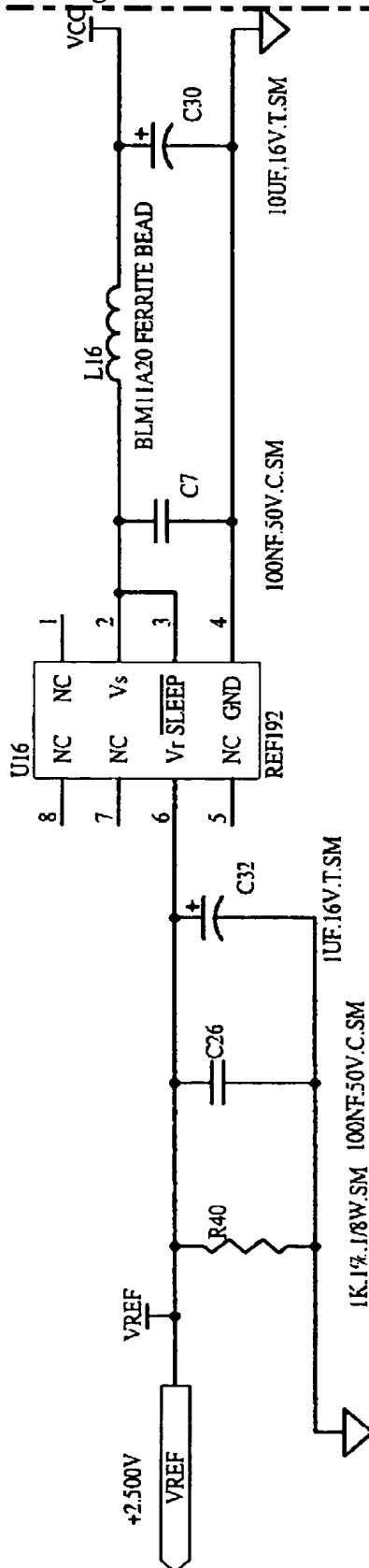
To Fig. 24j

To Fig. 24e

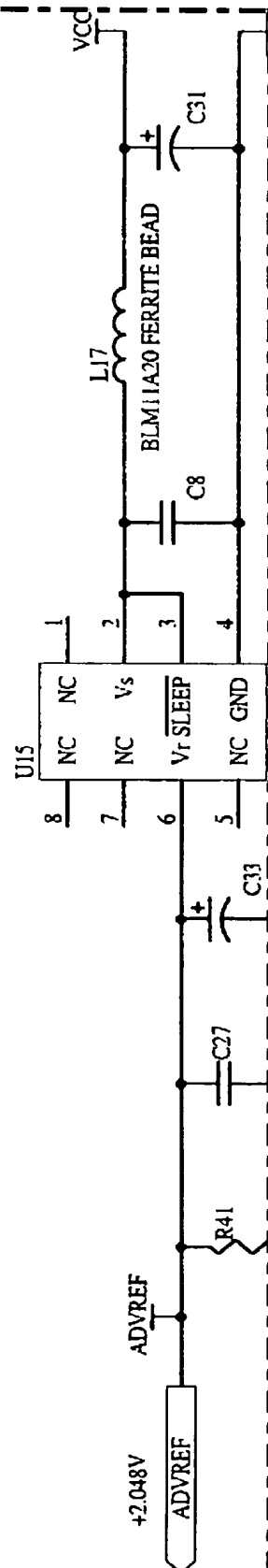
FIG. 24h

REFERENCE VOLTAGE

To Fig. 24i



To Fig. 24g



To Fig. 24k

FIG. 24i

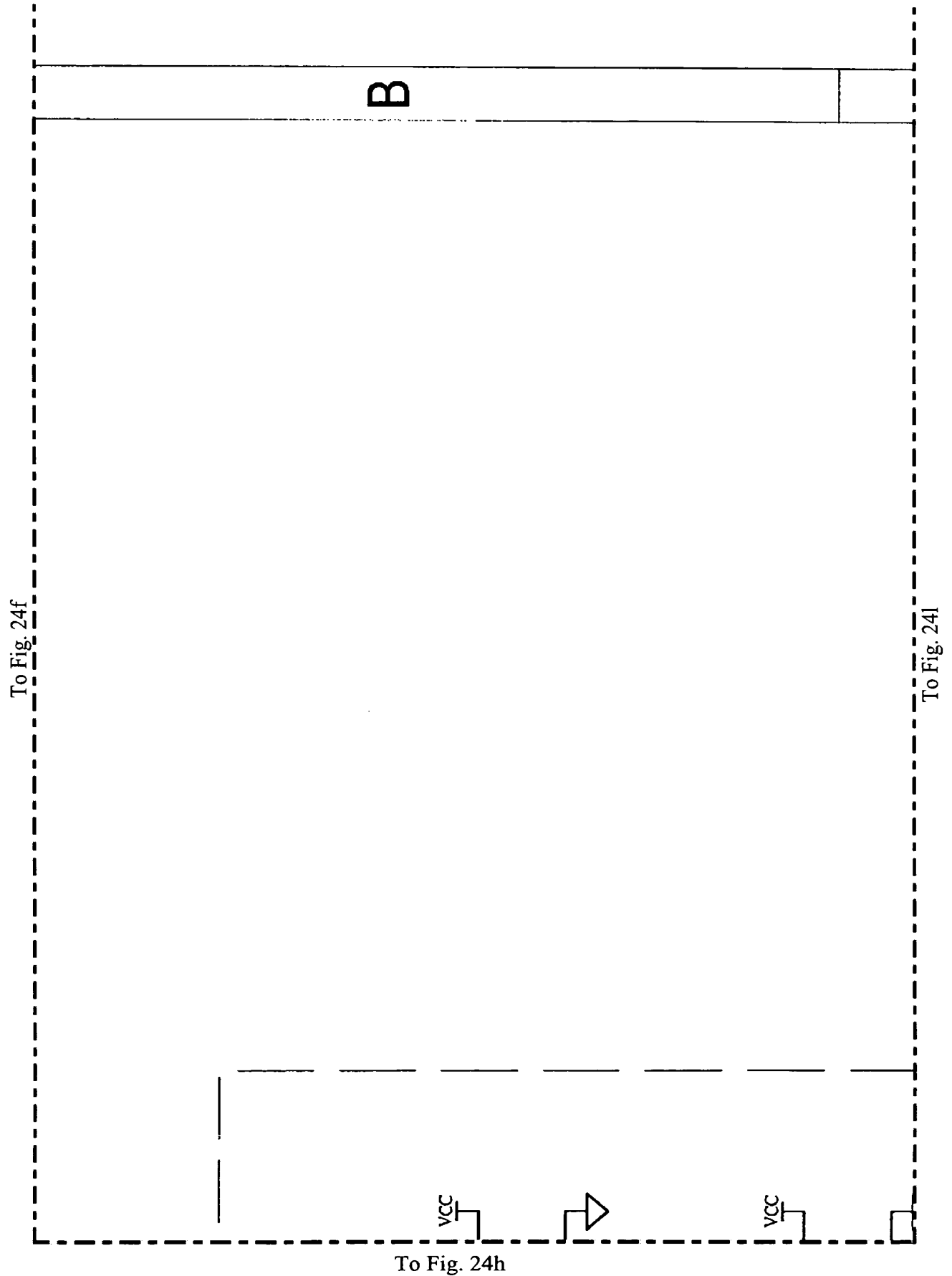
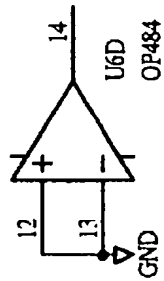


FIG. 24j

To Fig. 24k

To Fig. 24g

UNUSED OP-AMP



2

1

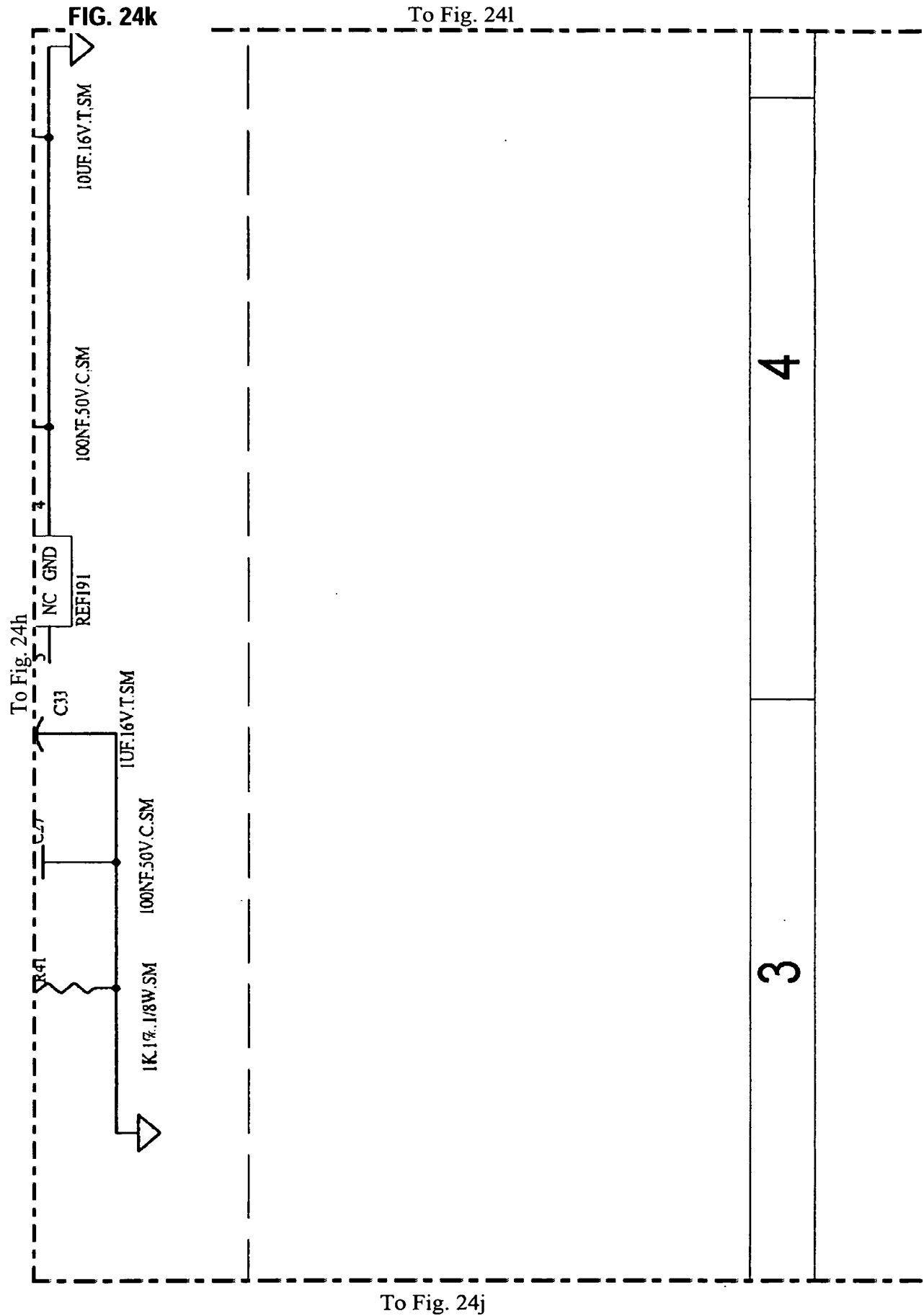


FIG. 24i

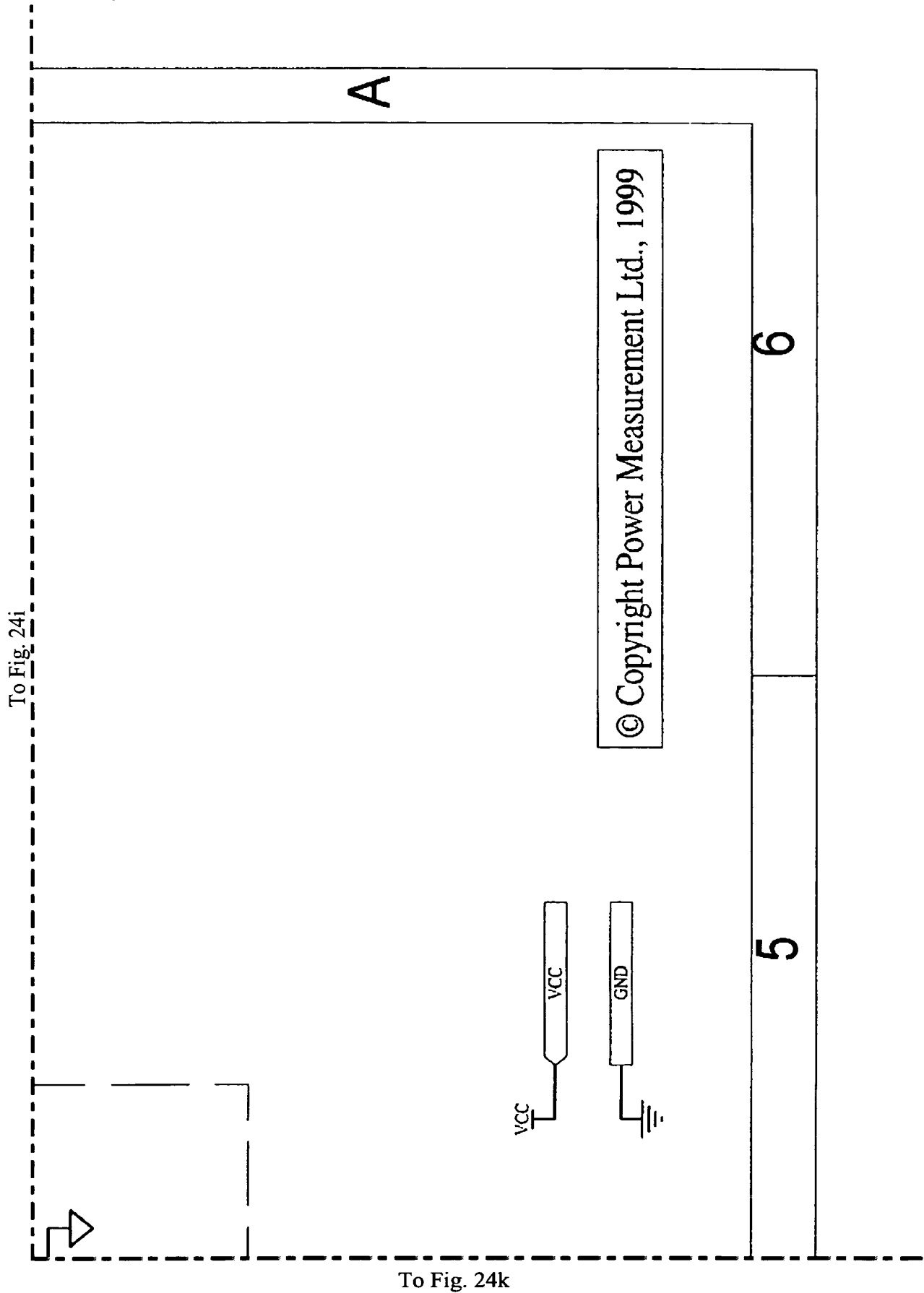
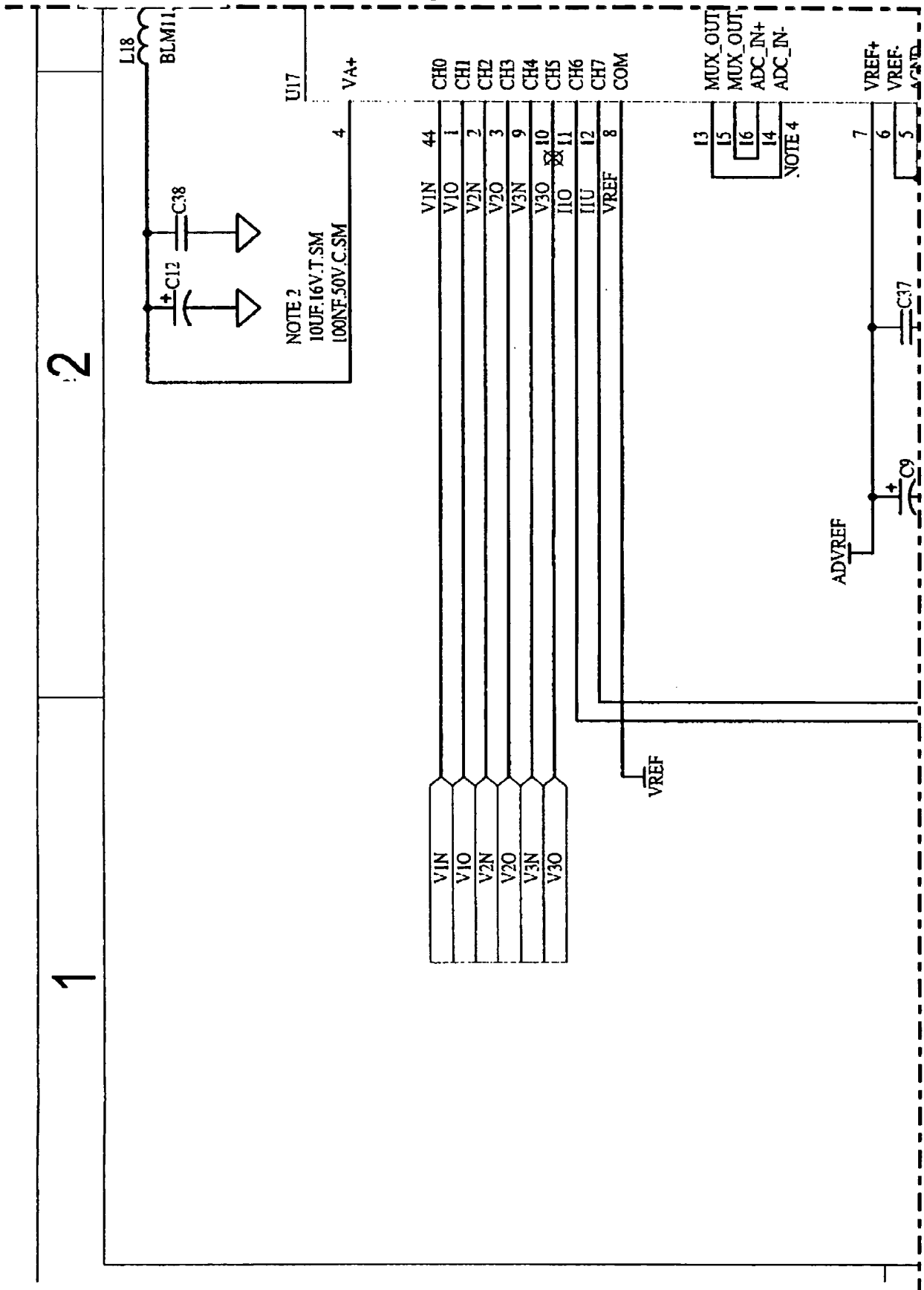


FIG. 25a

To Fig. 25b



To Fig. 25d

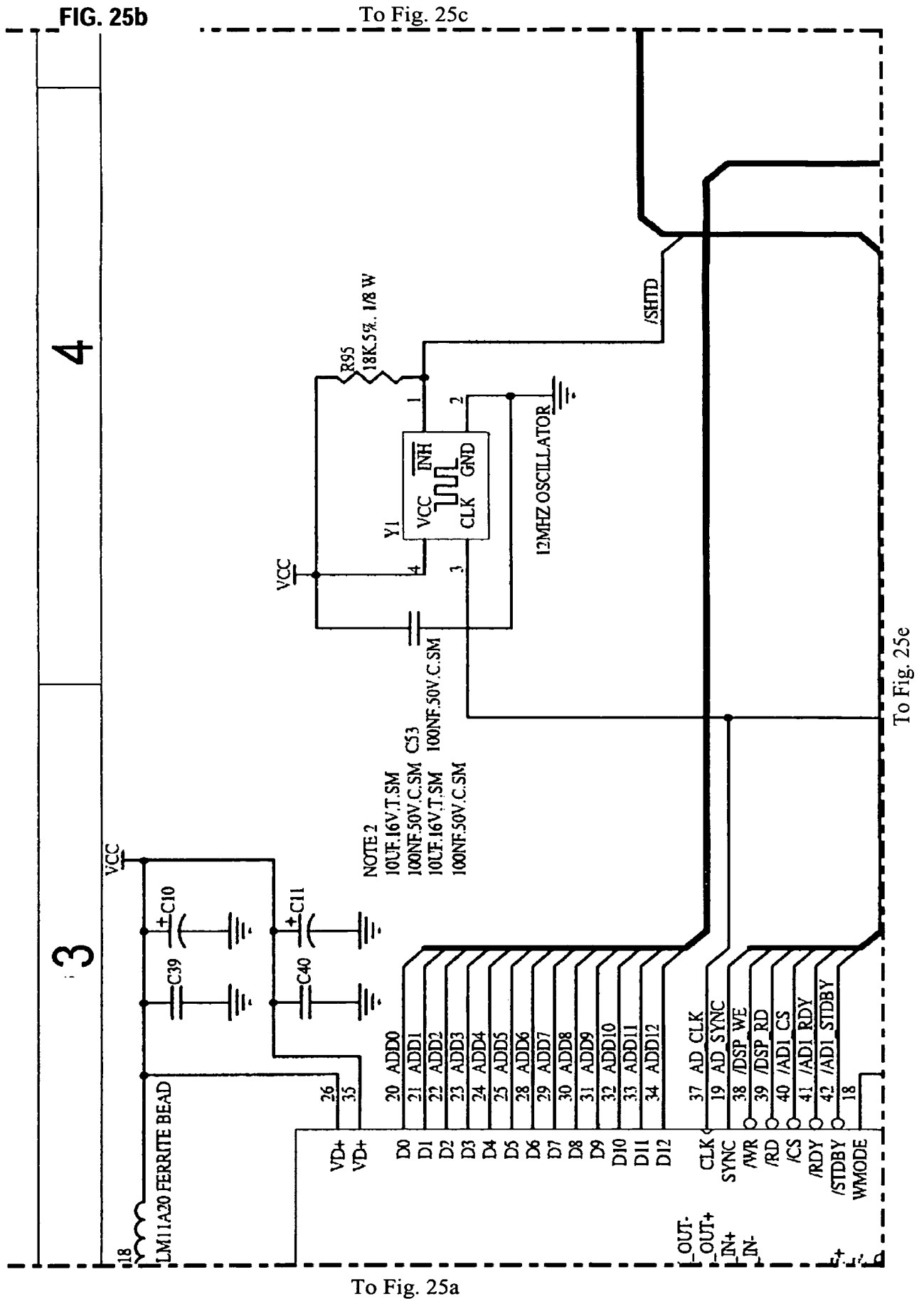
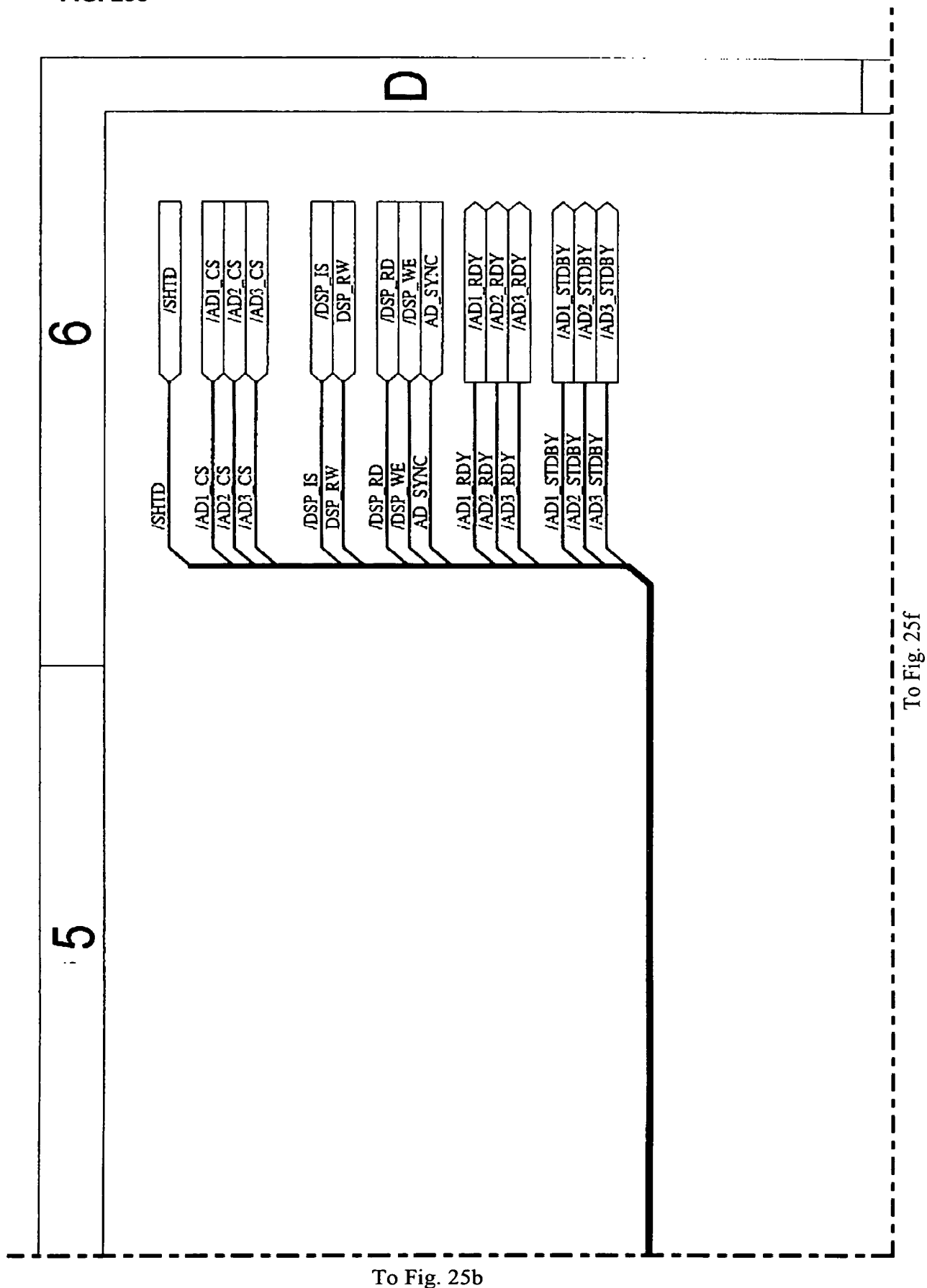
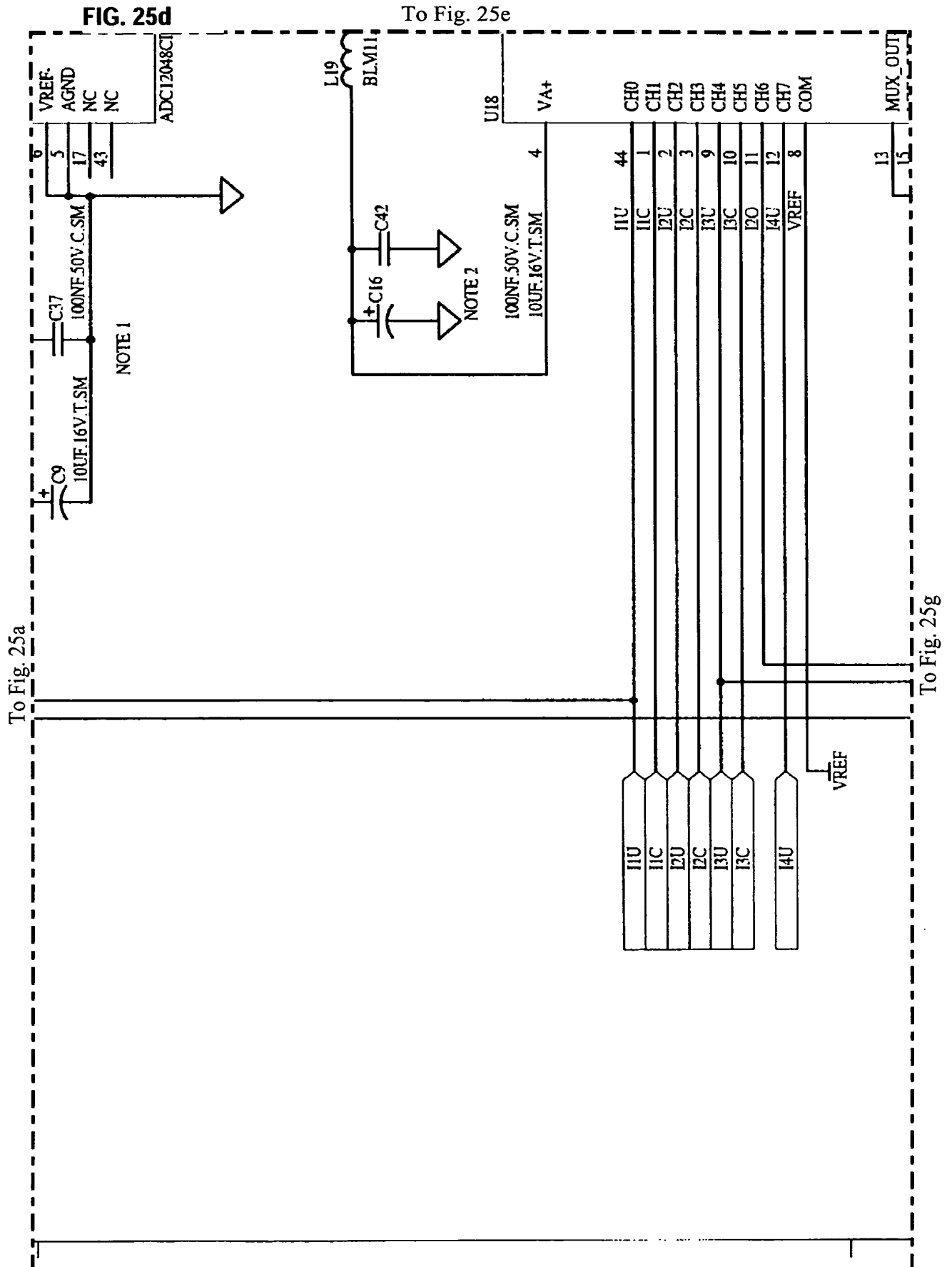


FIG. 25c





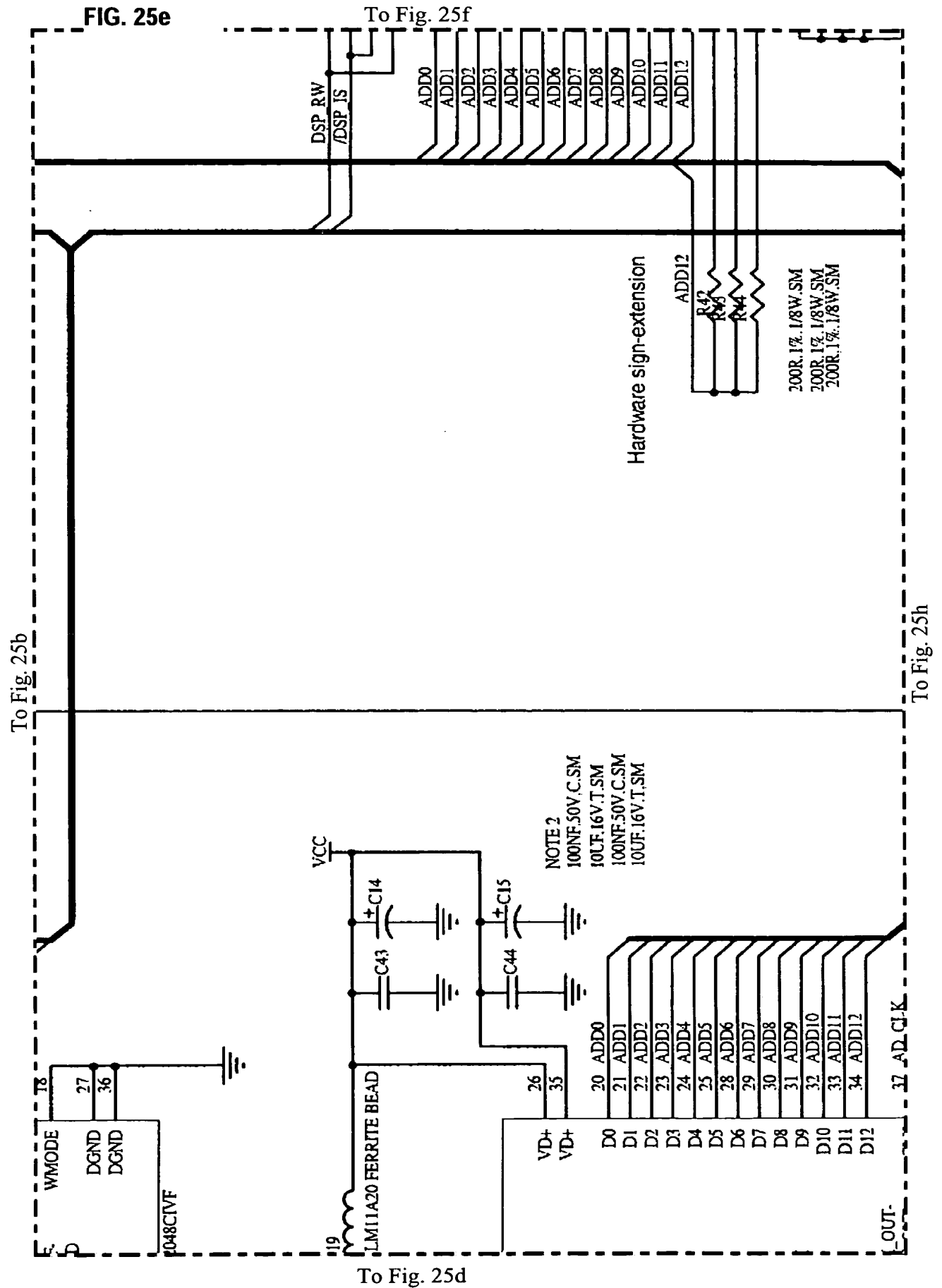
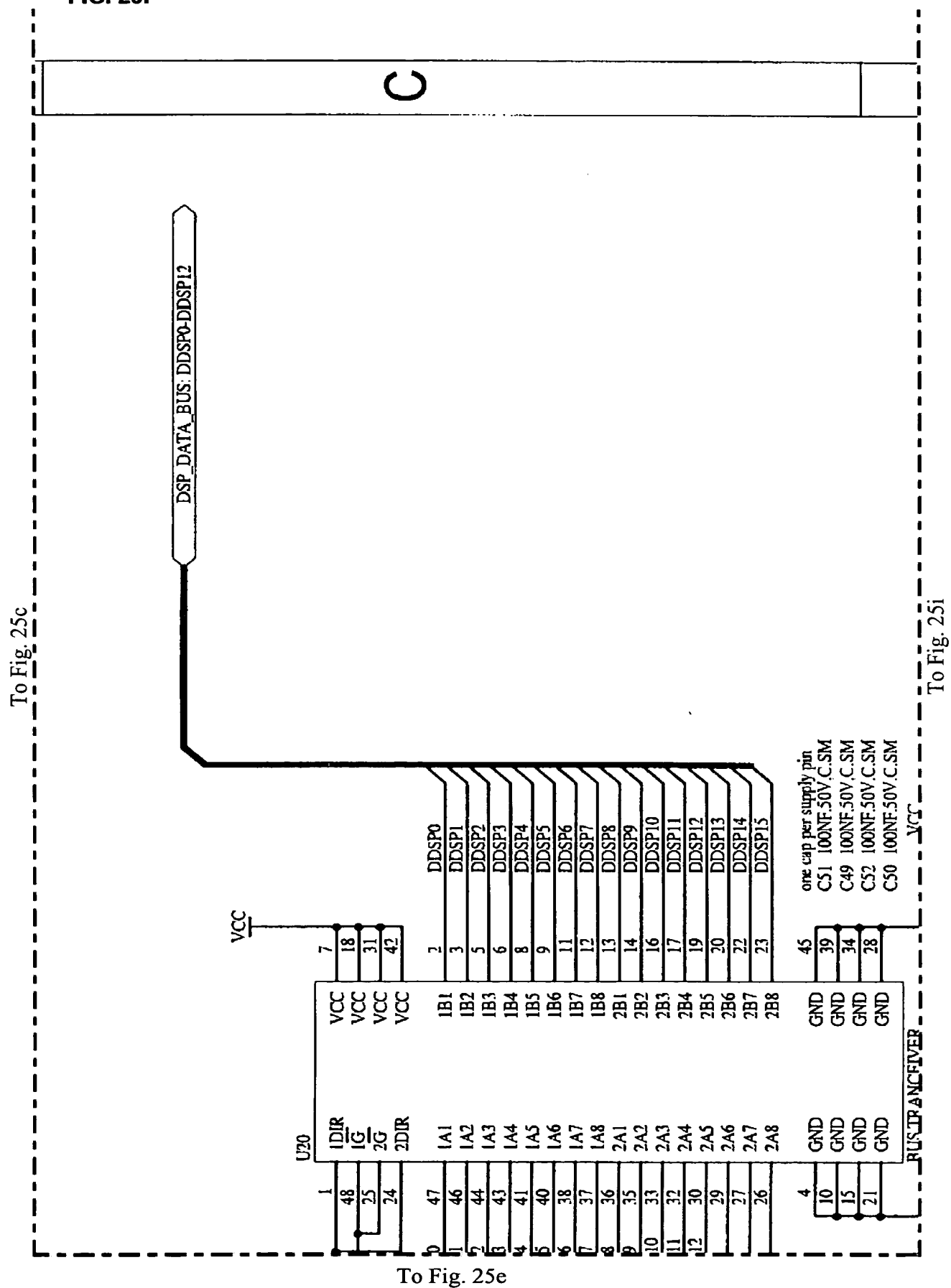
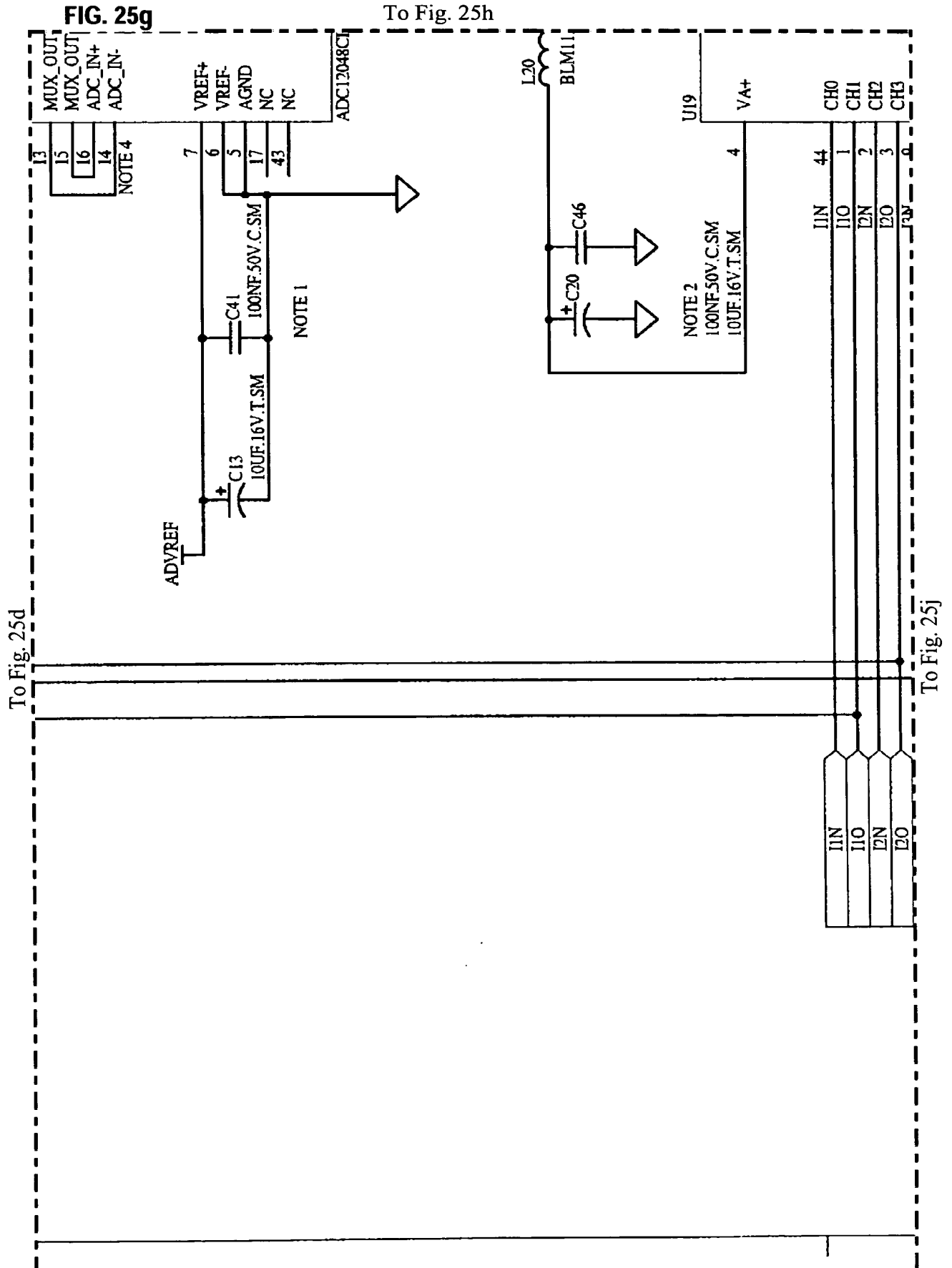


FIG. 25f





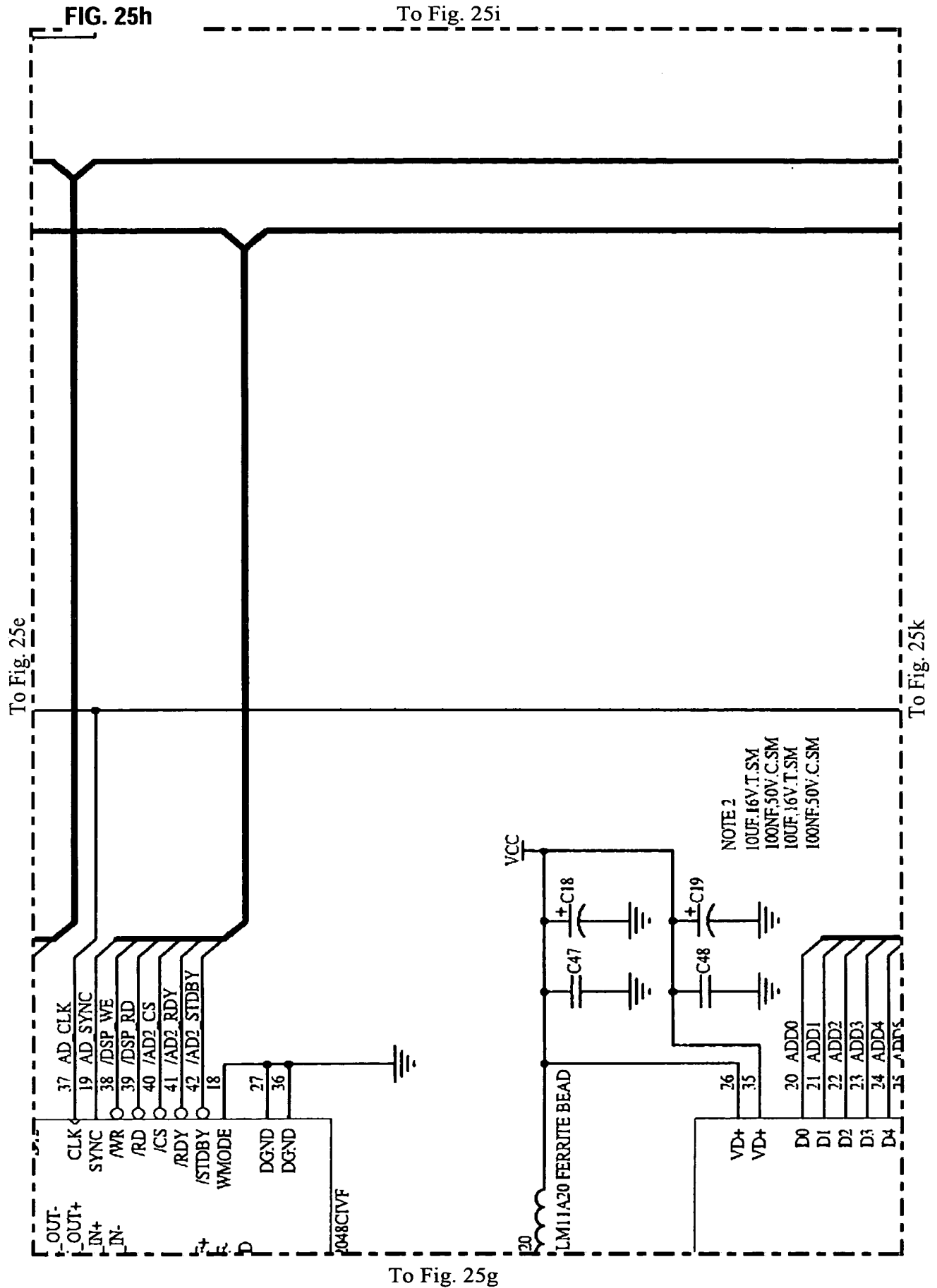
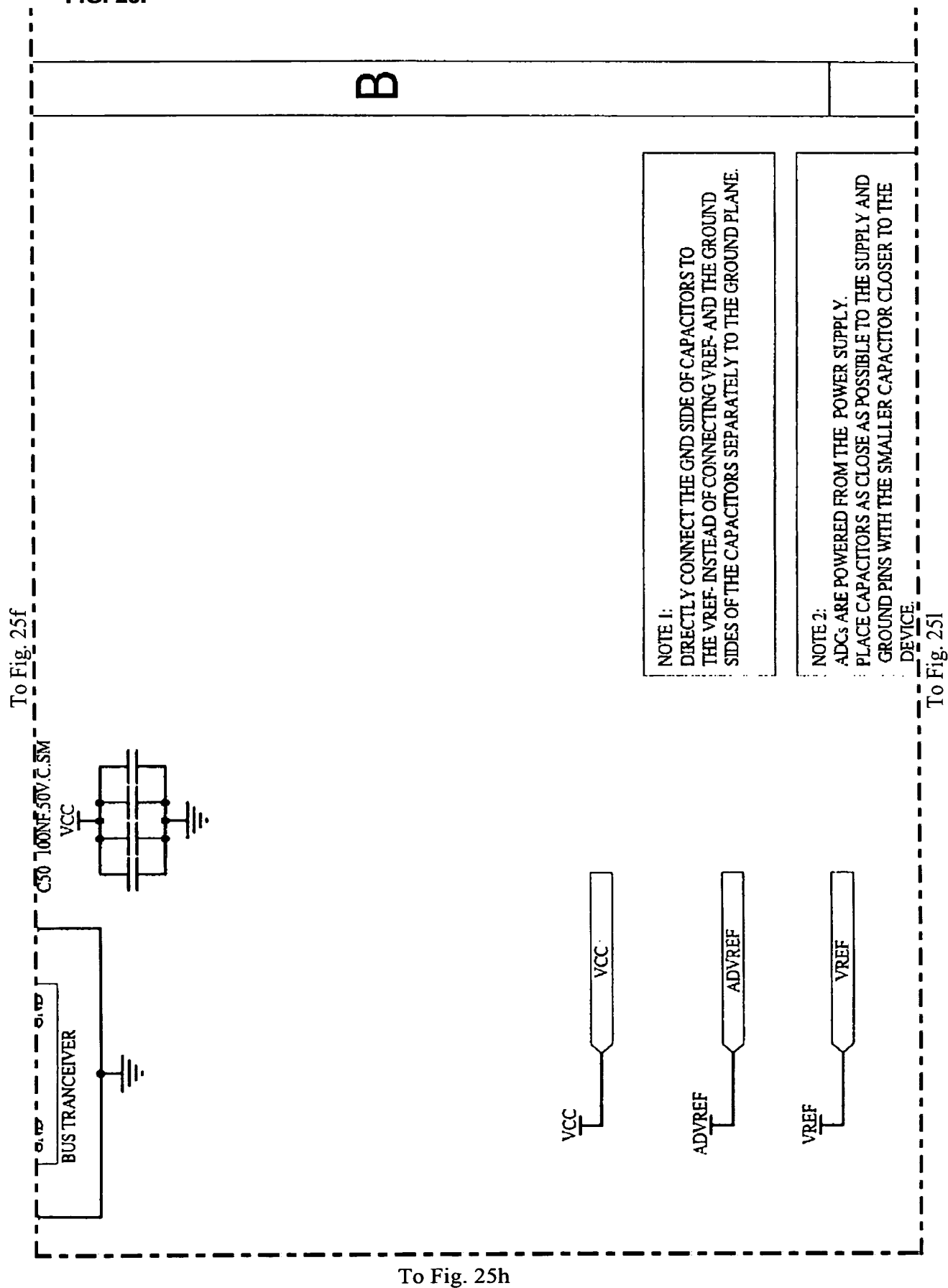
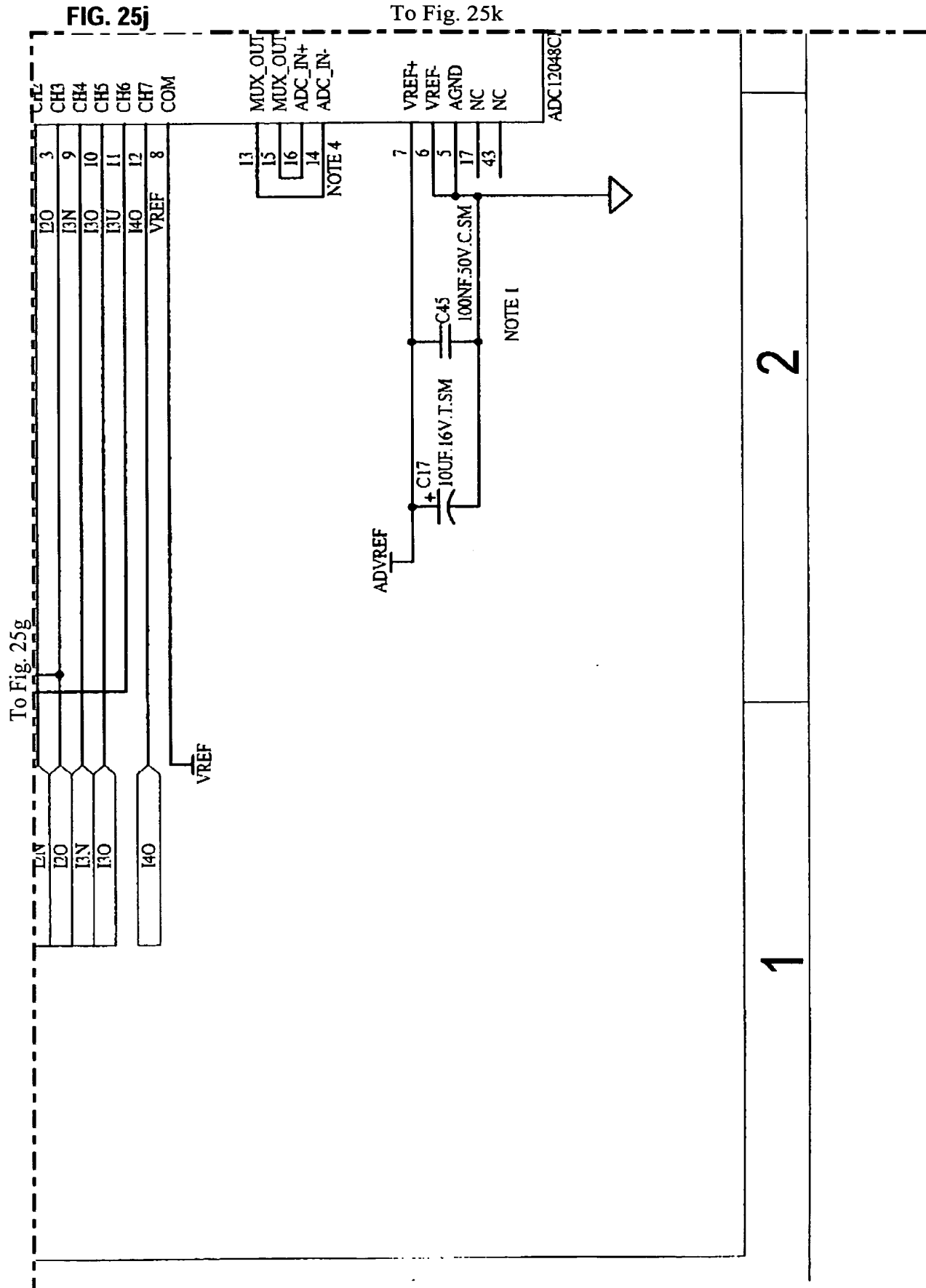


FIG. 25i





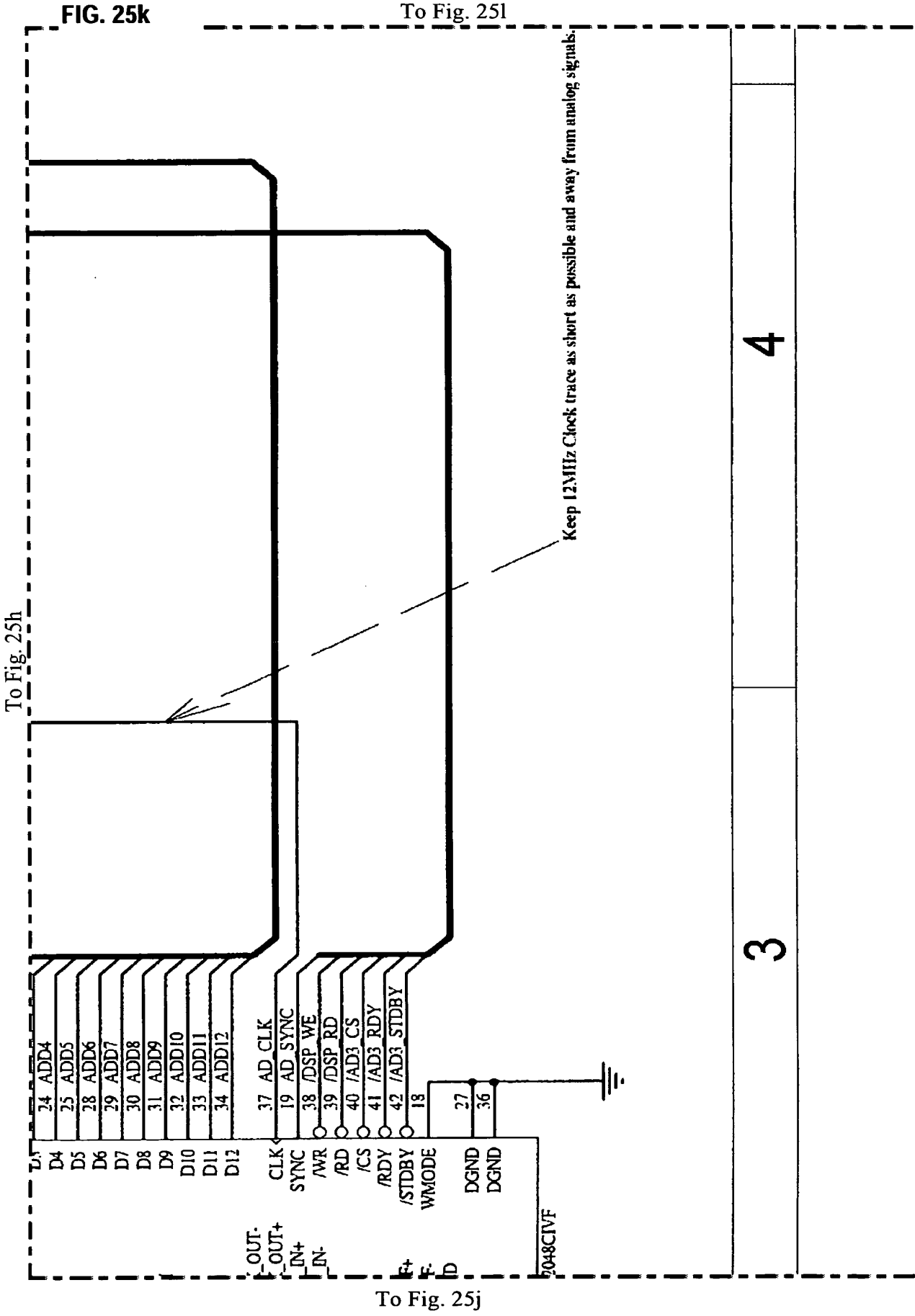
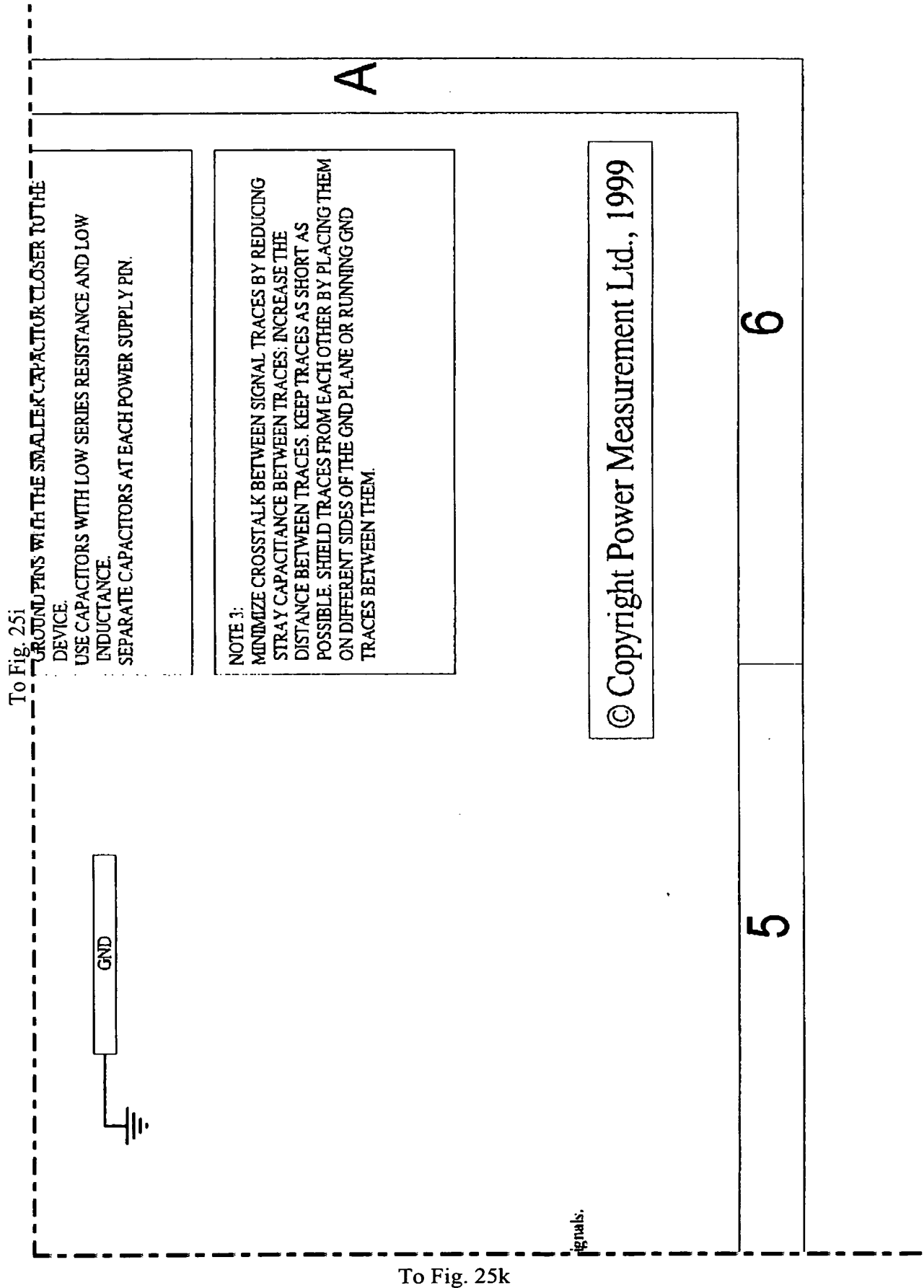


FIG. 25I



To Fig. 26b

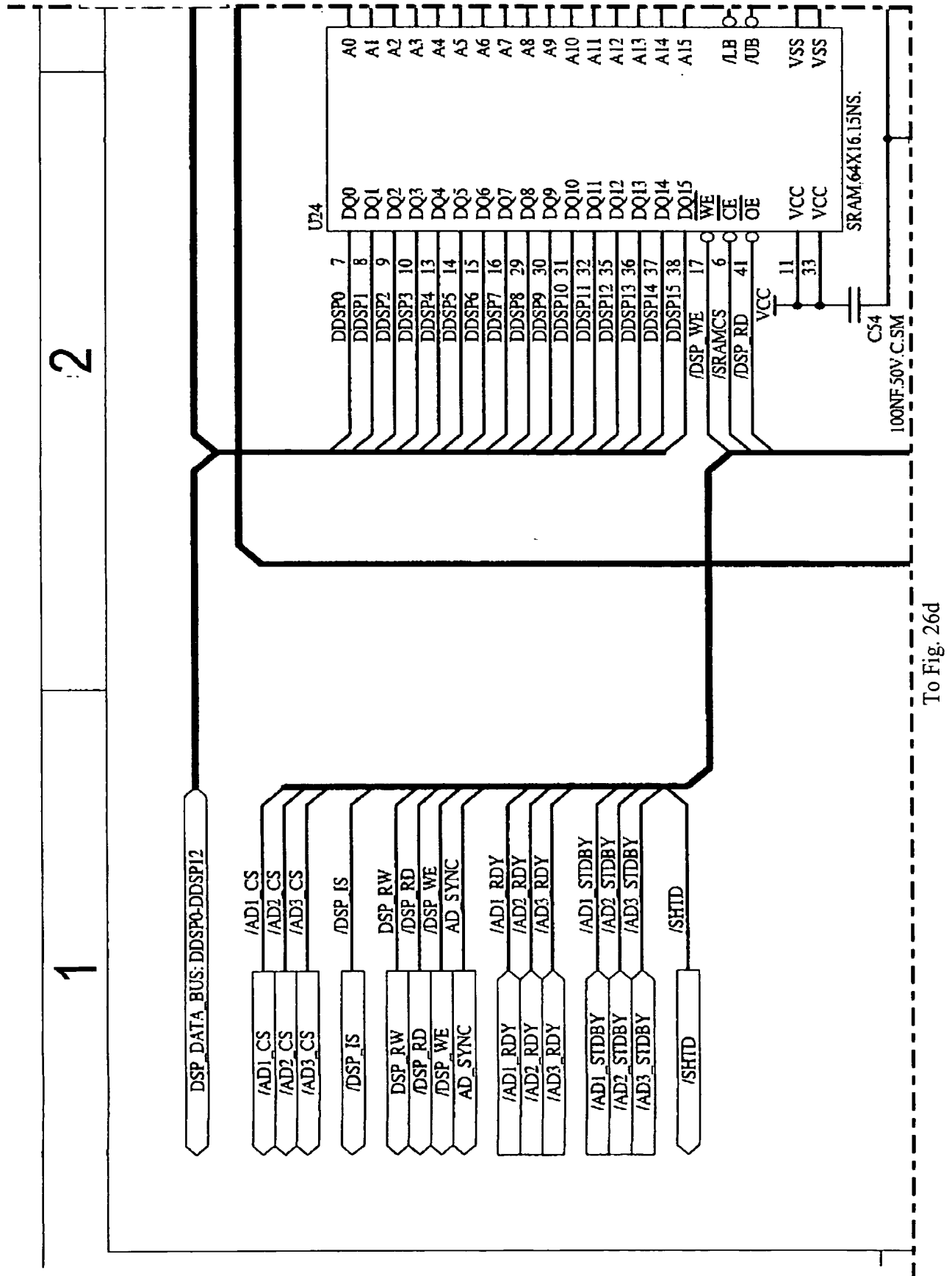
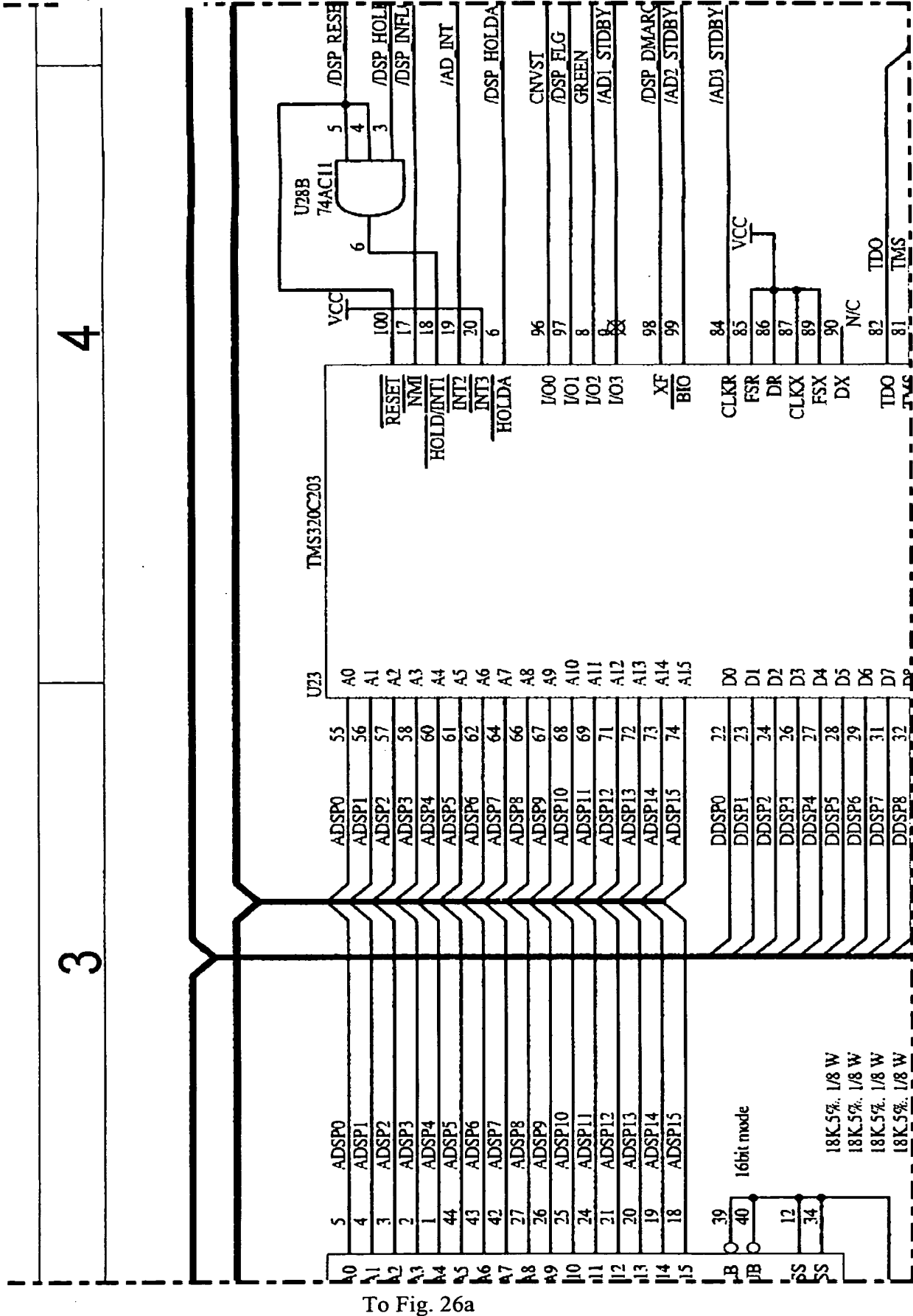


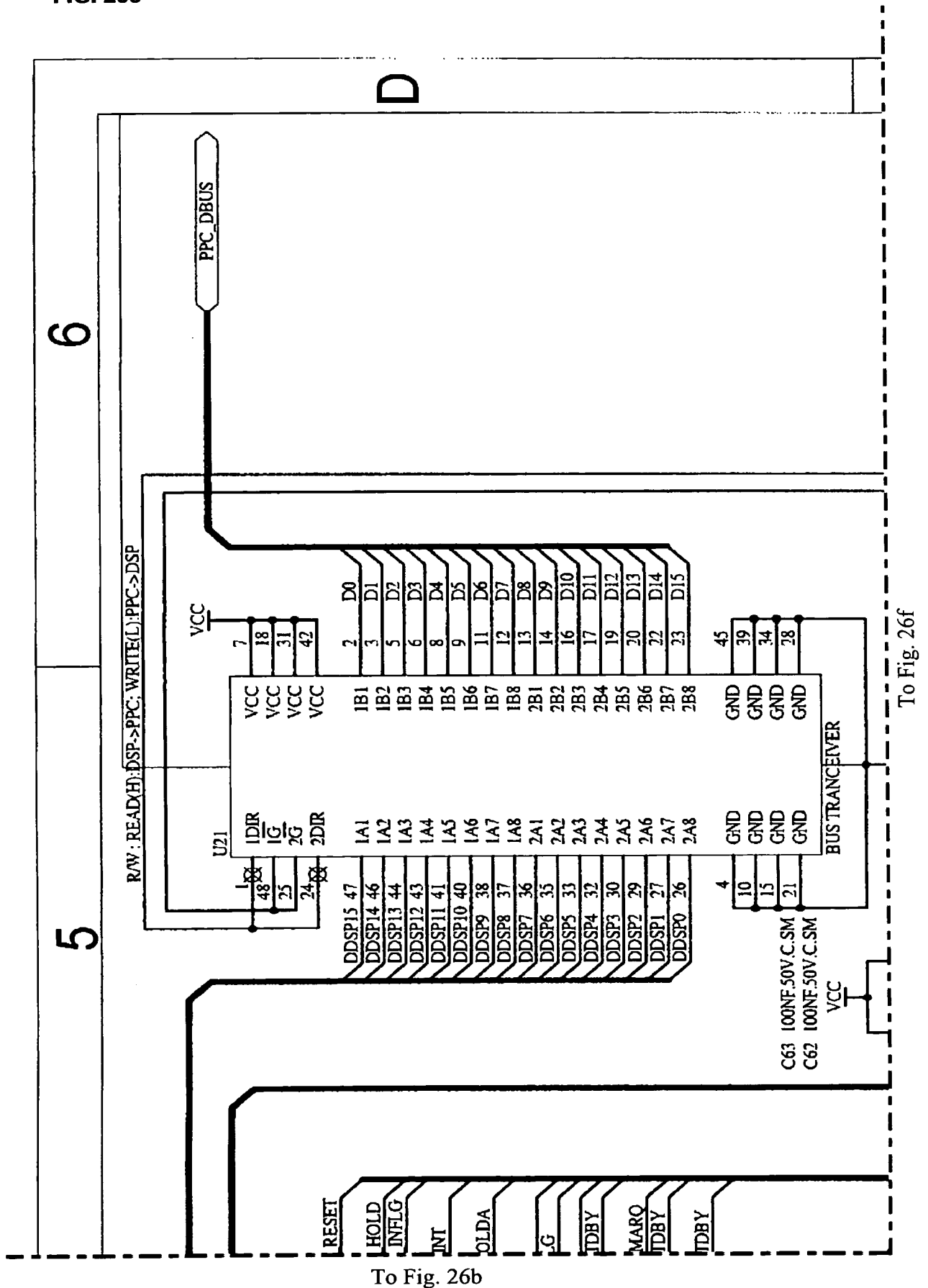
FIG. 26b



To Fig. 26a

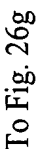
To Fig. 26e

FIG. 26c



To Fig. 26b

To Fig. 26f



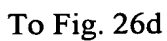
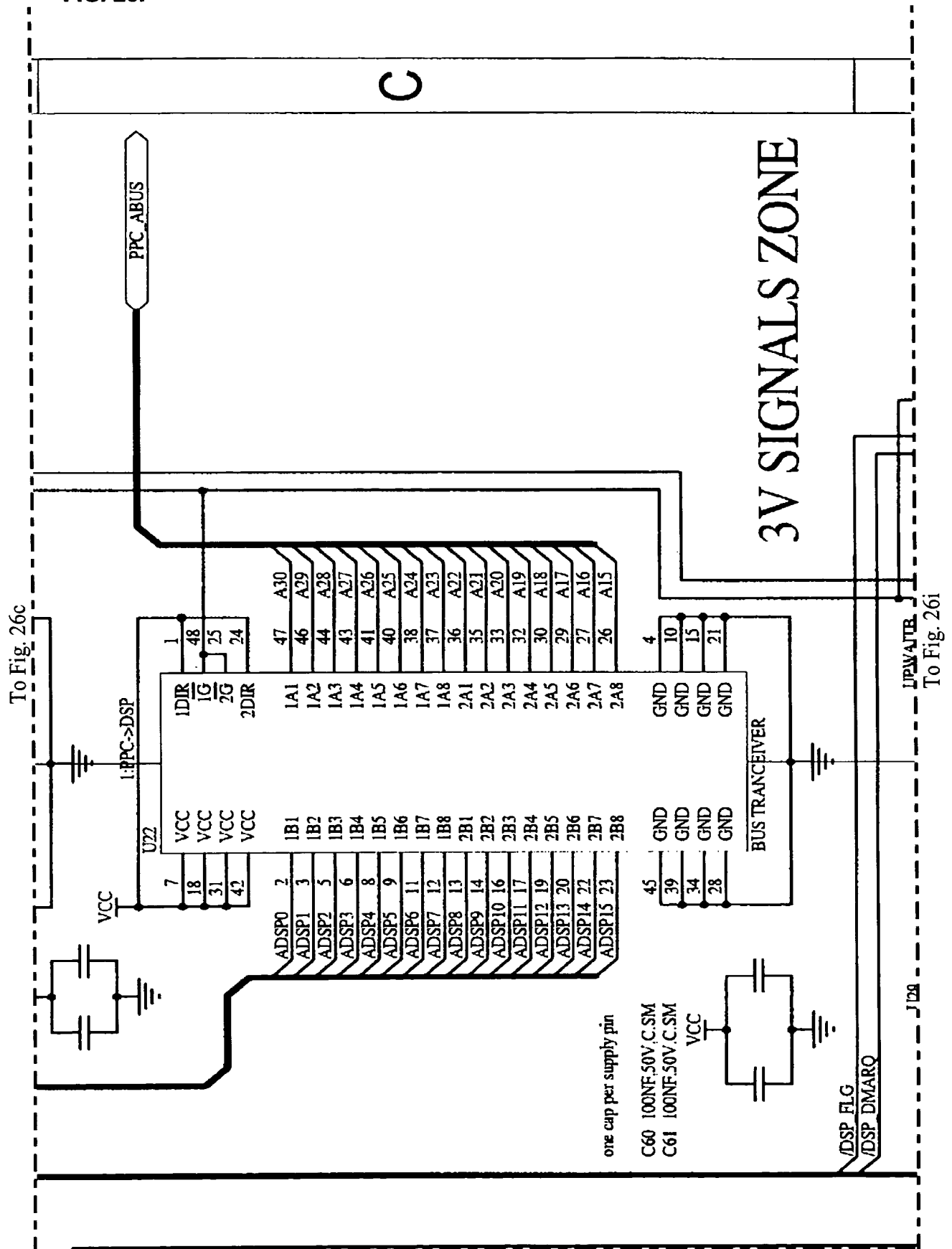
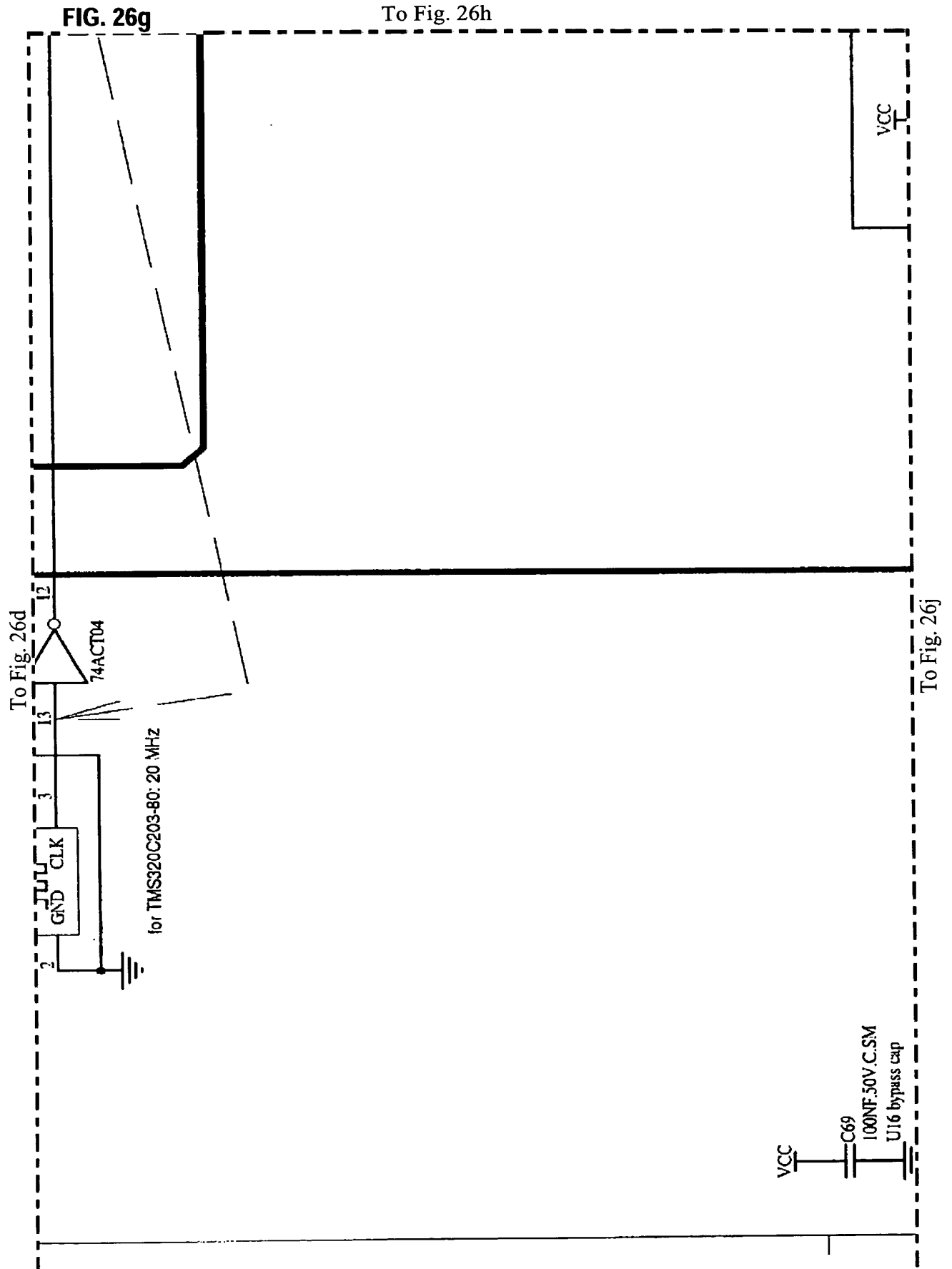


FIG. 26f





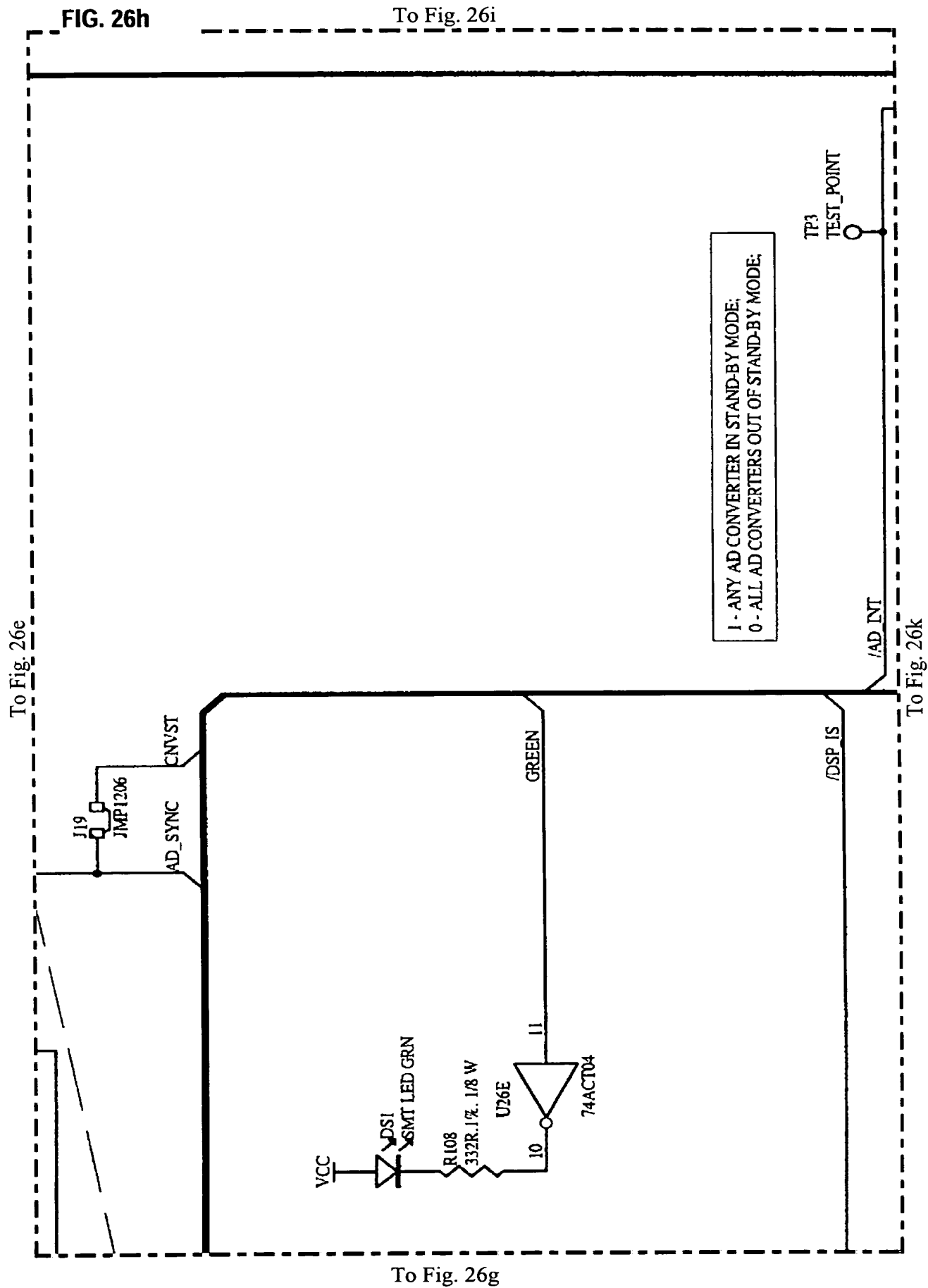
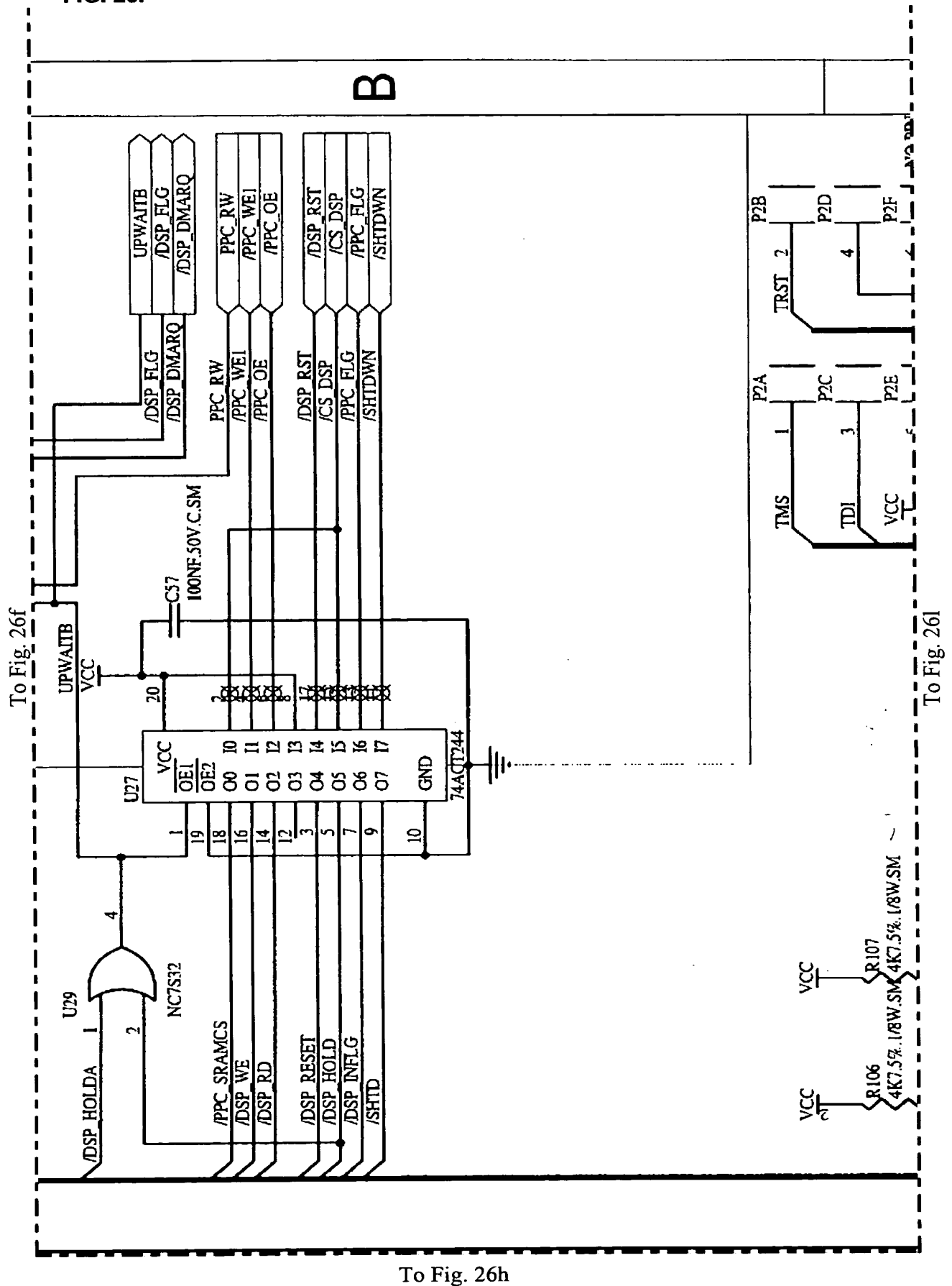
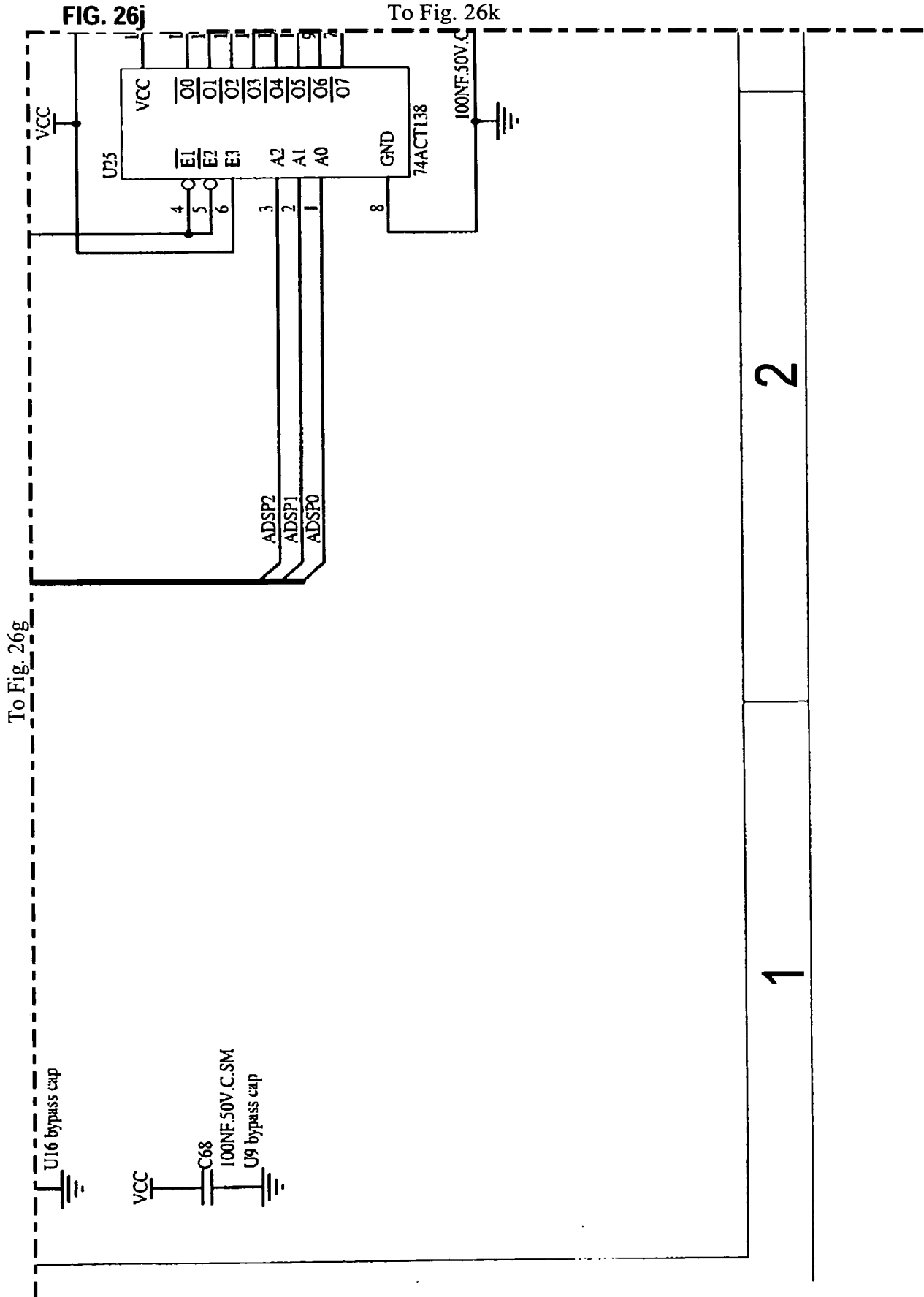


FIG. 26i





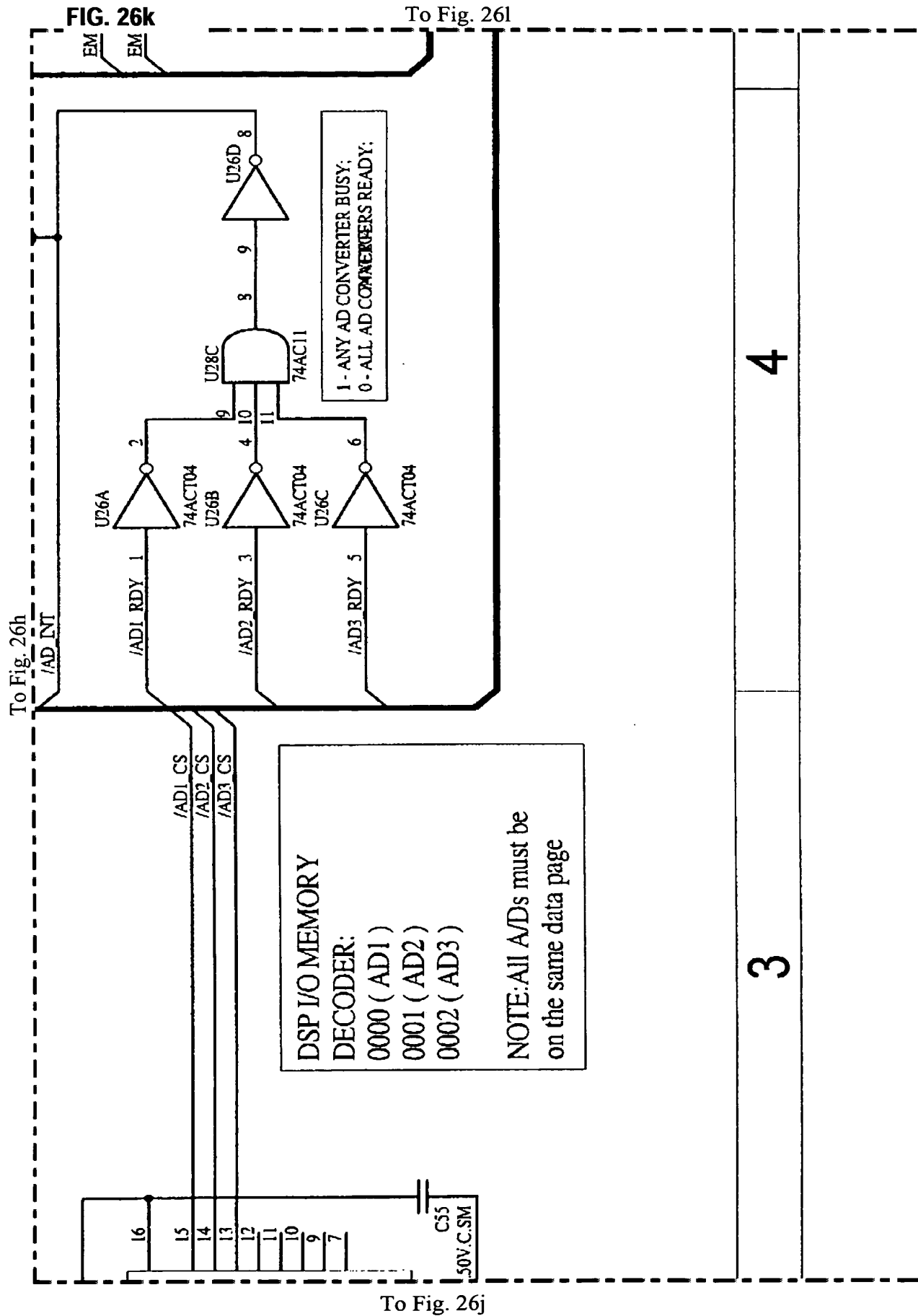


FIG. 26l

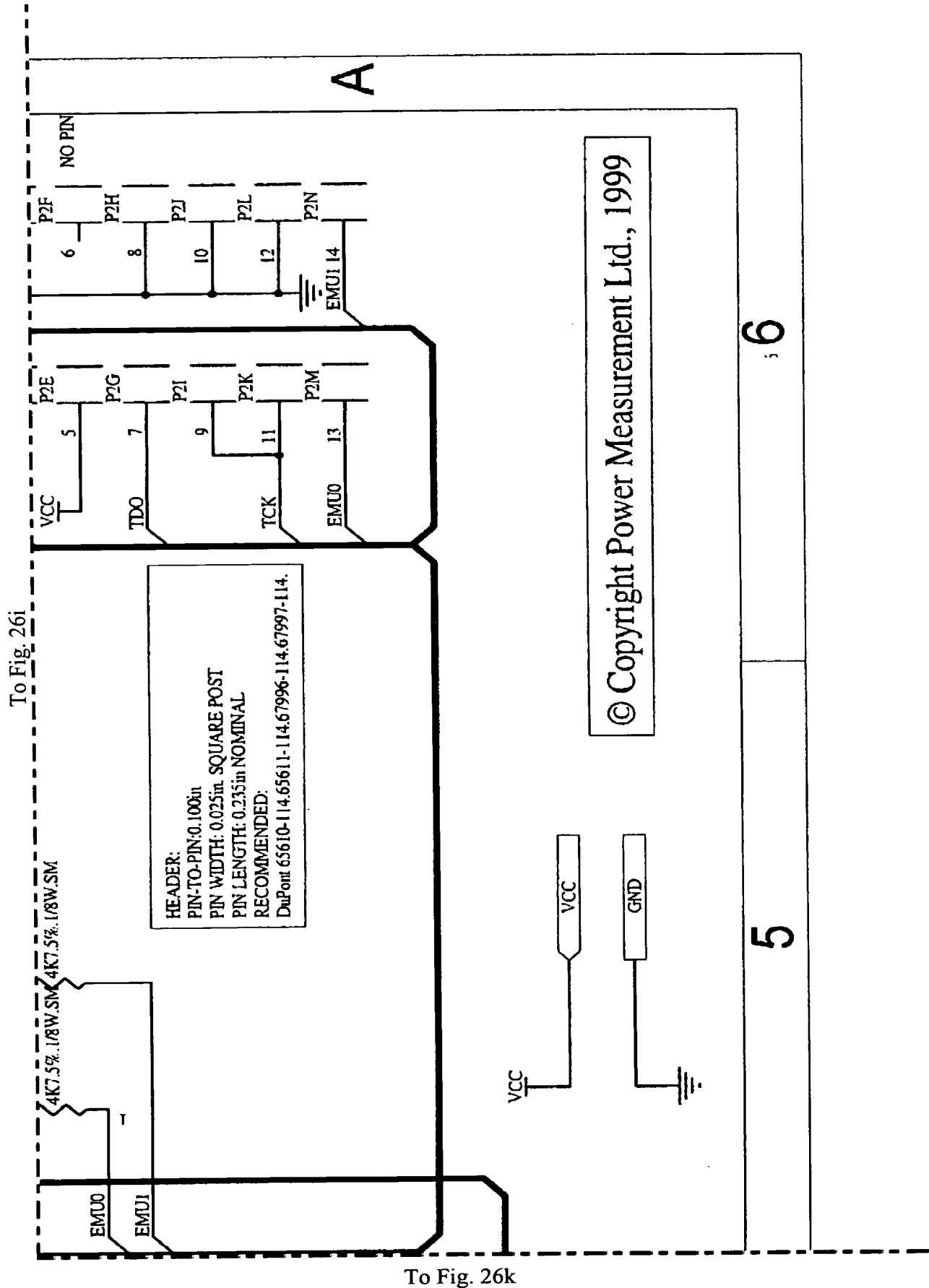
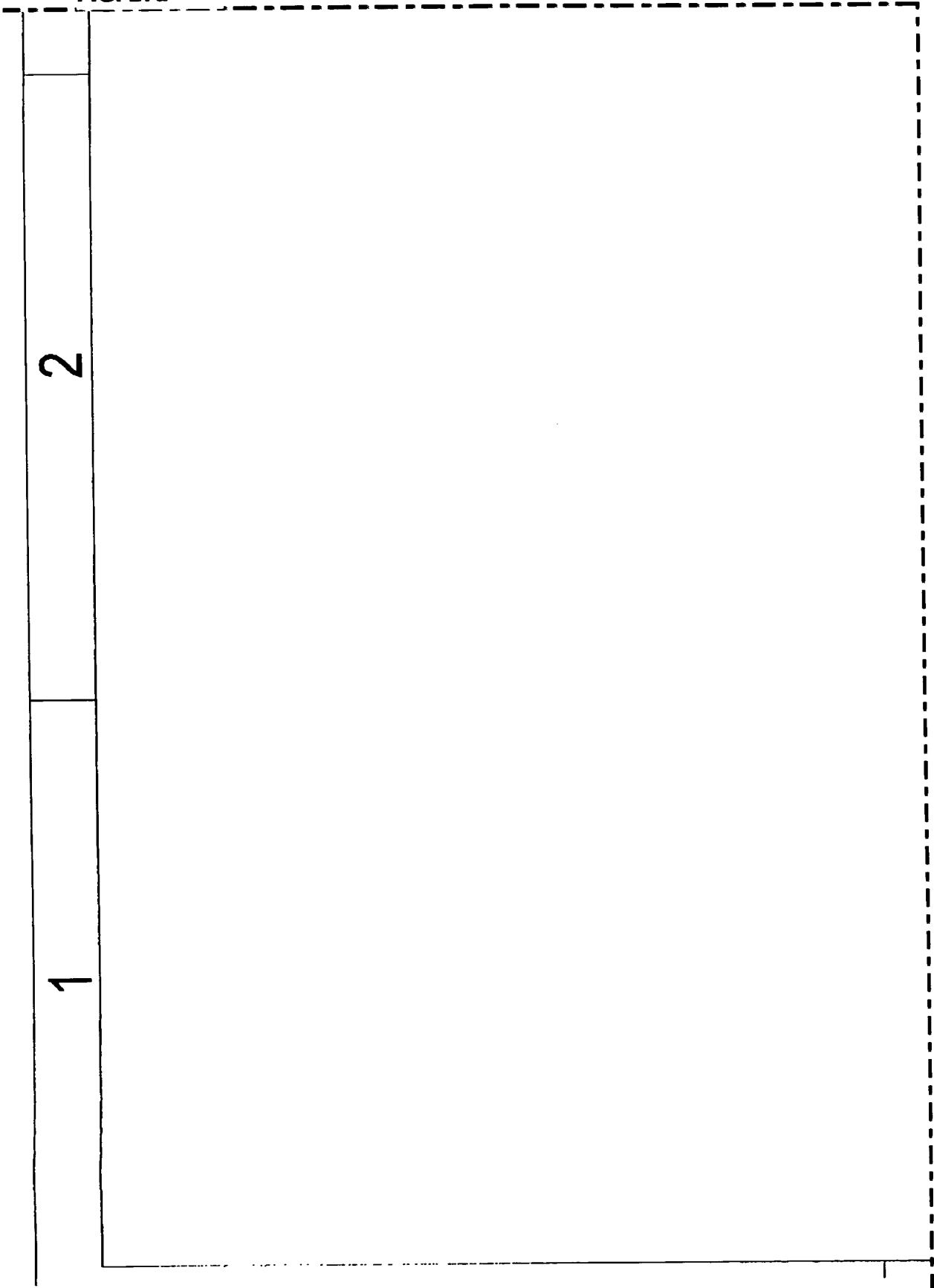


FIG. 27a

To Fig. 27b



To Fig. 27d

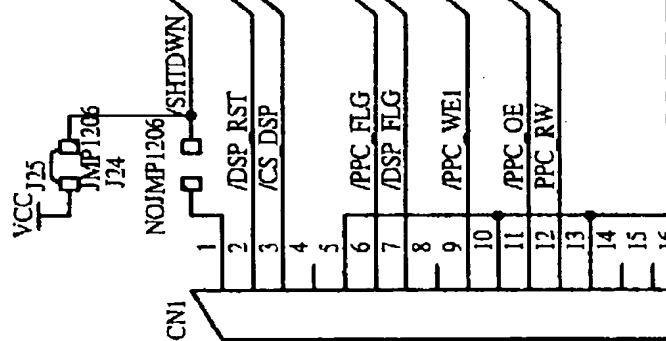
FIG. 27b

To Fig. 27c

4

3

MEASUREMENT BOARD CONNECTOR



To Fig. 27e

To Fig. 27a

FIG. 27c

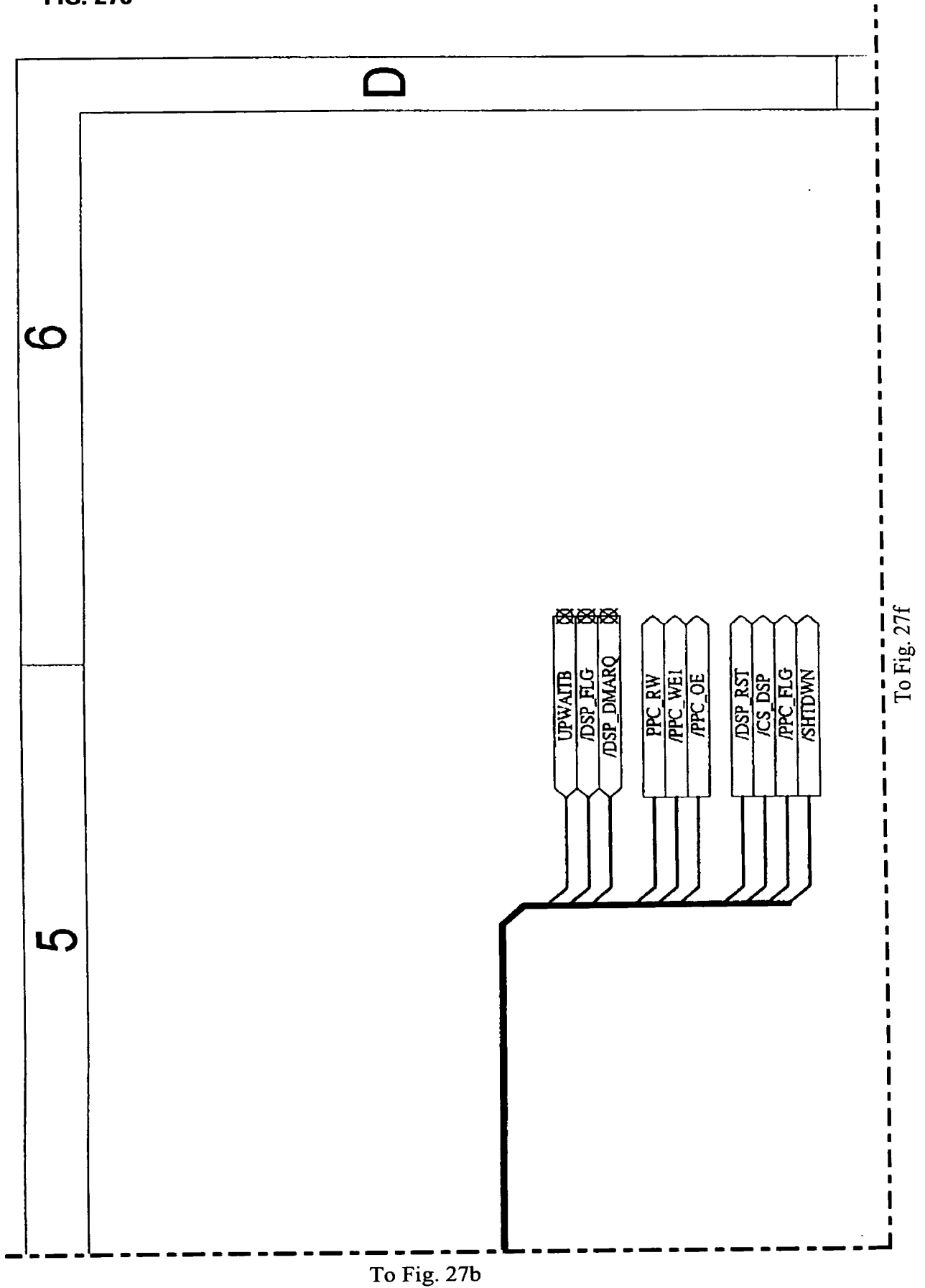


FIG. 27d

To Fig. 27e

To Fig. 27a

To Fig. 27g

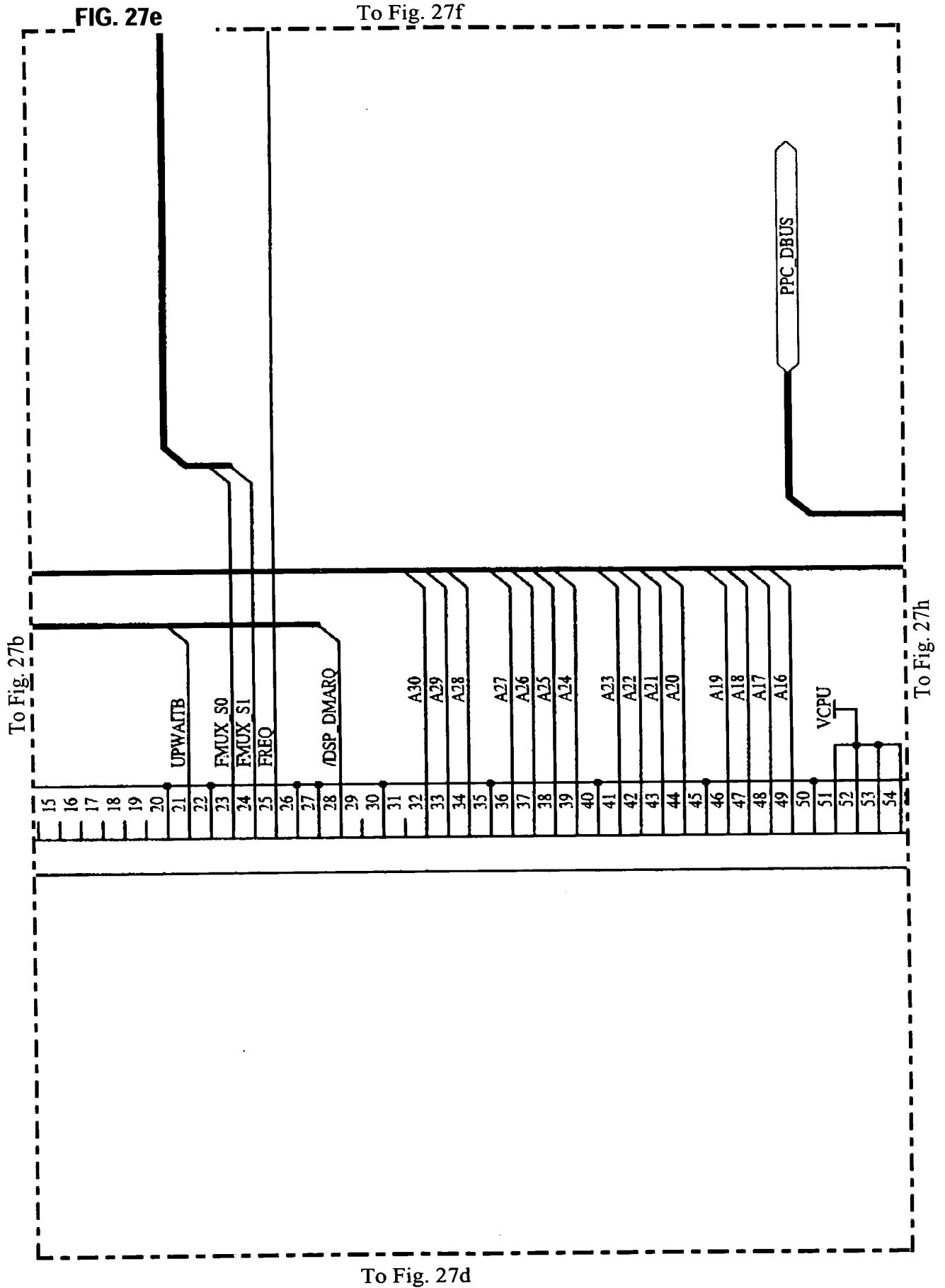


FIG. 27f

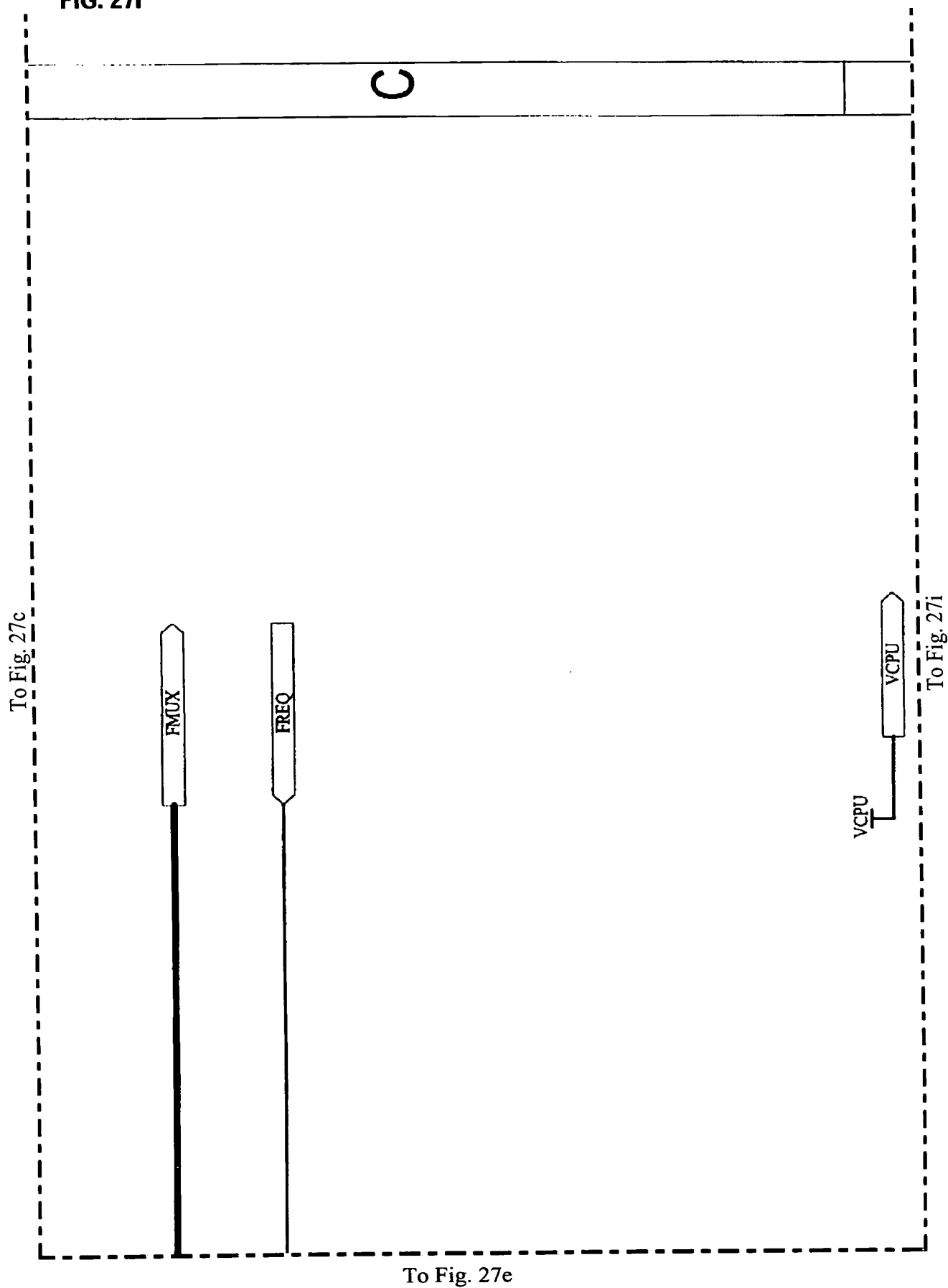


FIG. 27g

To Fig. 27h

To Fig. 27d

To Fig. 27j

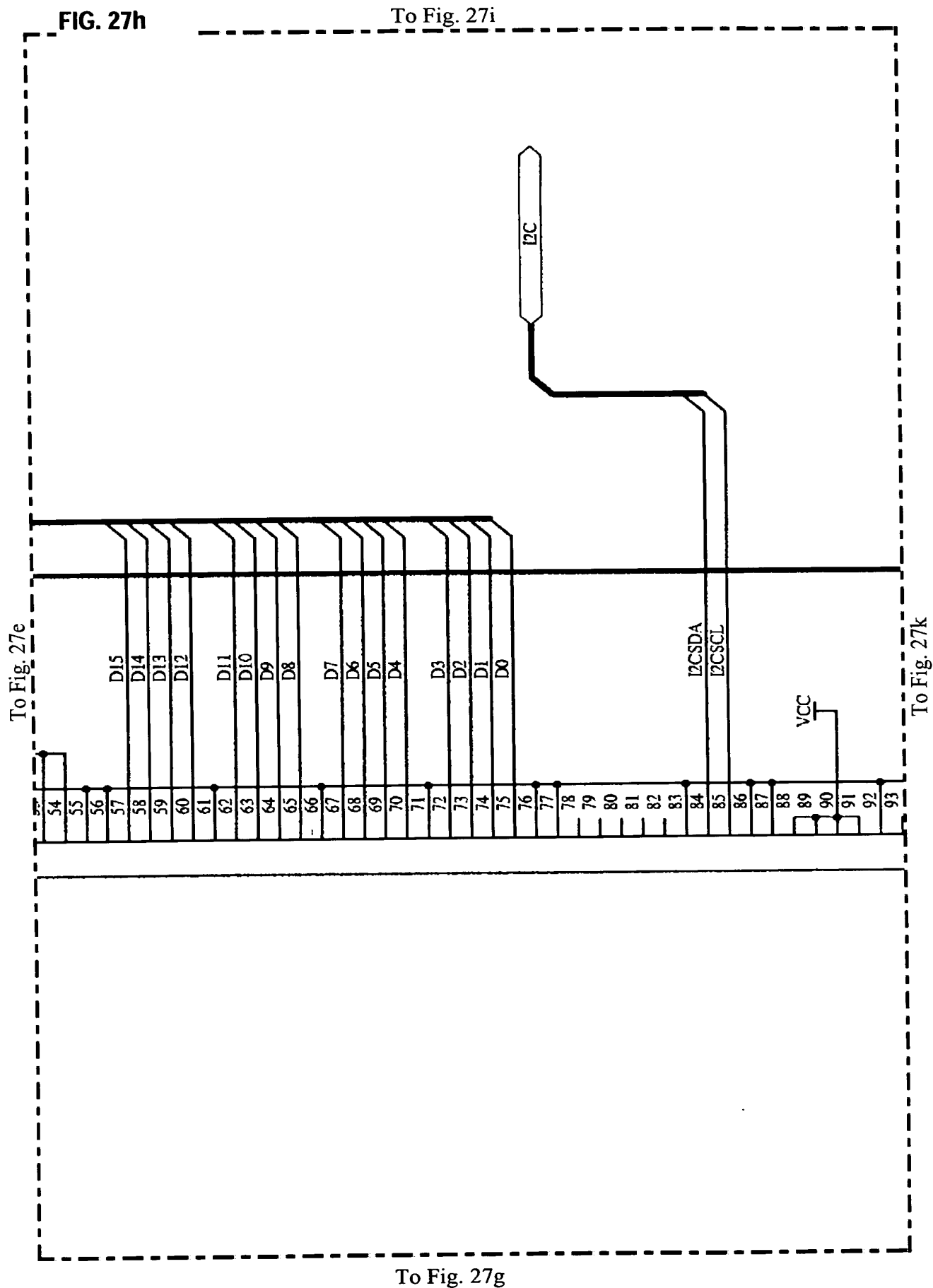


FIG. 27i

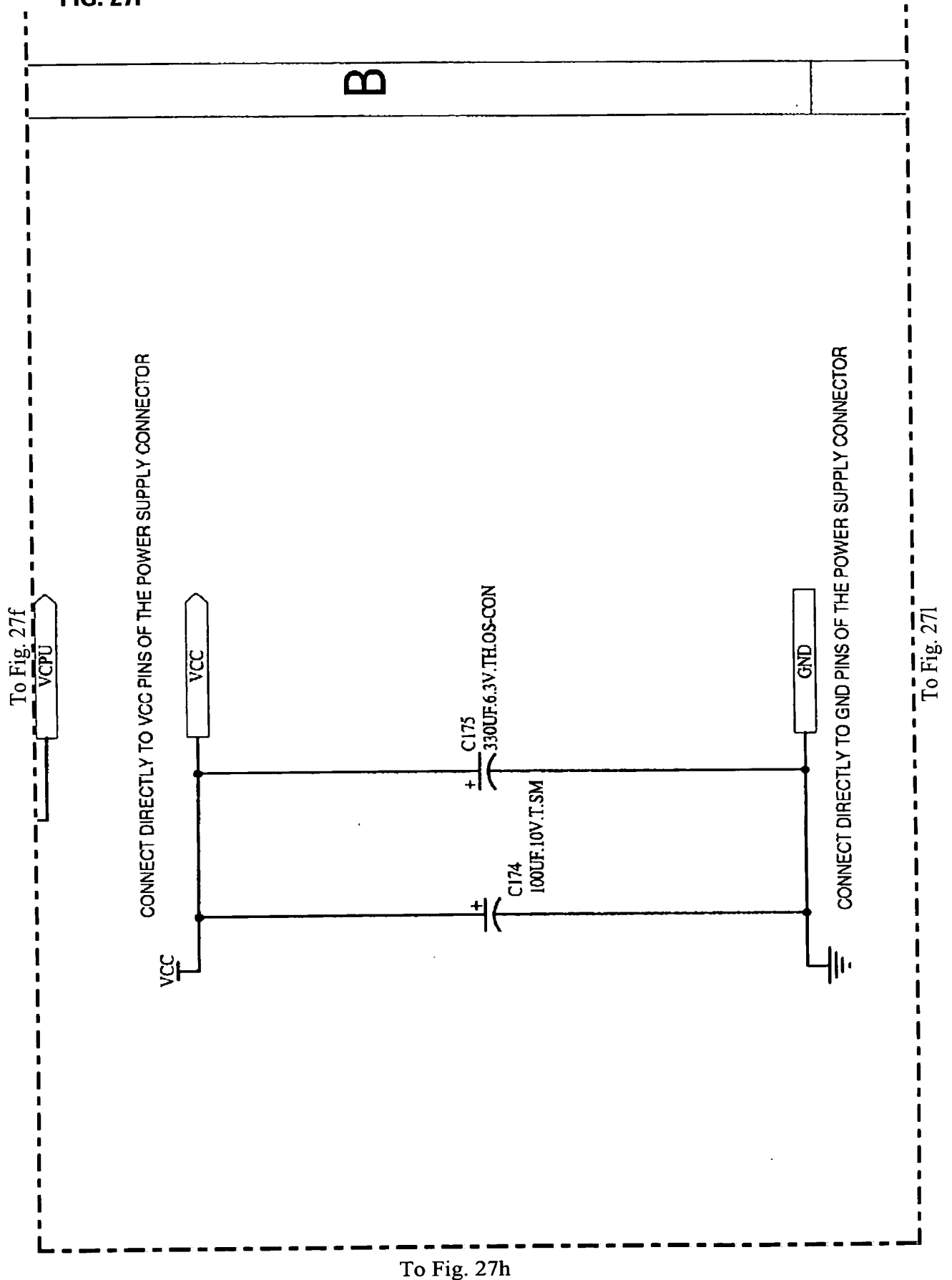
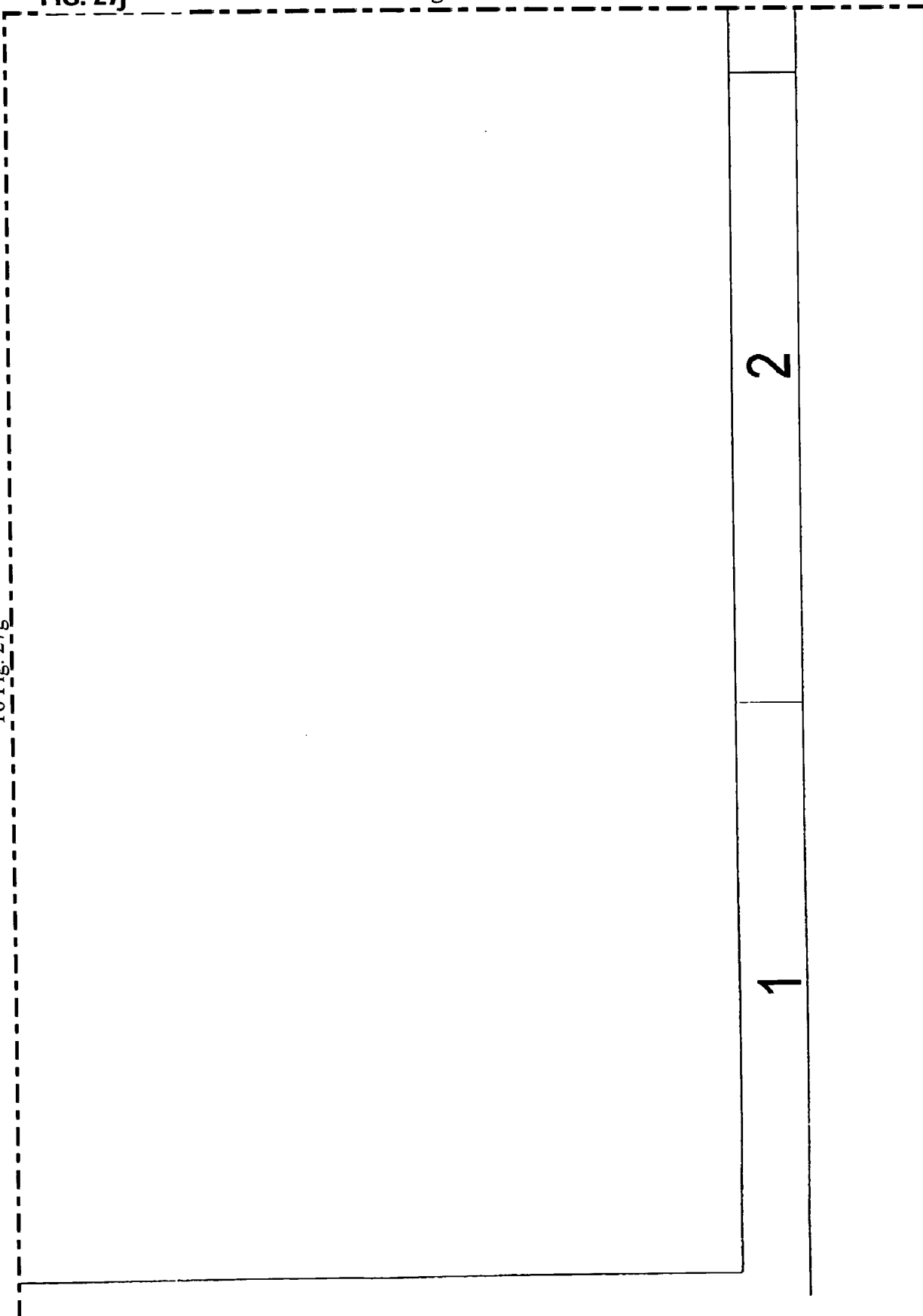


FIG. 27j

To Fig. 27k

To Fig. 27g



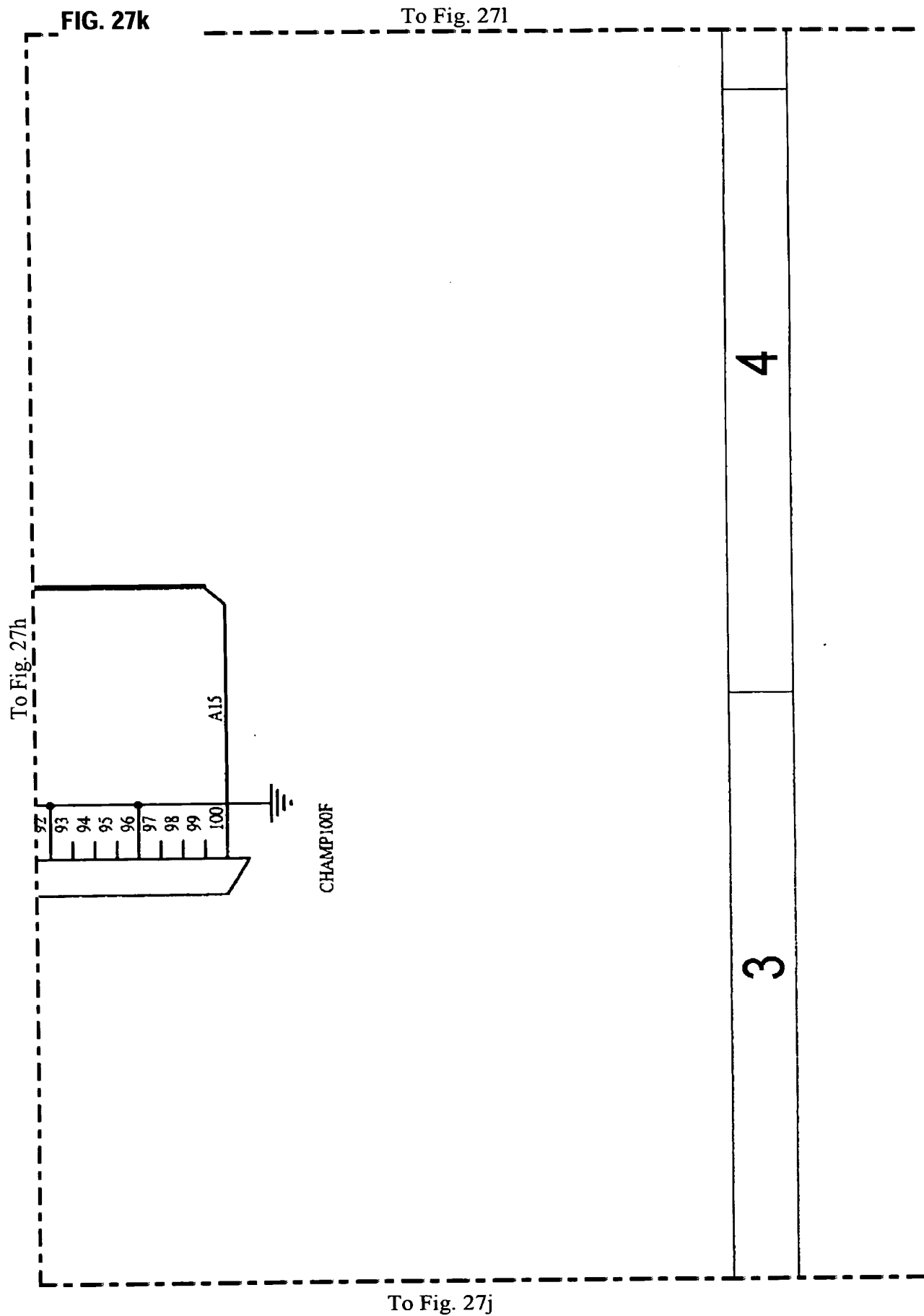


FIG. 27l

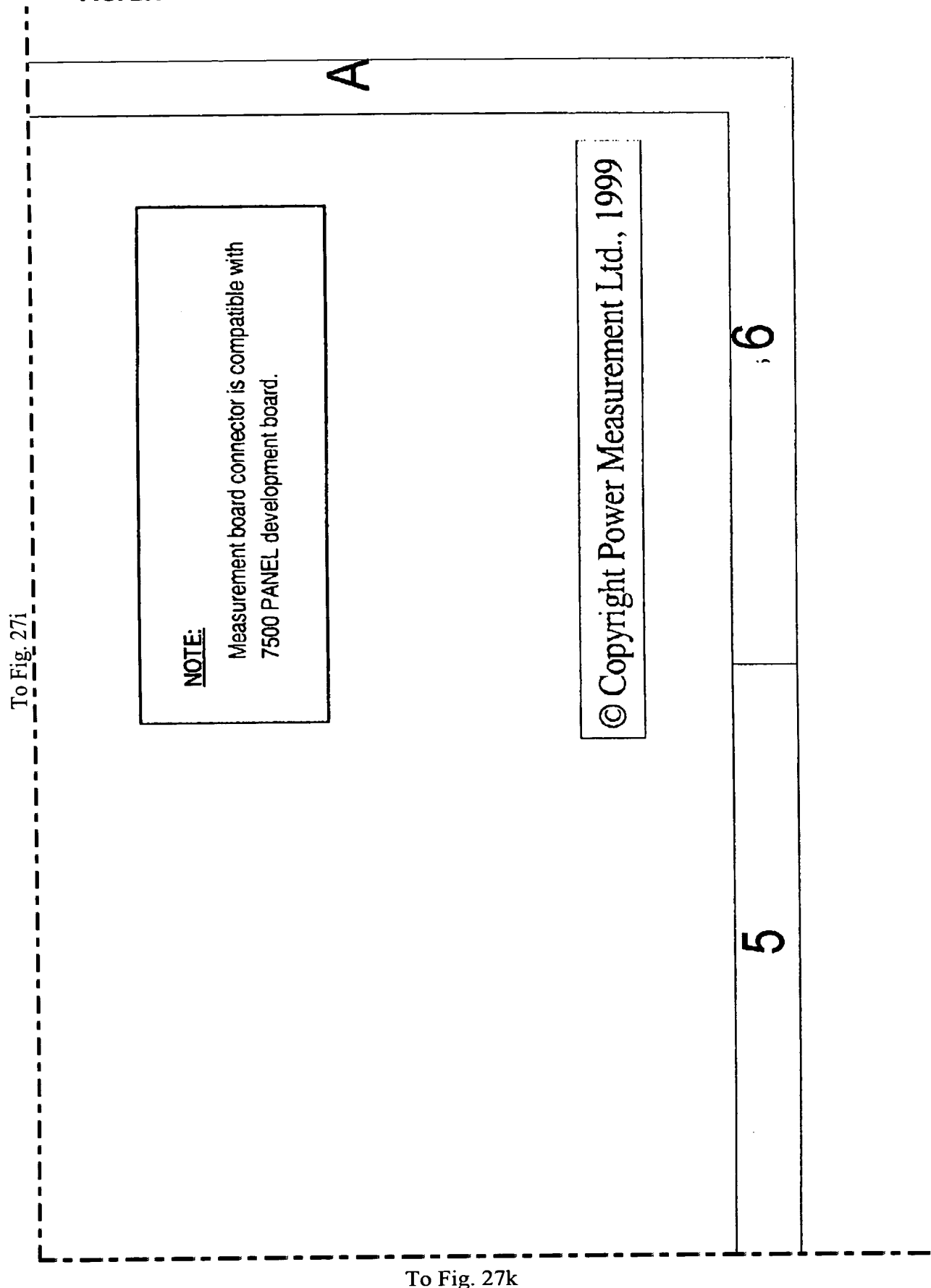
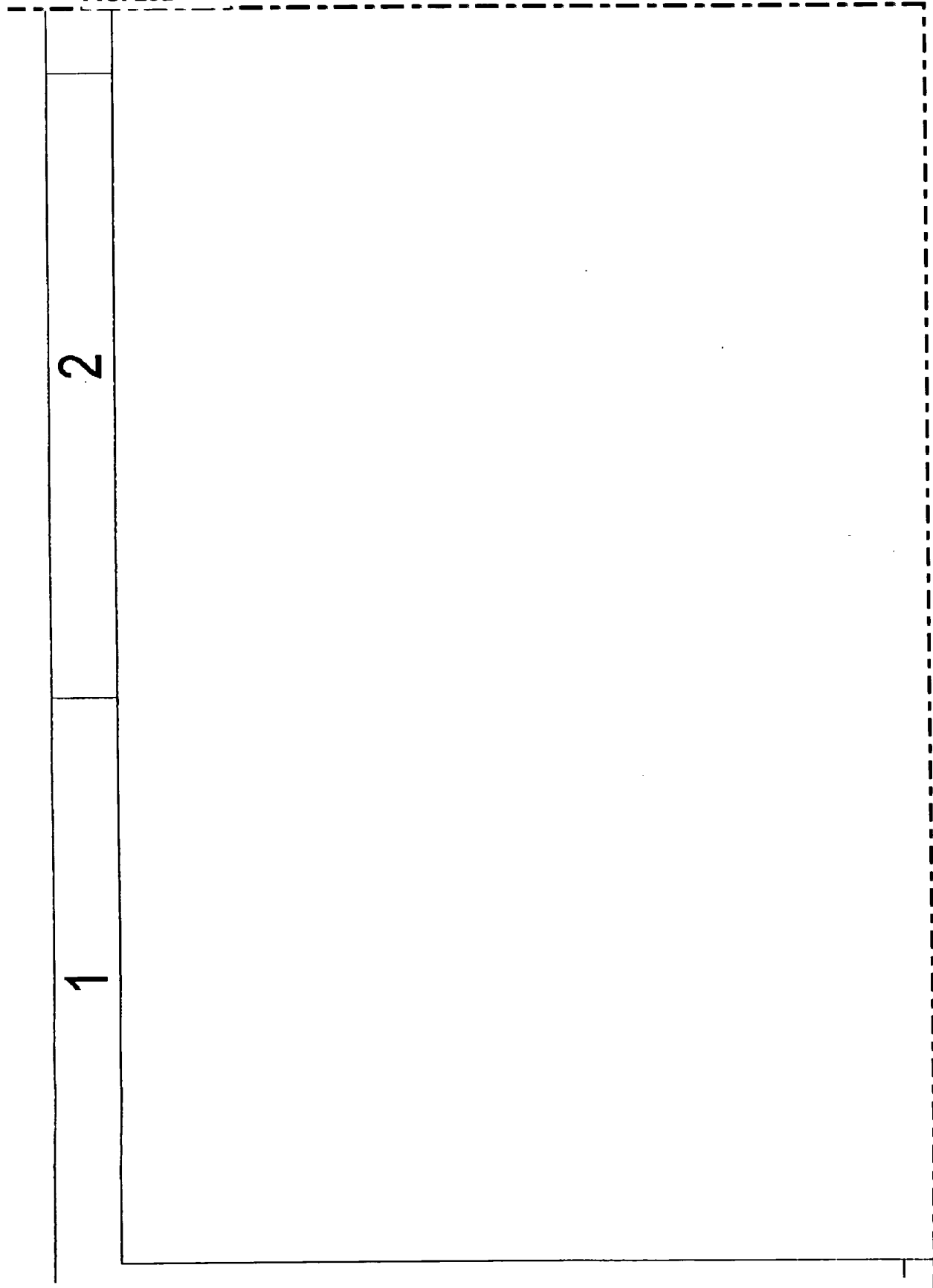


FIG. 28a

To Fig. 28b



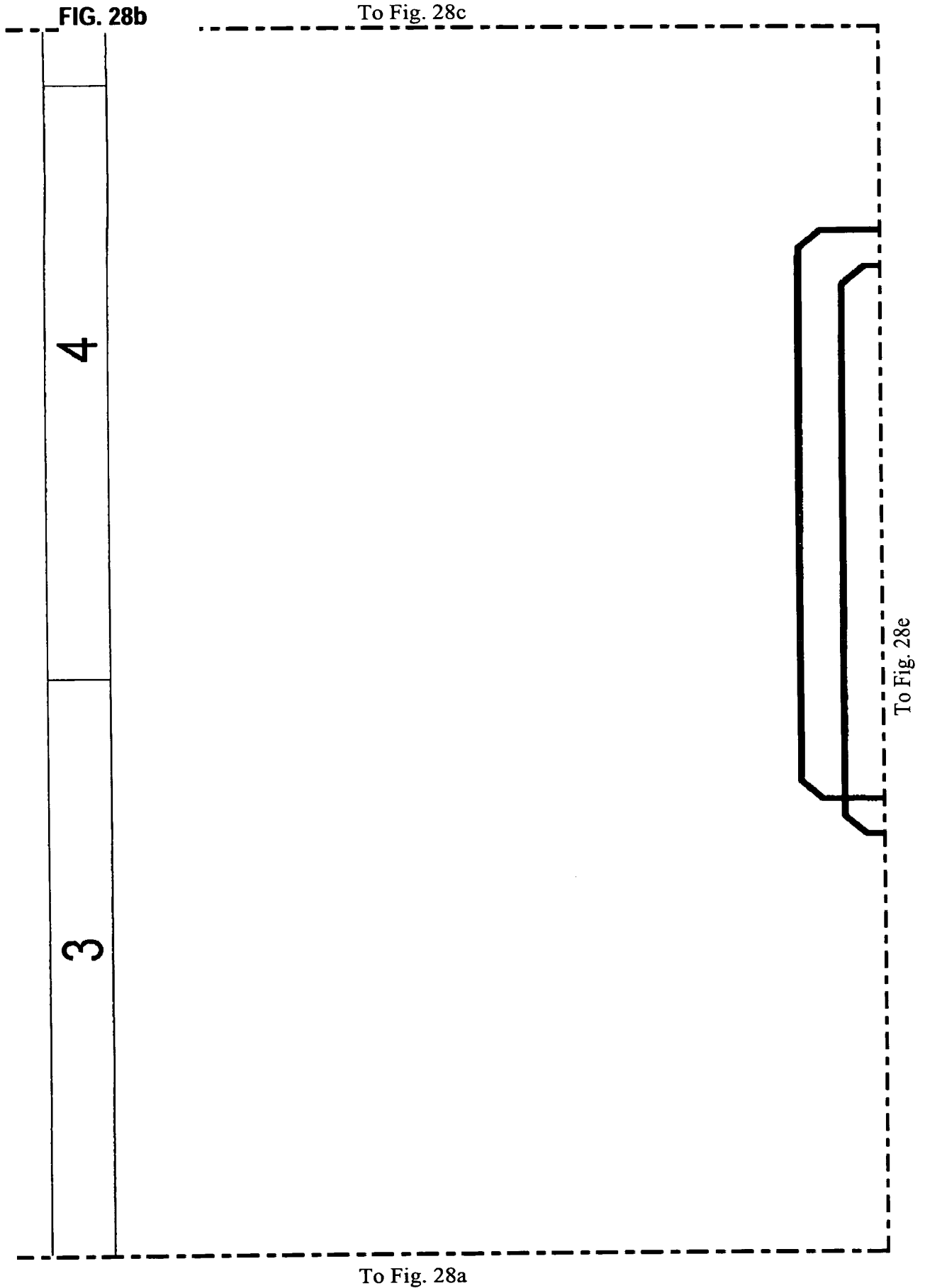


FIG. 28c

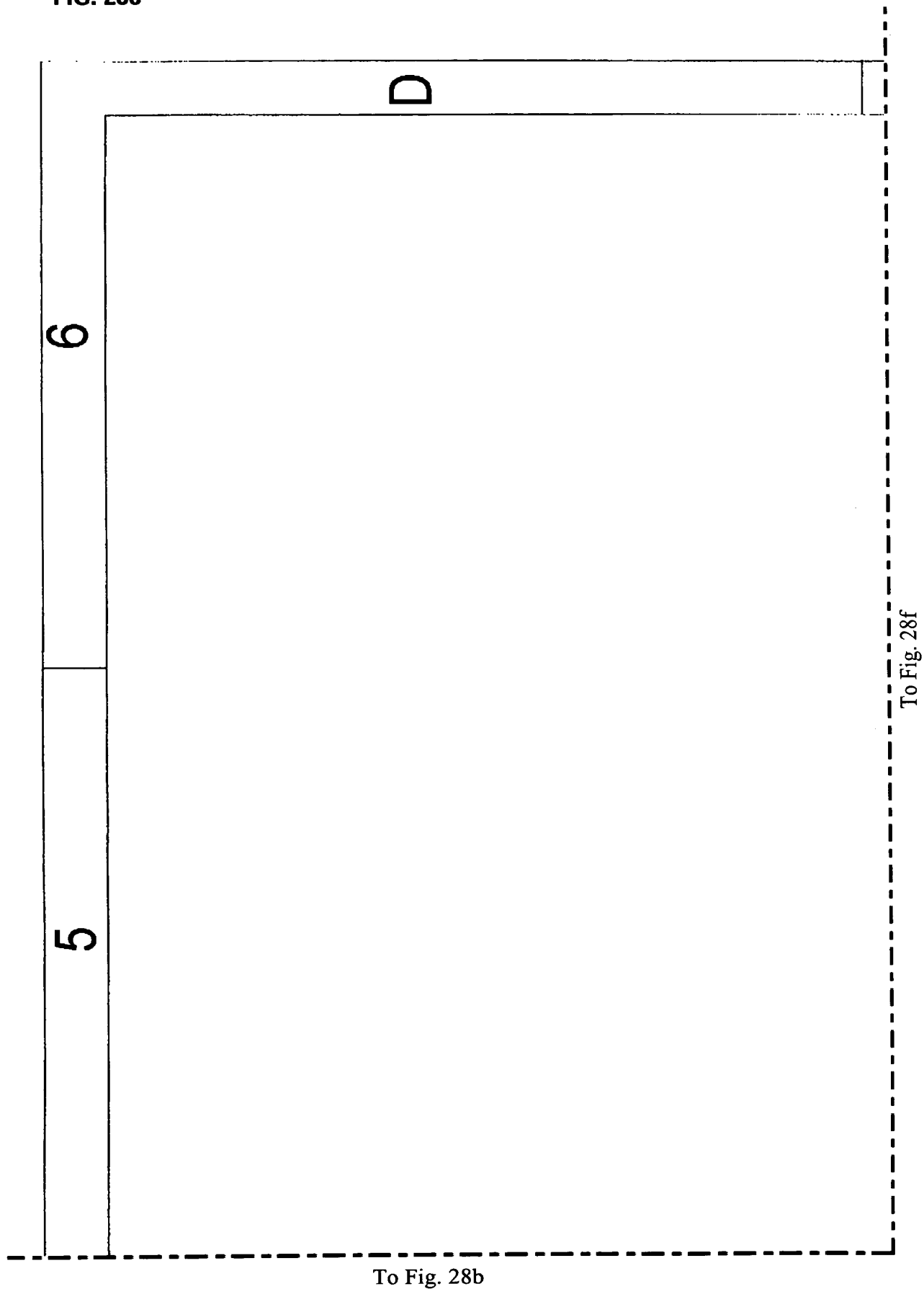


FIG. 28d

To Fig. 28e

S-053C
S-053C

To Fig. 28a

To Fig. 28g

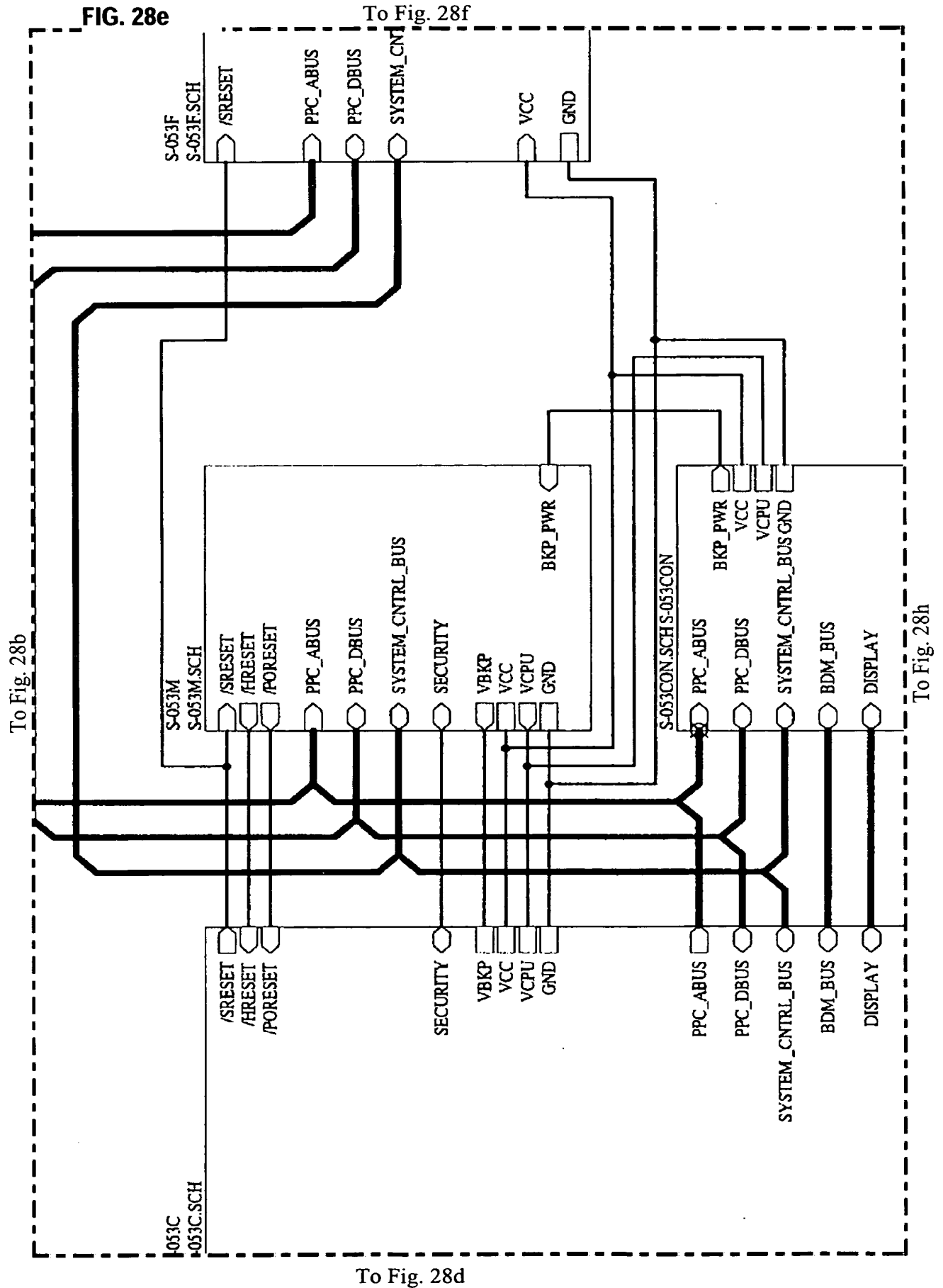


FIG. 28f

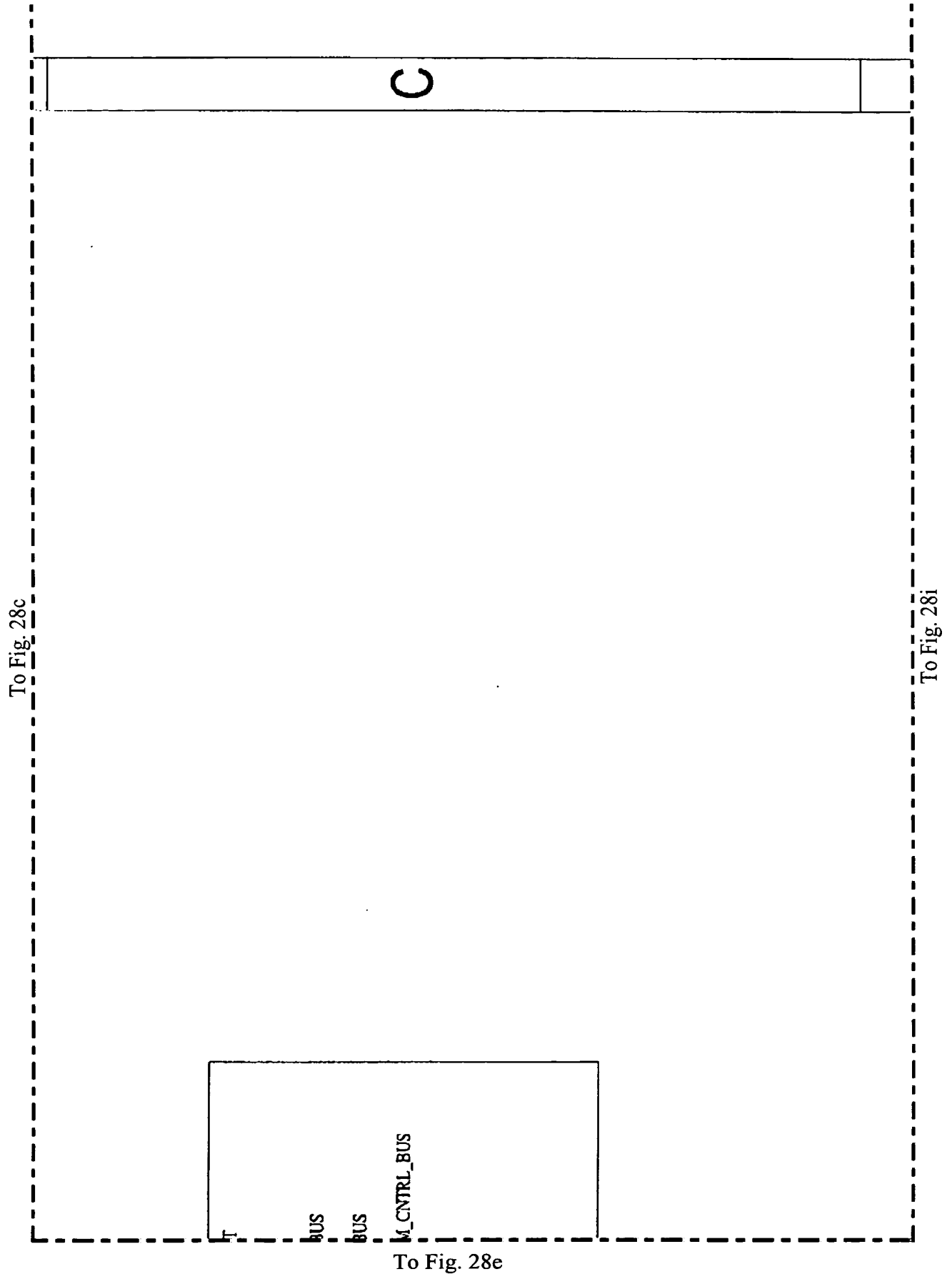
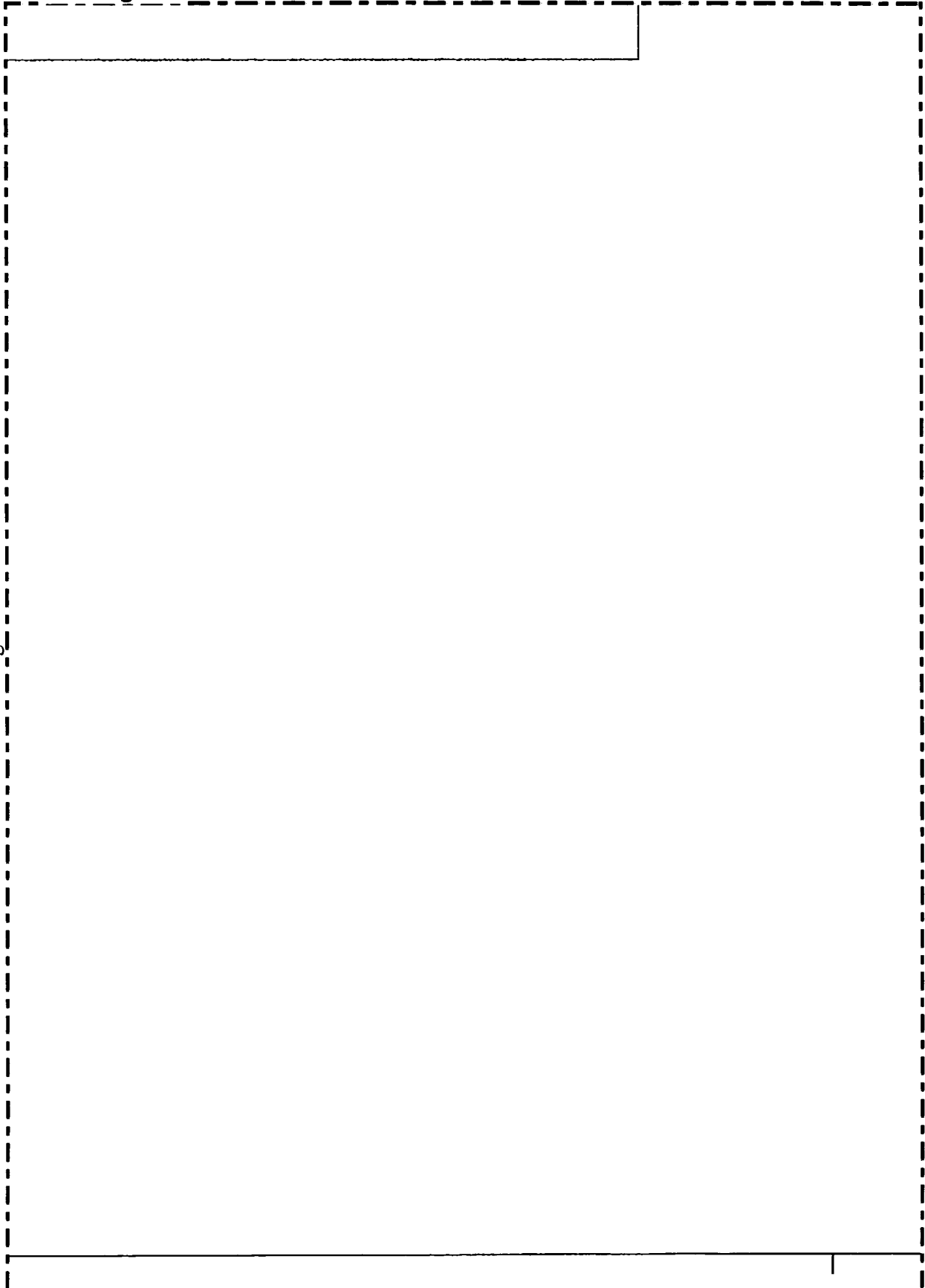


FIG. 28g

To Fig. 28h

To Fig. 28d

To Fig. 28j



To Fig. 28i

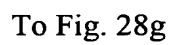


FIG. 28i

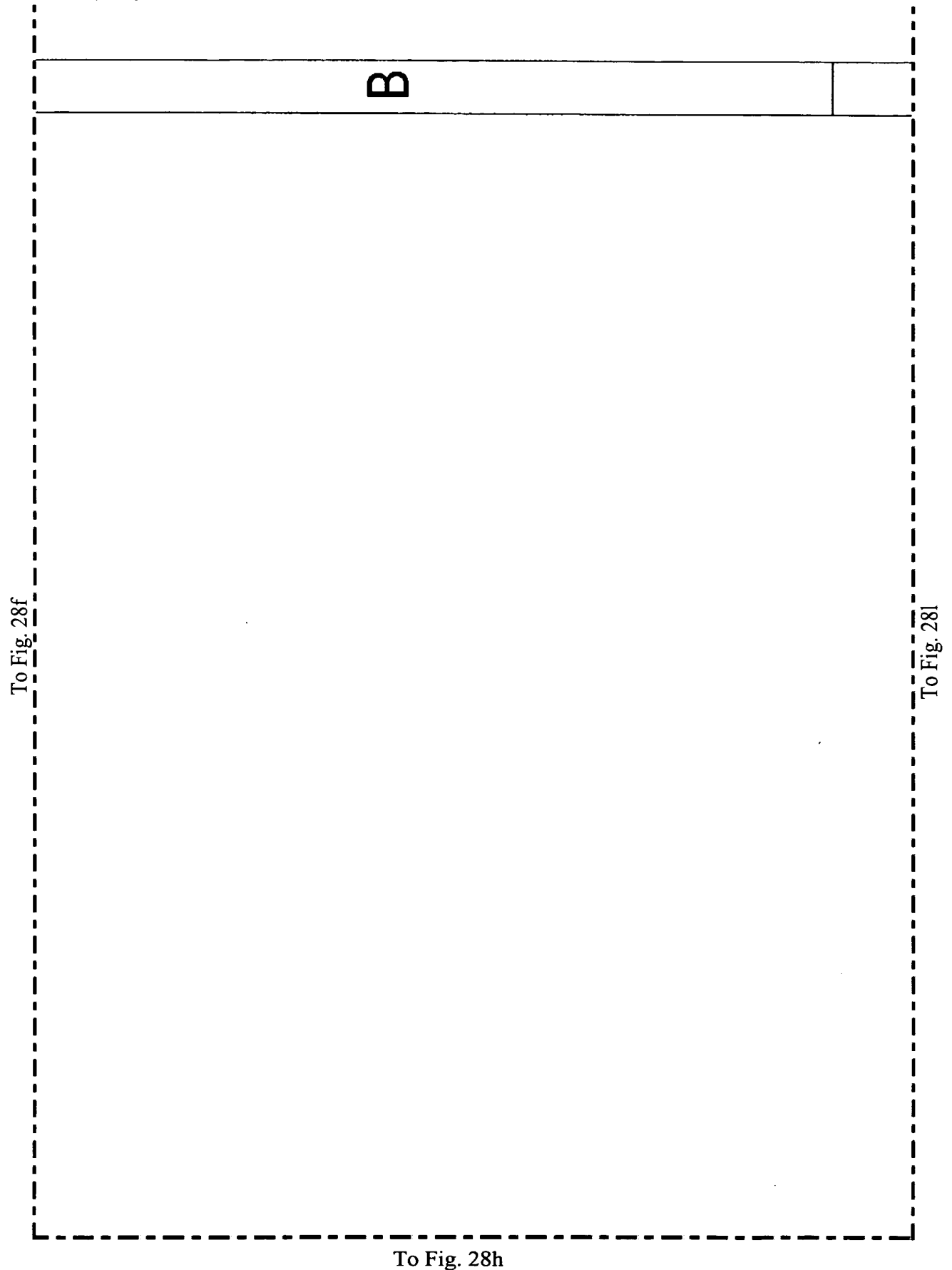


FIG. 28j

To Fig. 28k

To Fig. 28g

2

1

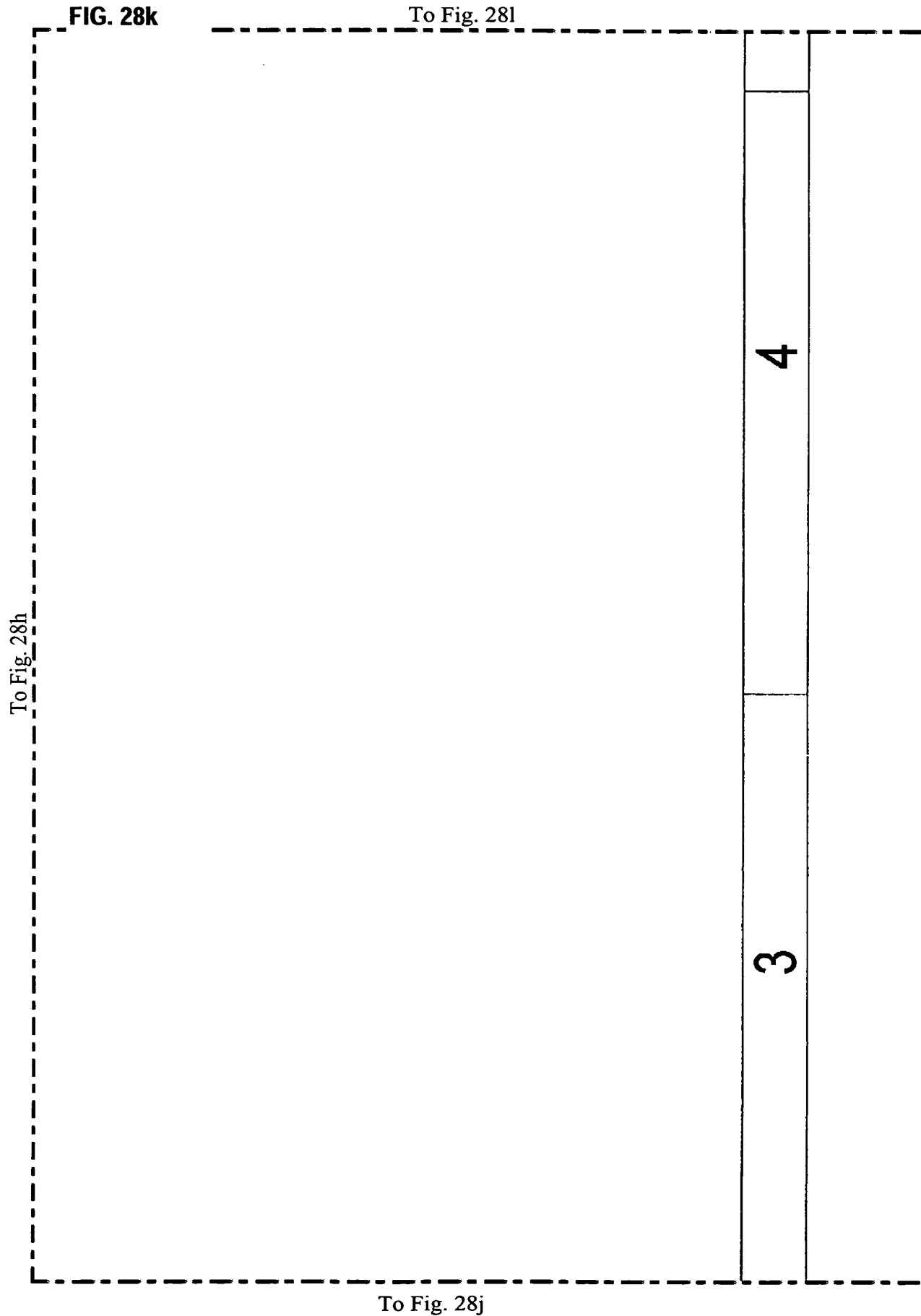


FIG. 28l

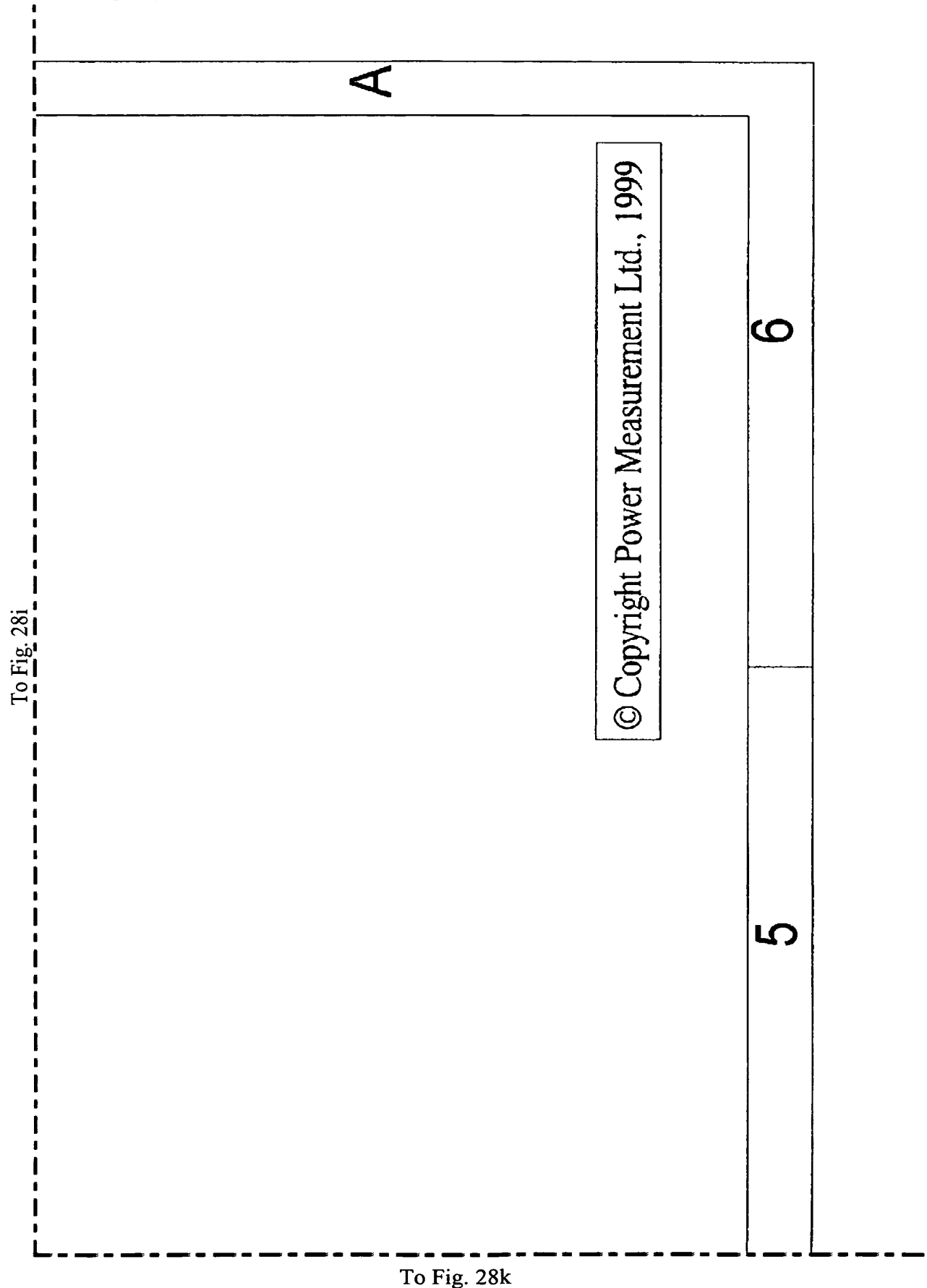
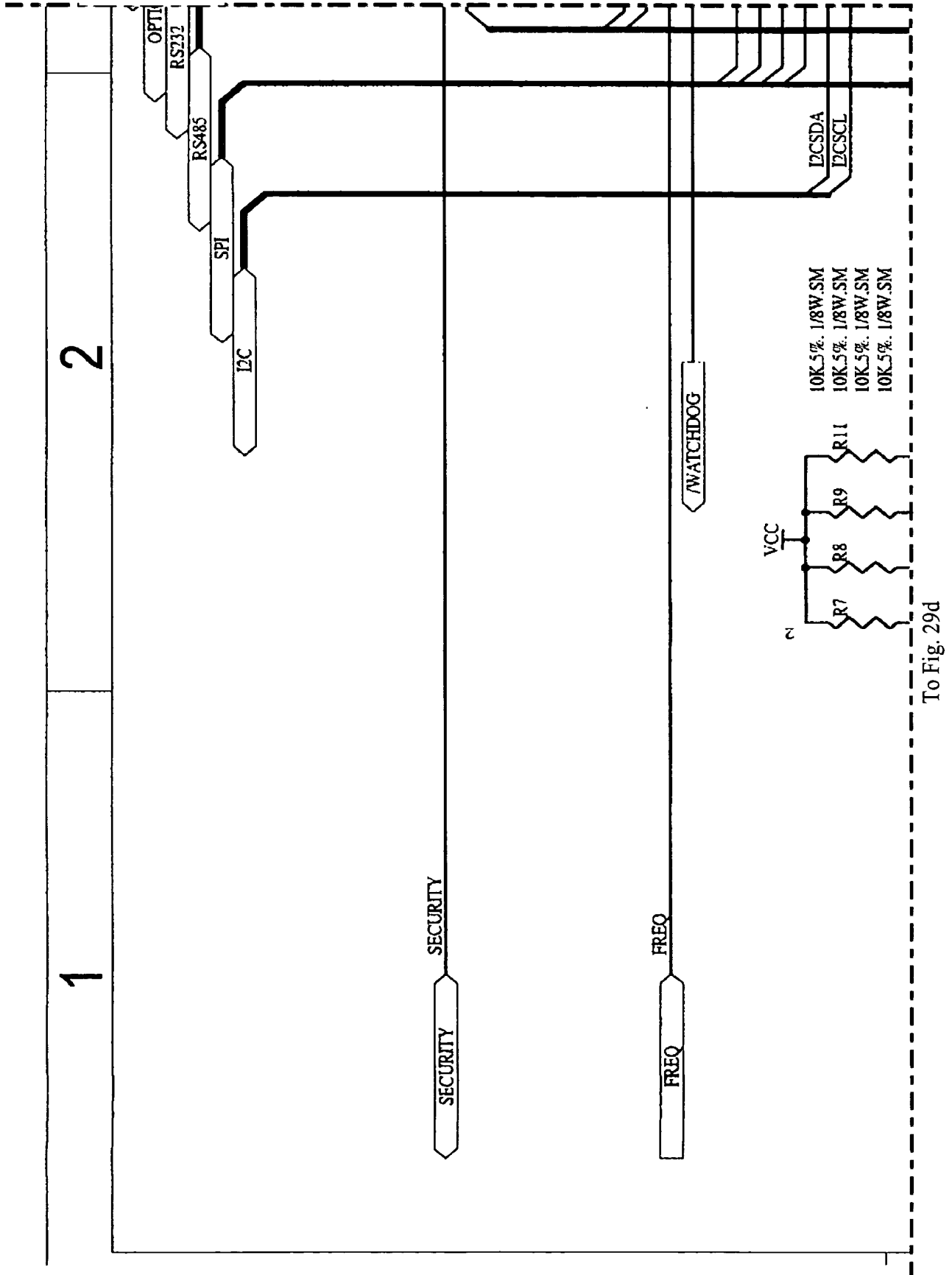


FIG. 29a

To Fig. 29b



To Fig. 29d

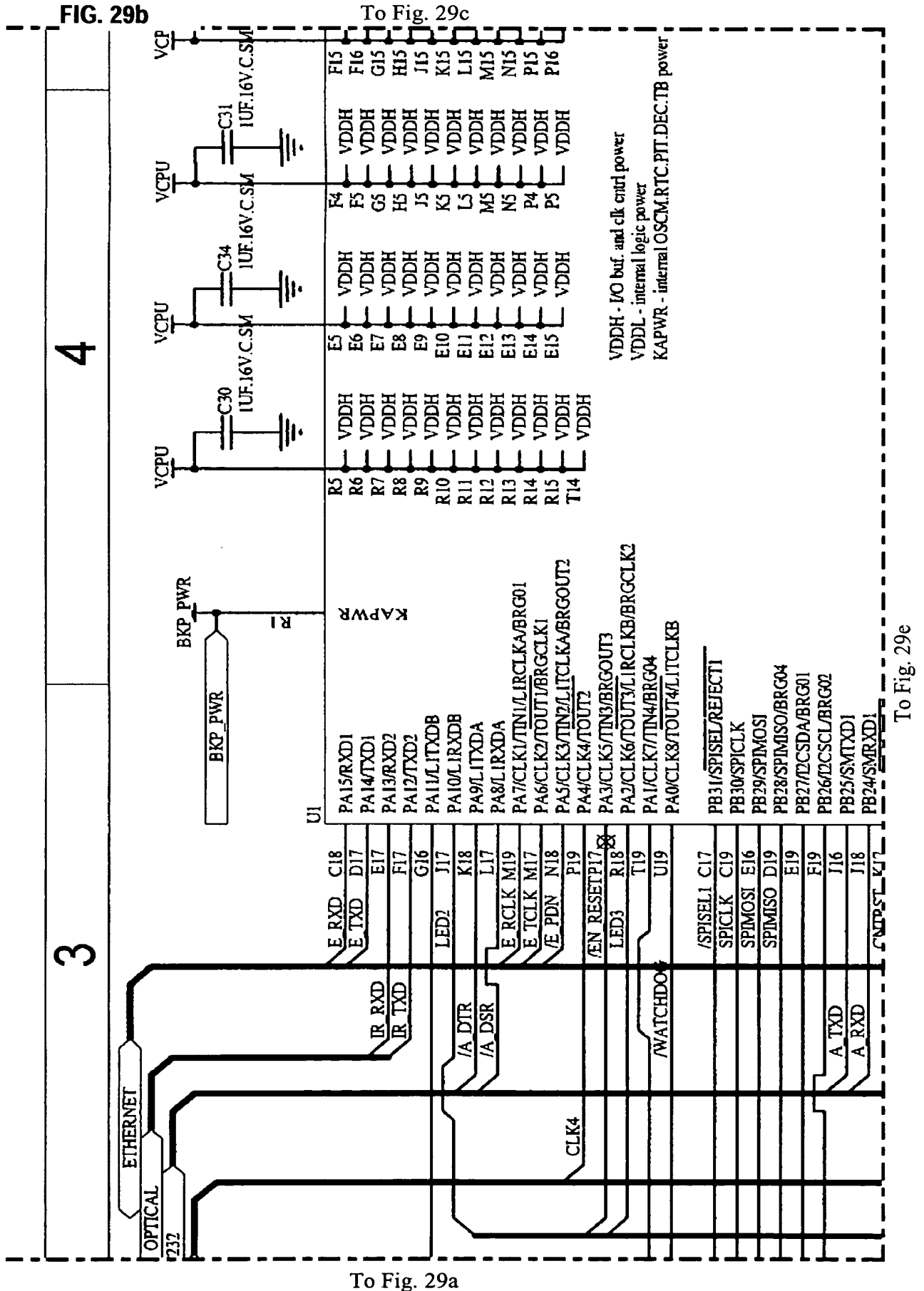
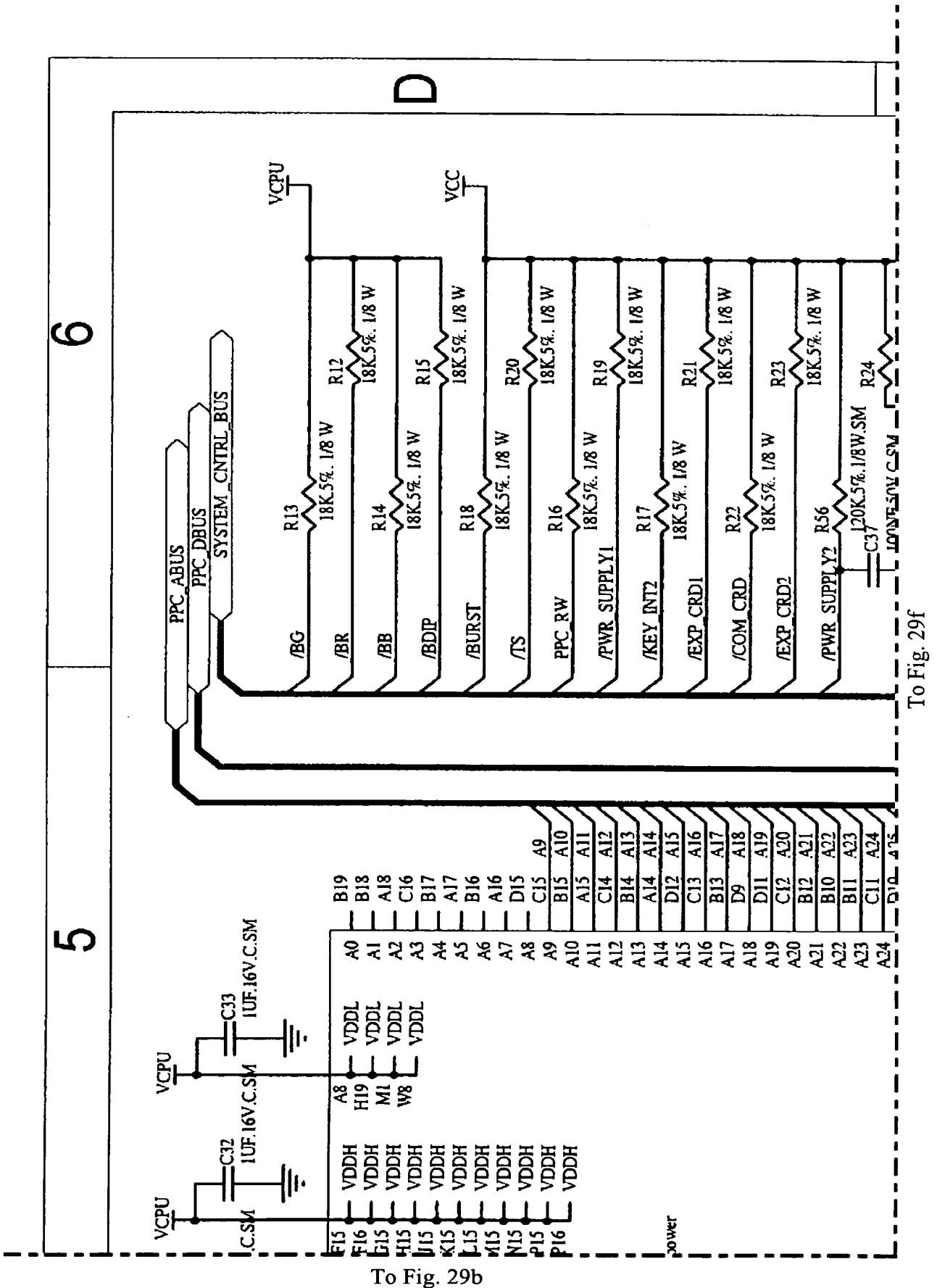


FIG. 29c



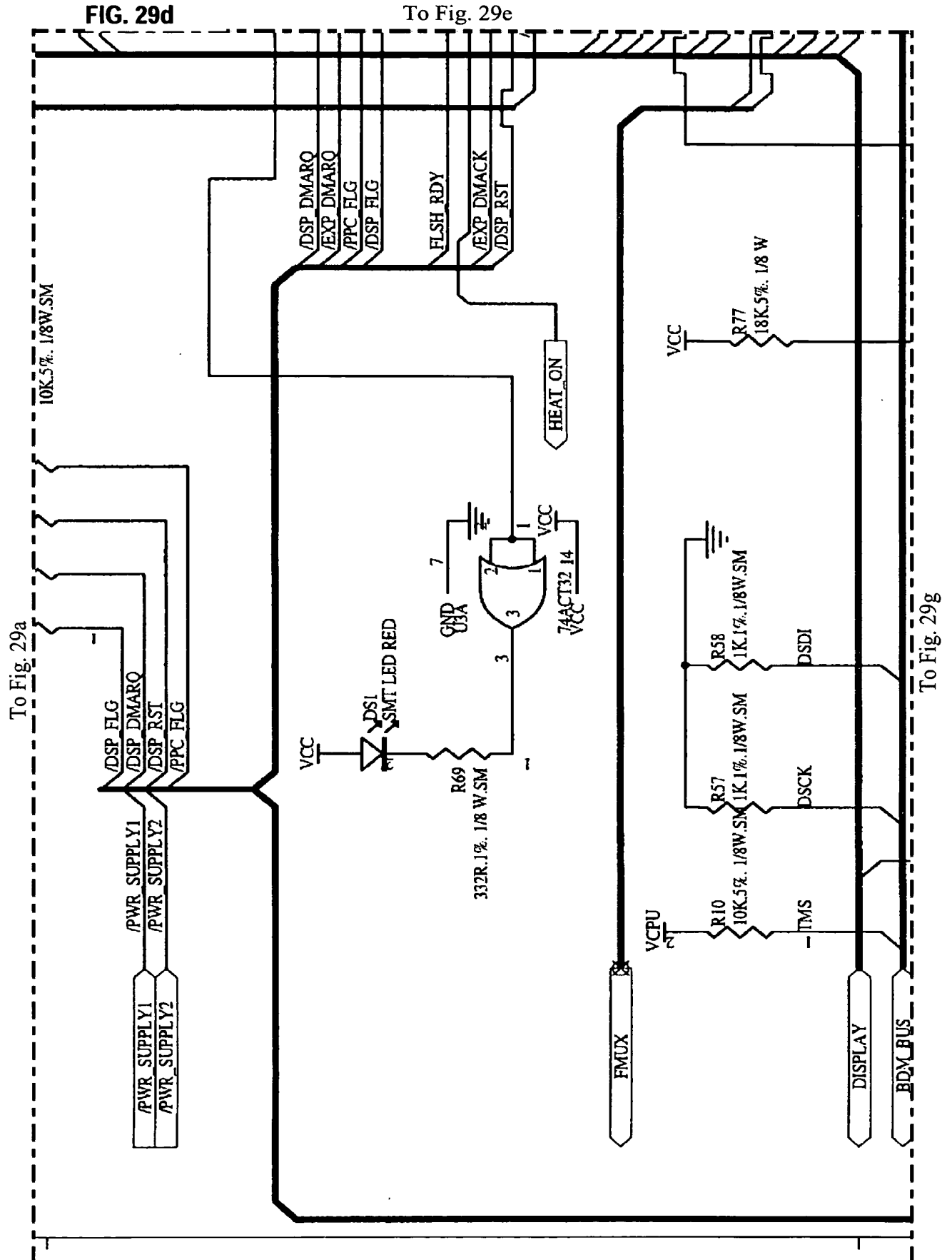


FIG. 29e

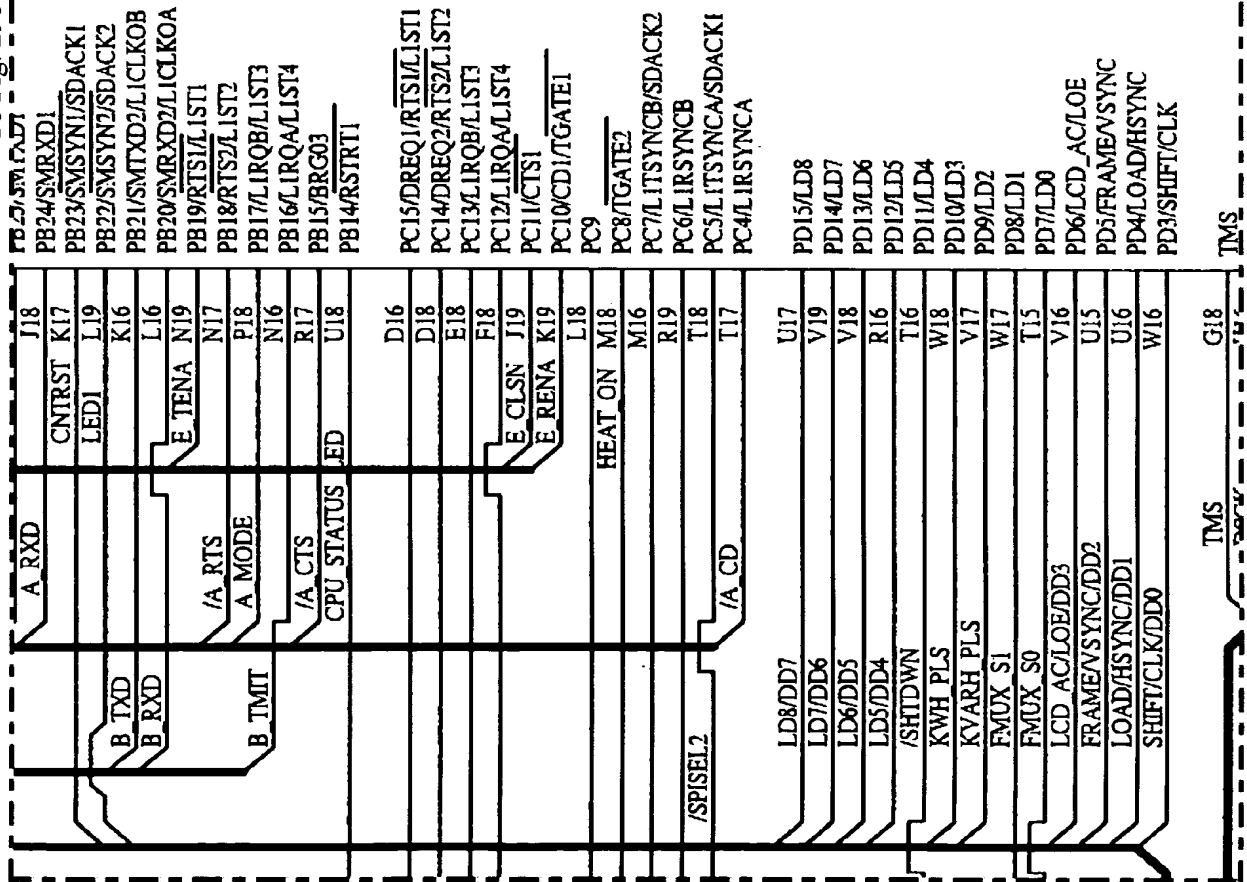
MPC821BGA

To Fig. 29f

D6 — N/C
D13 — N/C
D14 — N/C
U2 — N/C
V2 — N/C

F6 — GND
F7 — GND
F8 — GND
F9 — GND
F10 — GND
F11 — GND
F12 — GND
F13 — GND
F14 — GND
G6 — GND
G7 — GND
G8 — GND
G9 — GND
G10 — GND
G11 — GND
G12 — GND
G13 — GND
G14 — GND
H6 — GND
H7 — GND
H8 — GND
H9 — GND
H10 — GND

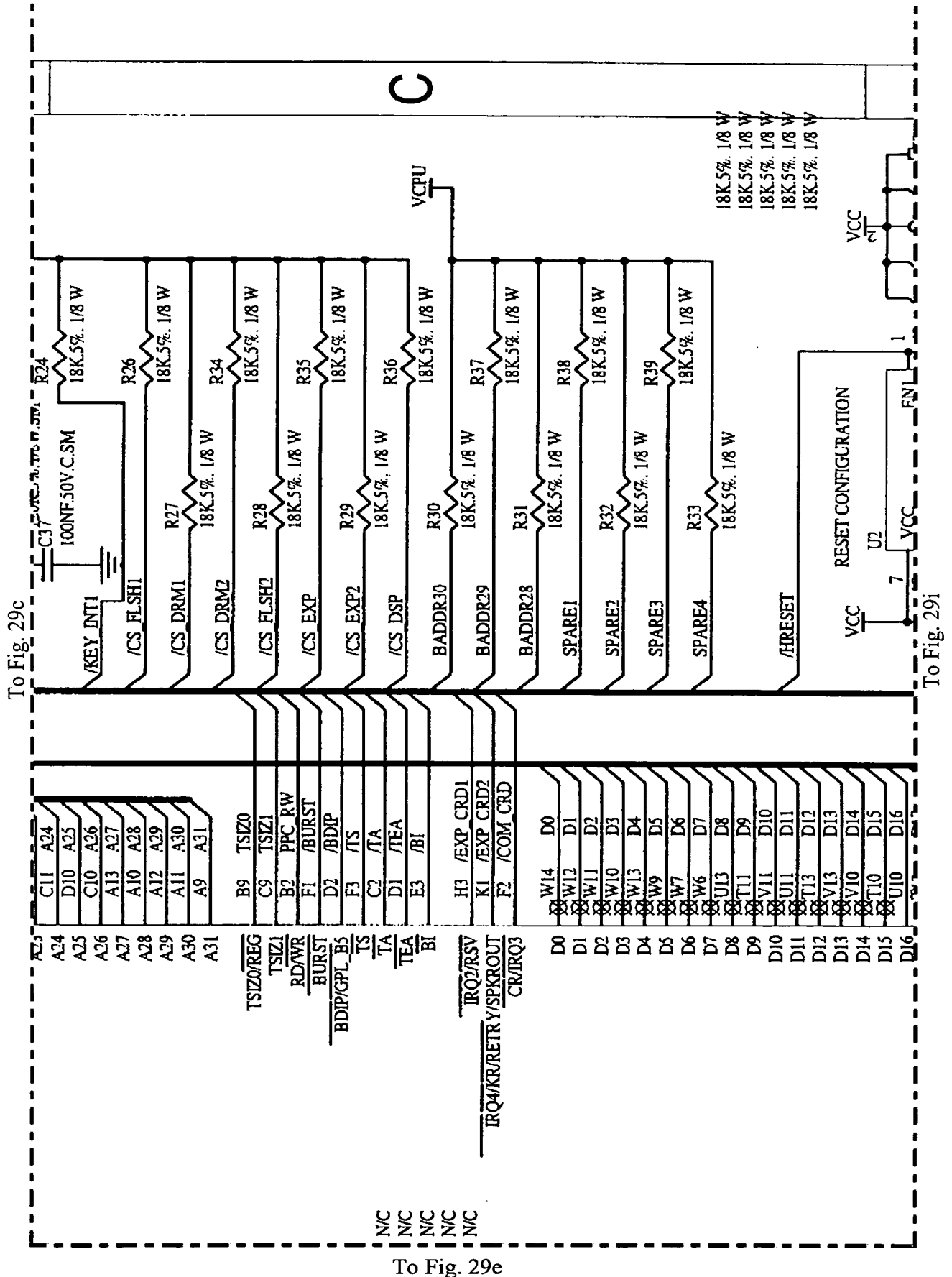
To Fig. 29b

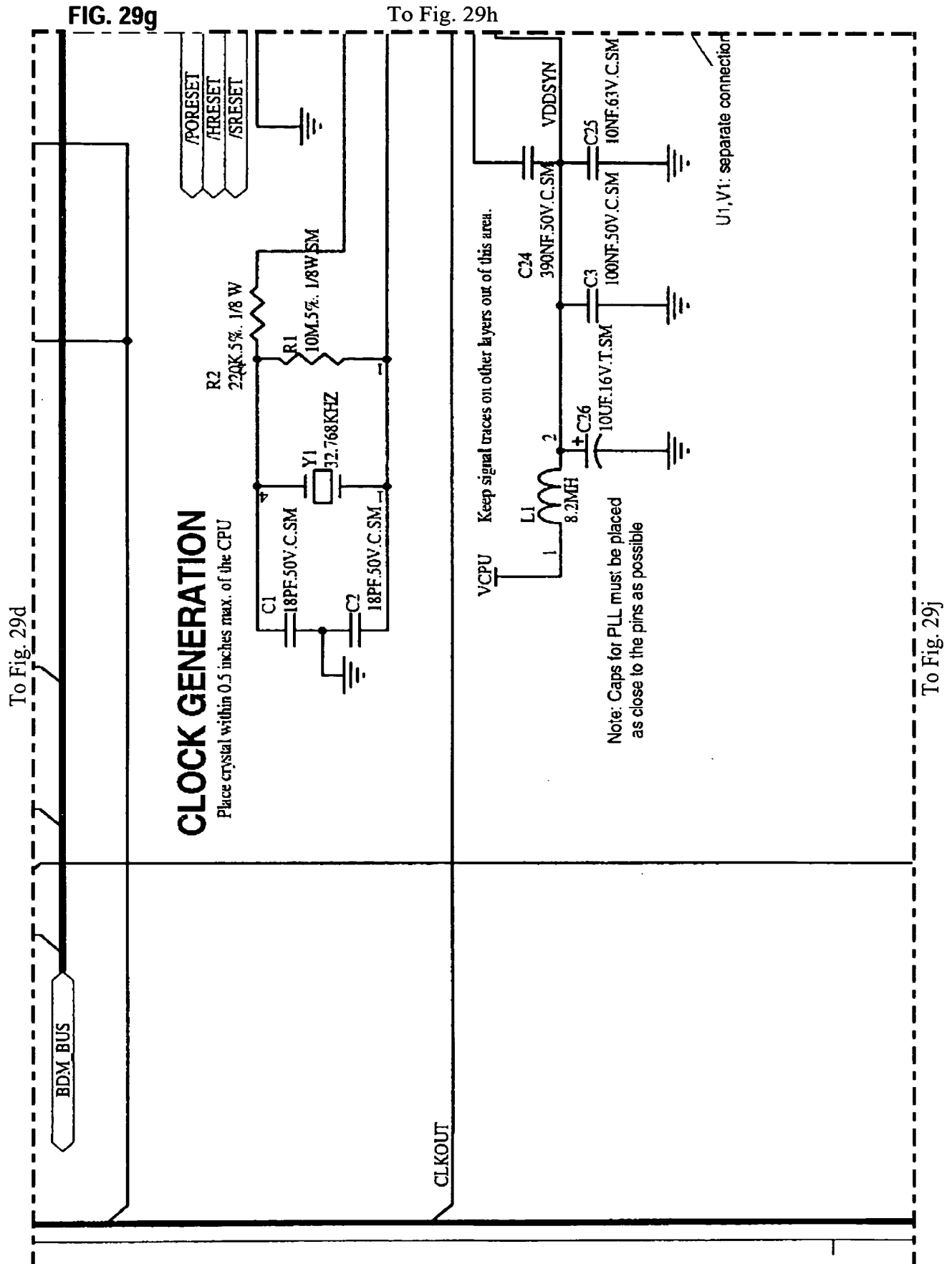


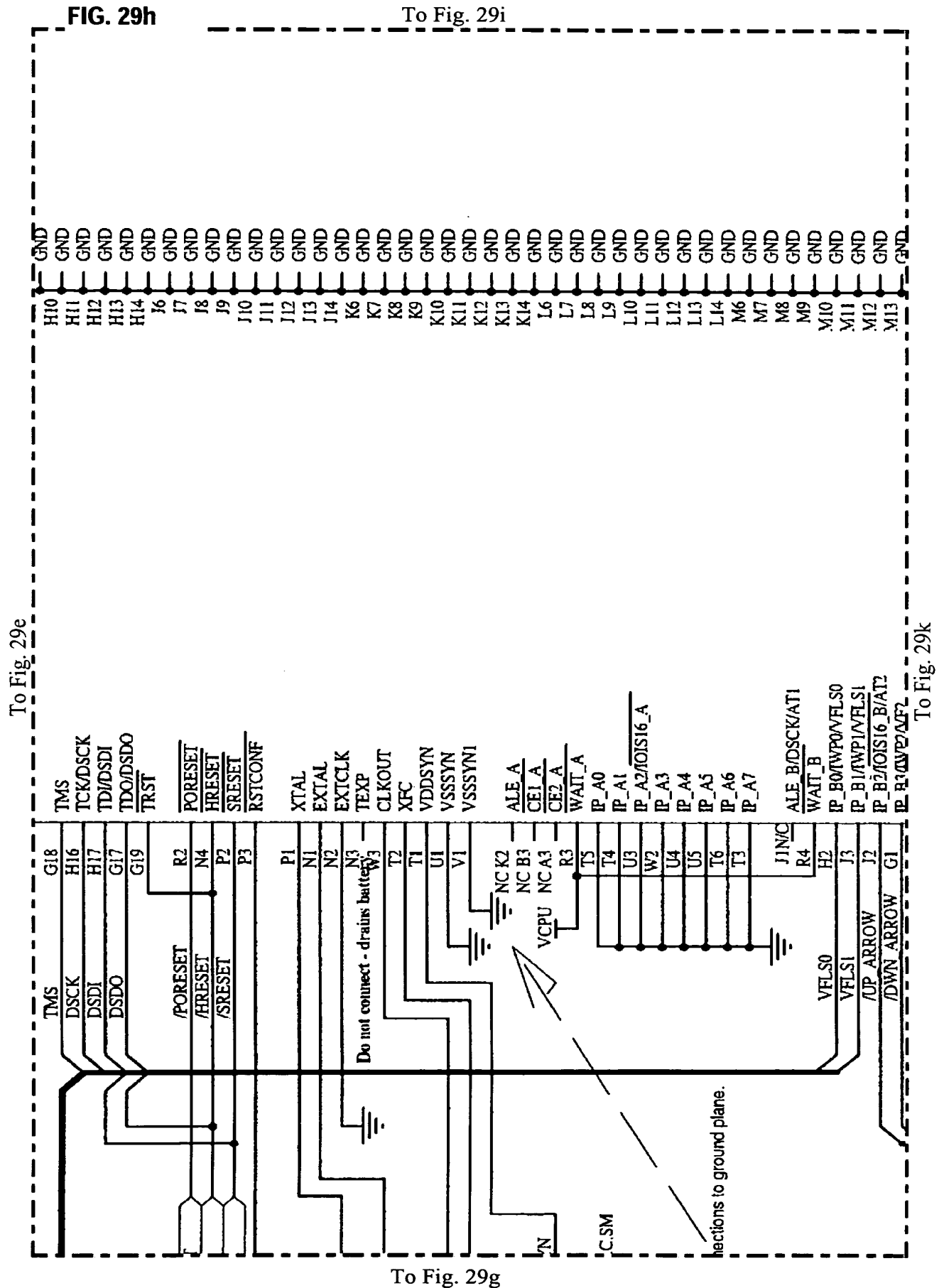
To Fig. 29d

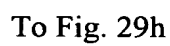
To Fig. 29h

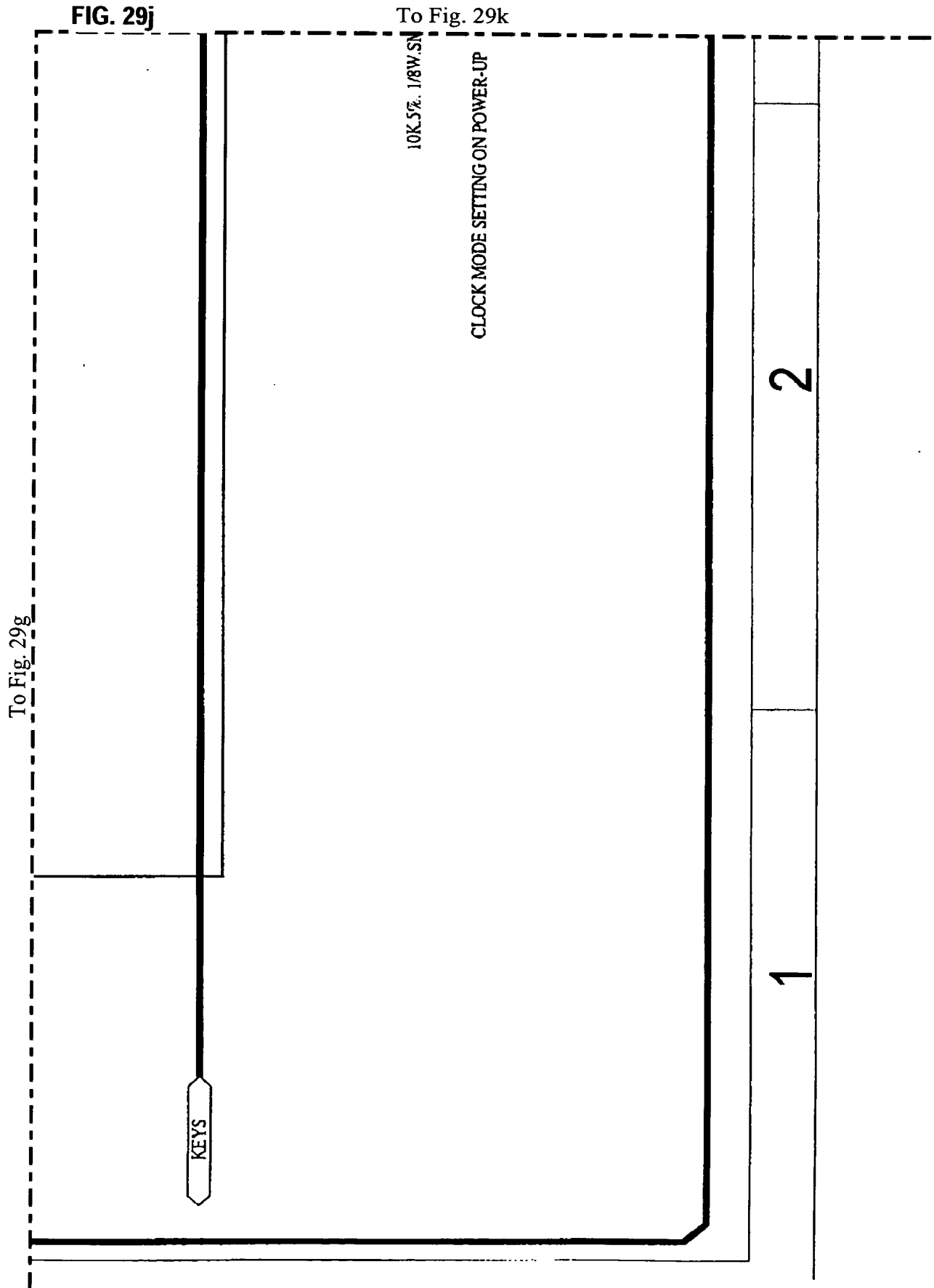
FIG. 29f











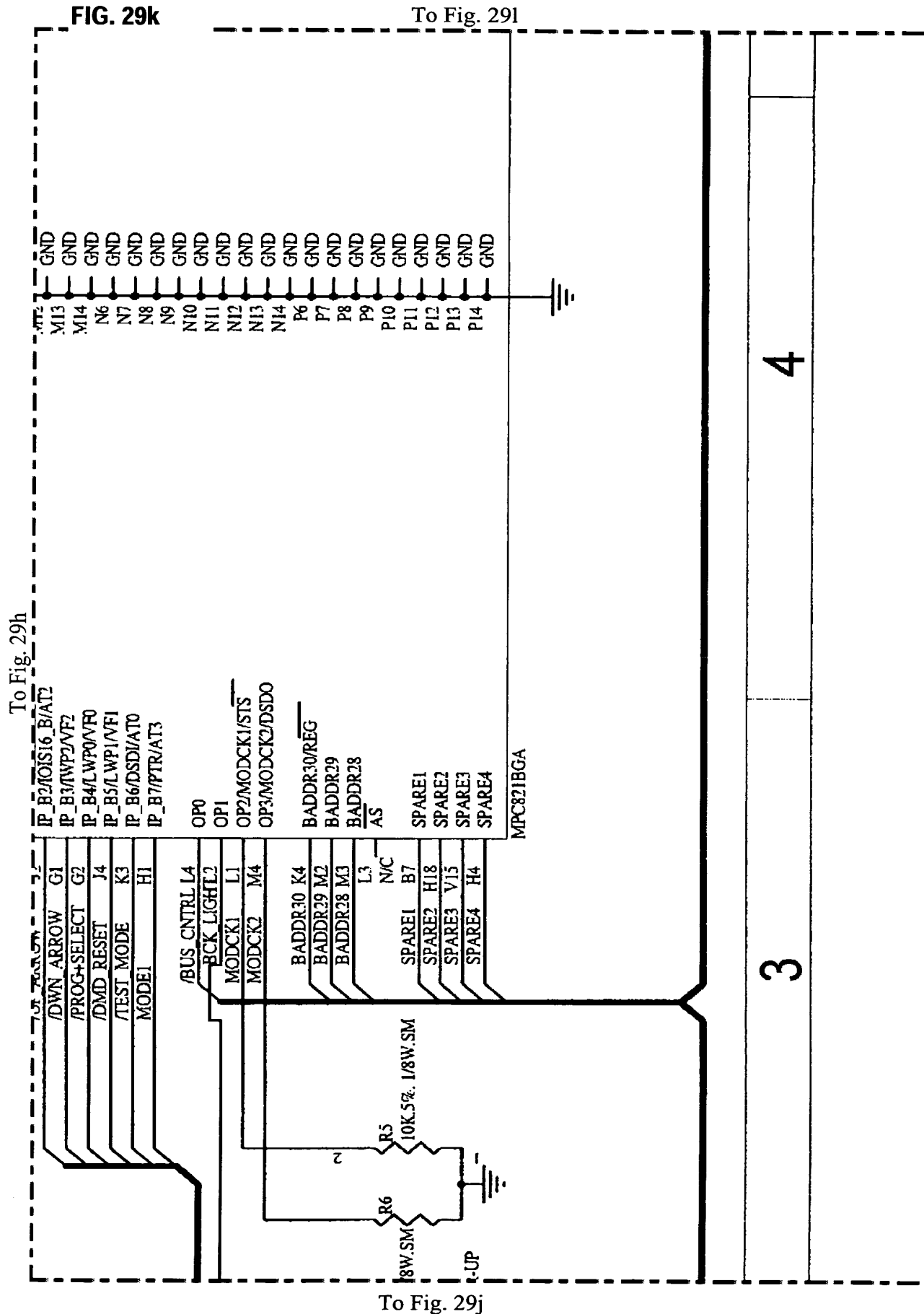
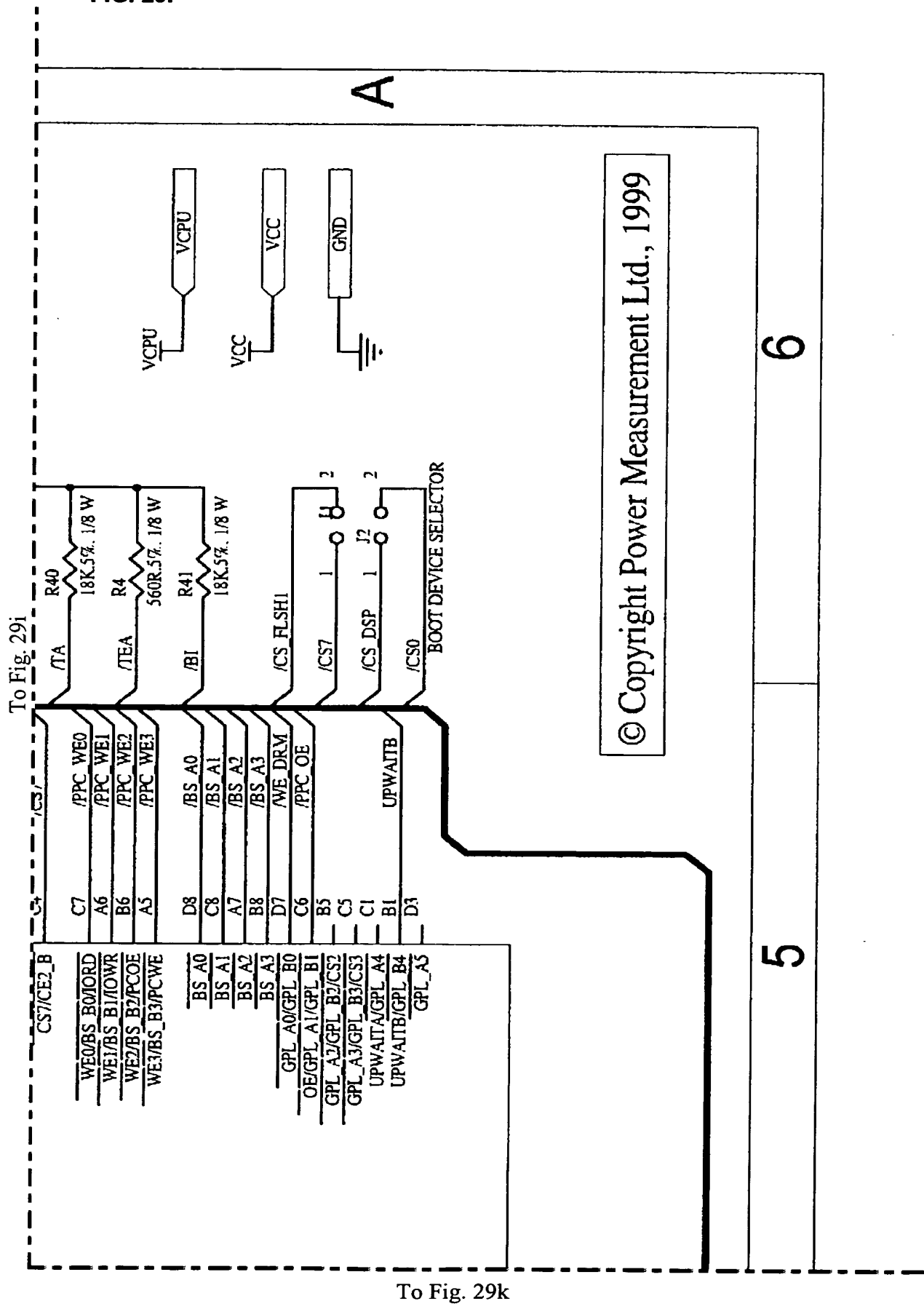
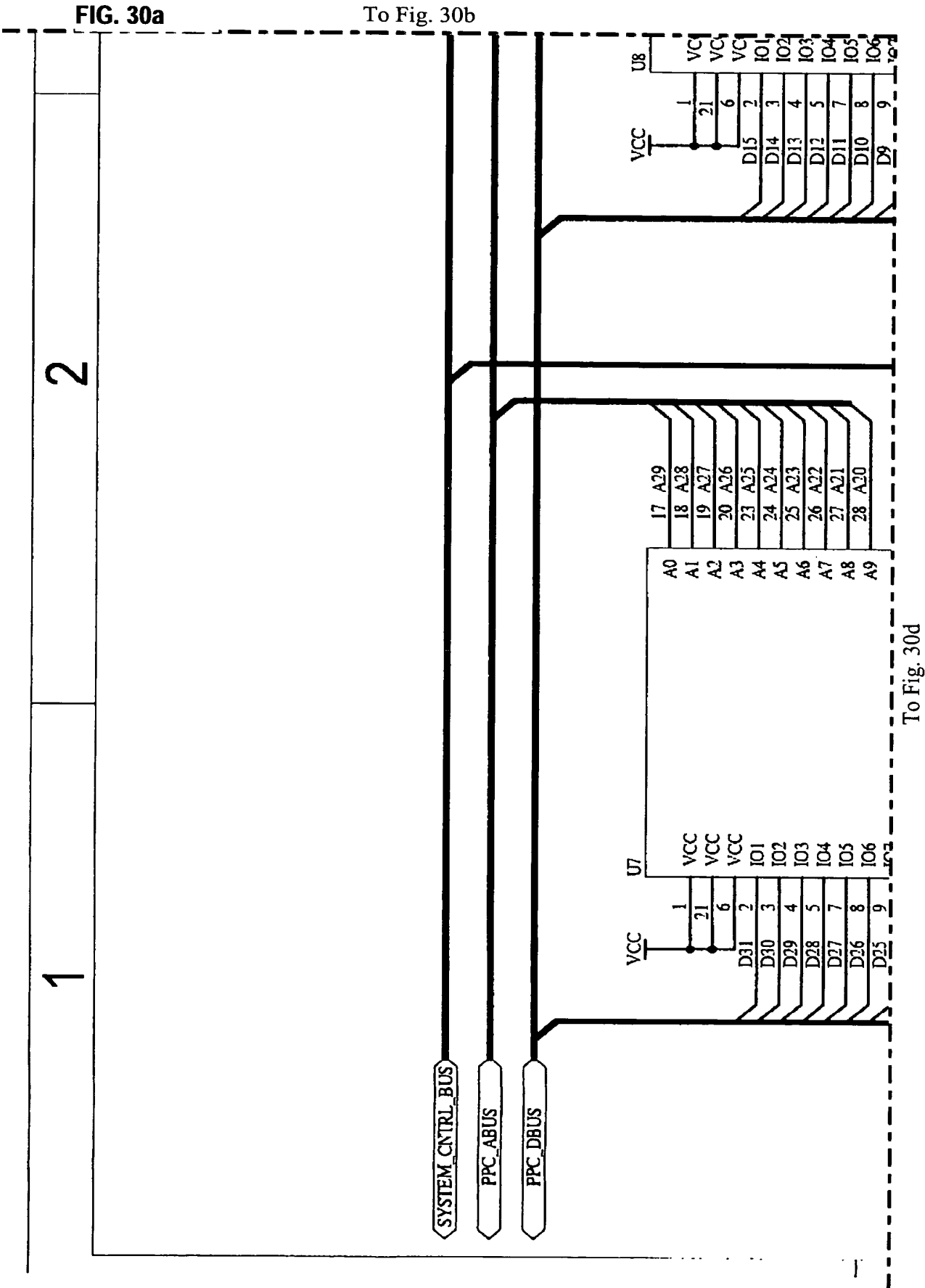
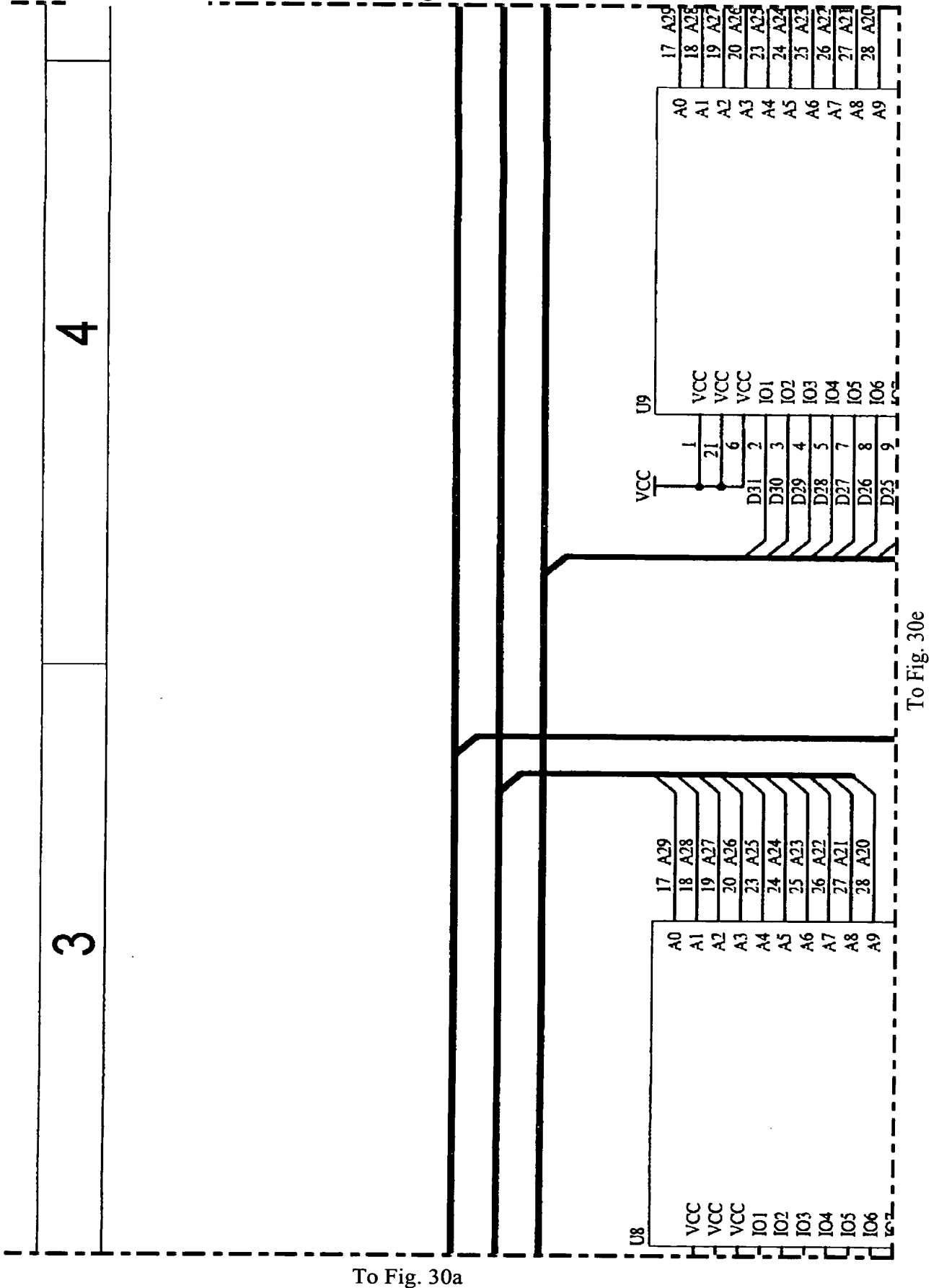


FIG. 29I



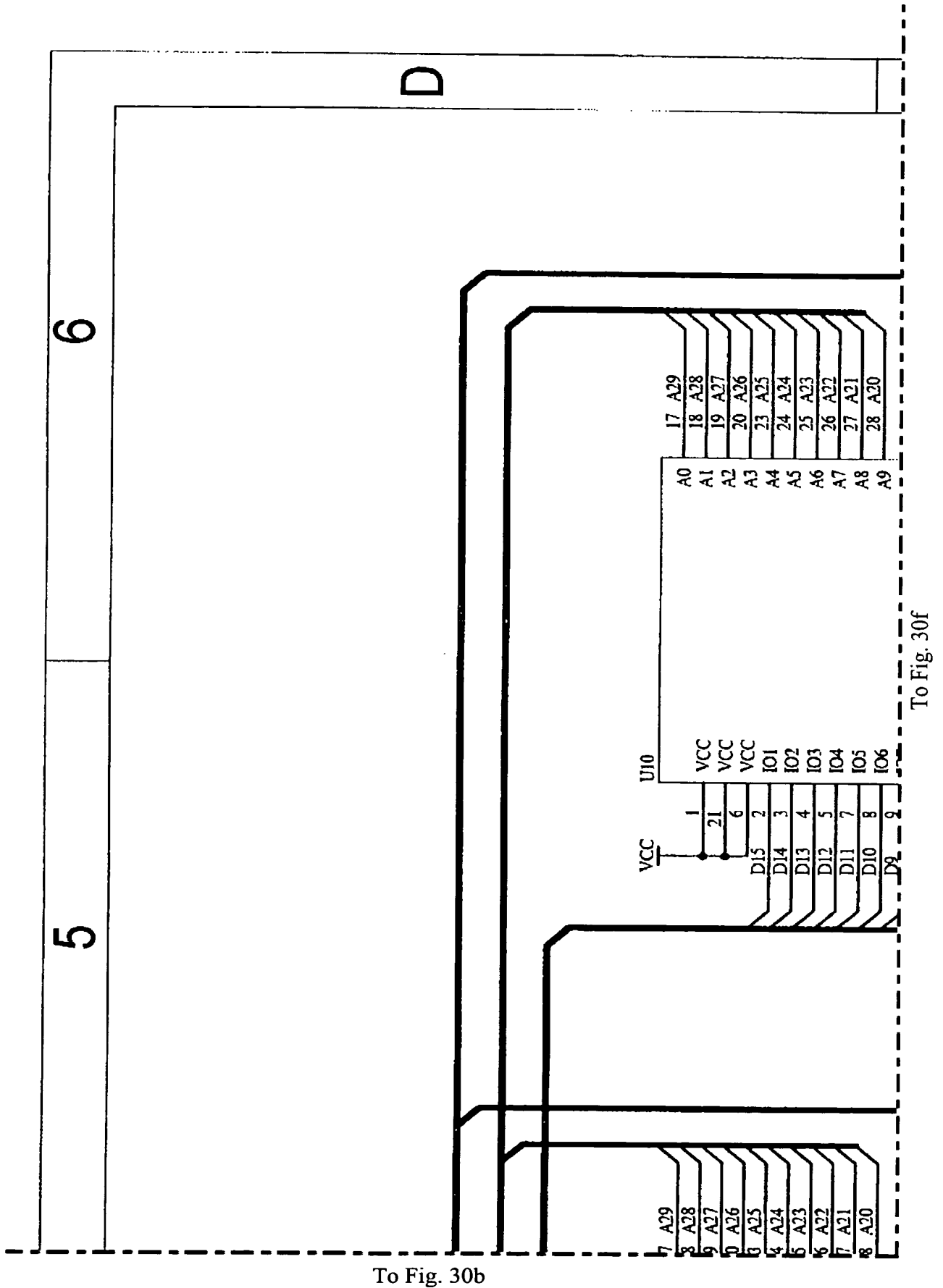


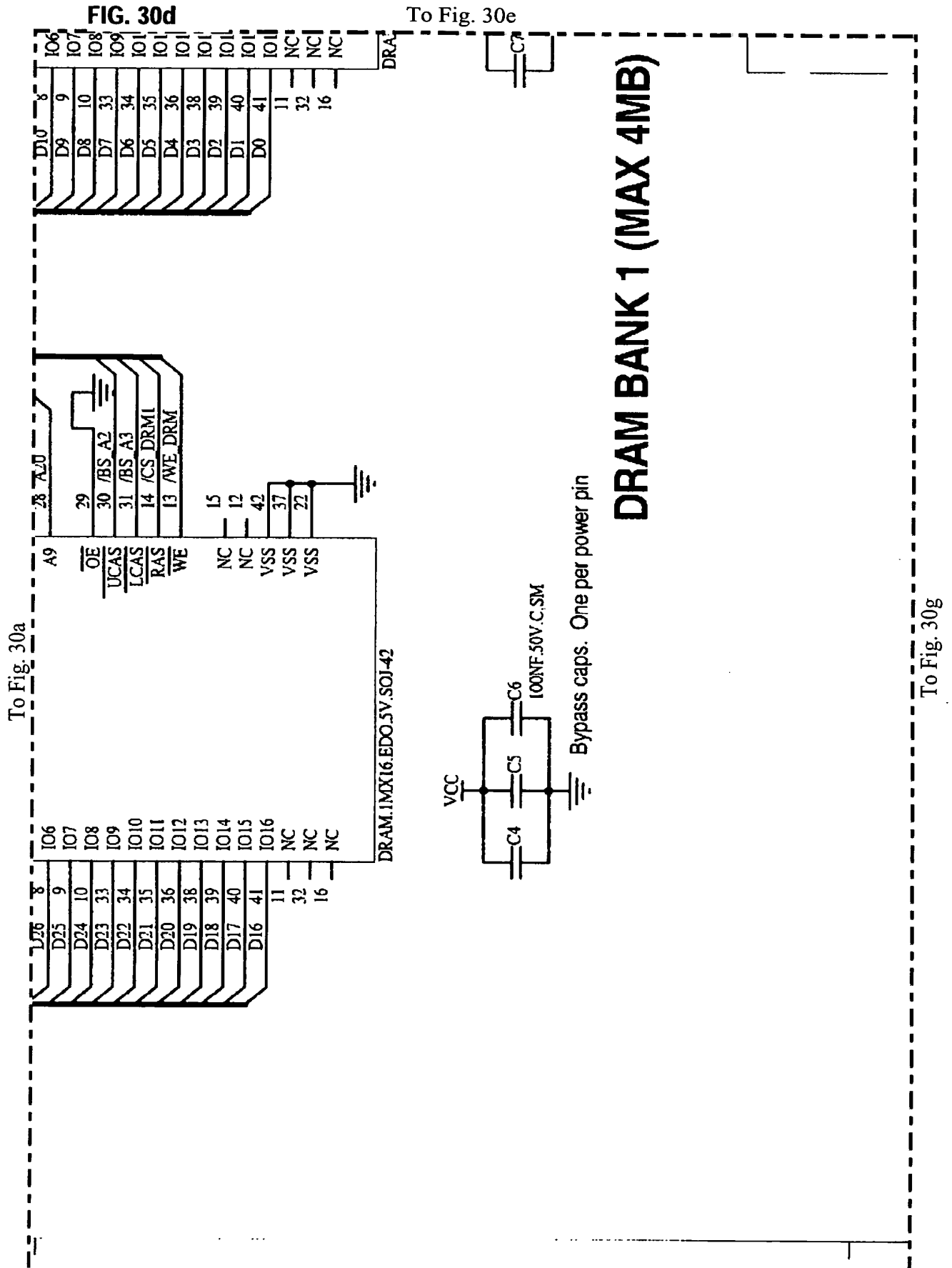
To Fig. 30c



To Fig. 30a

FIG. 30c





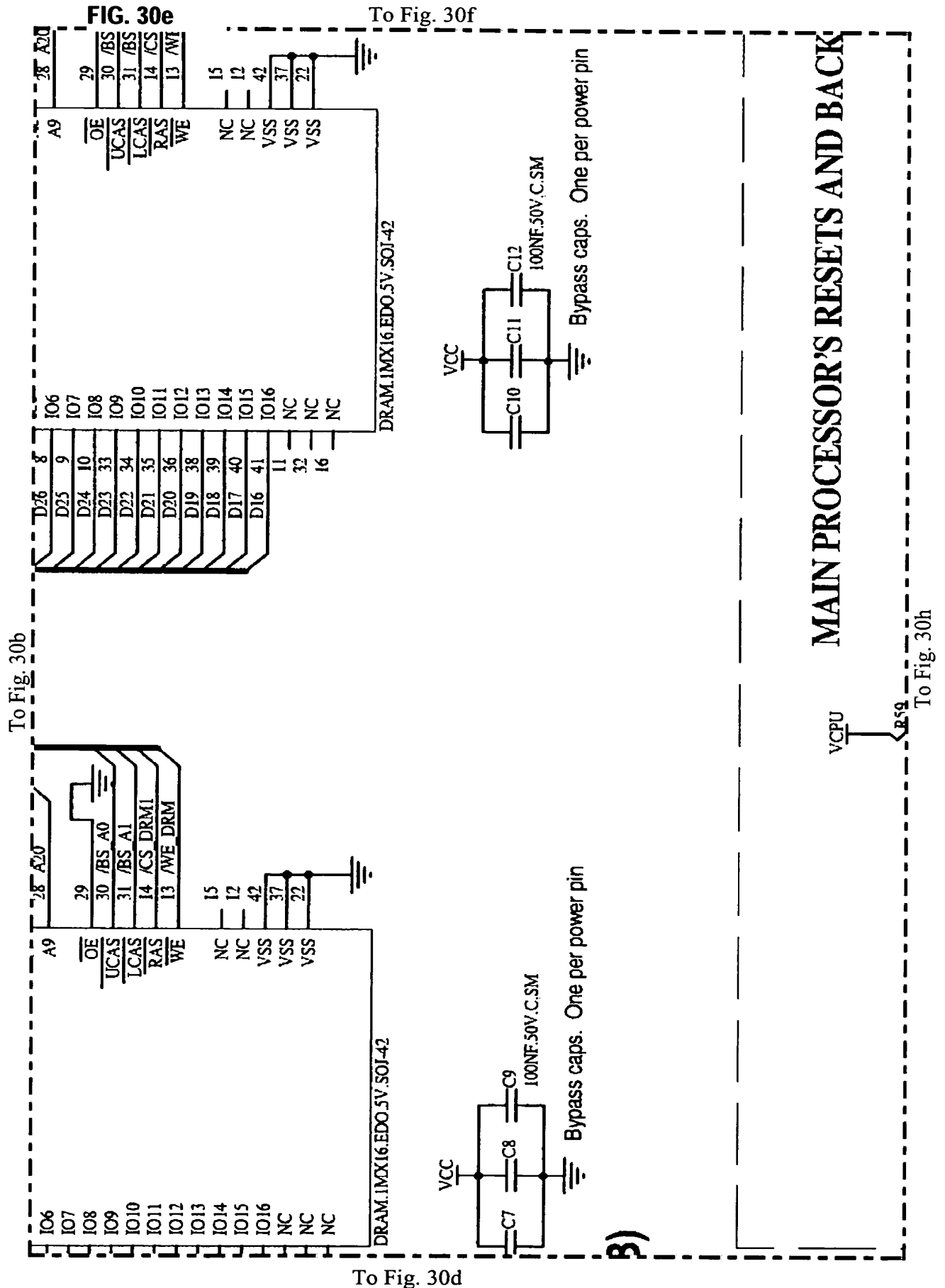
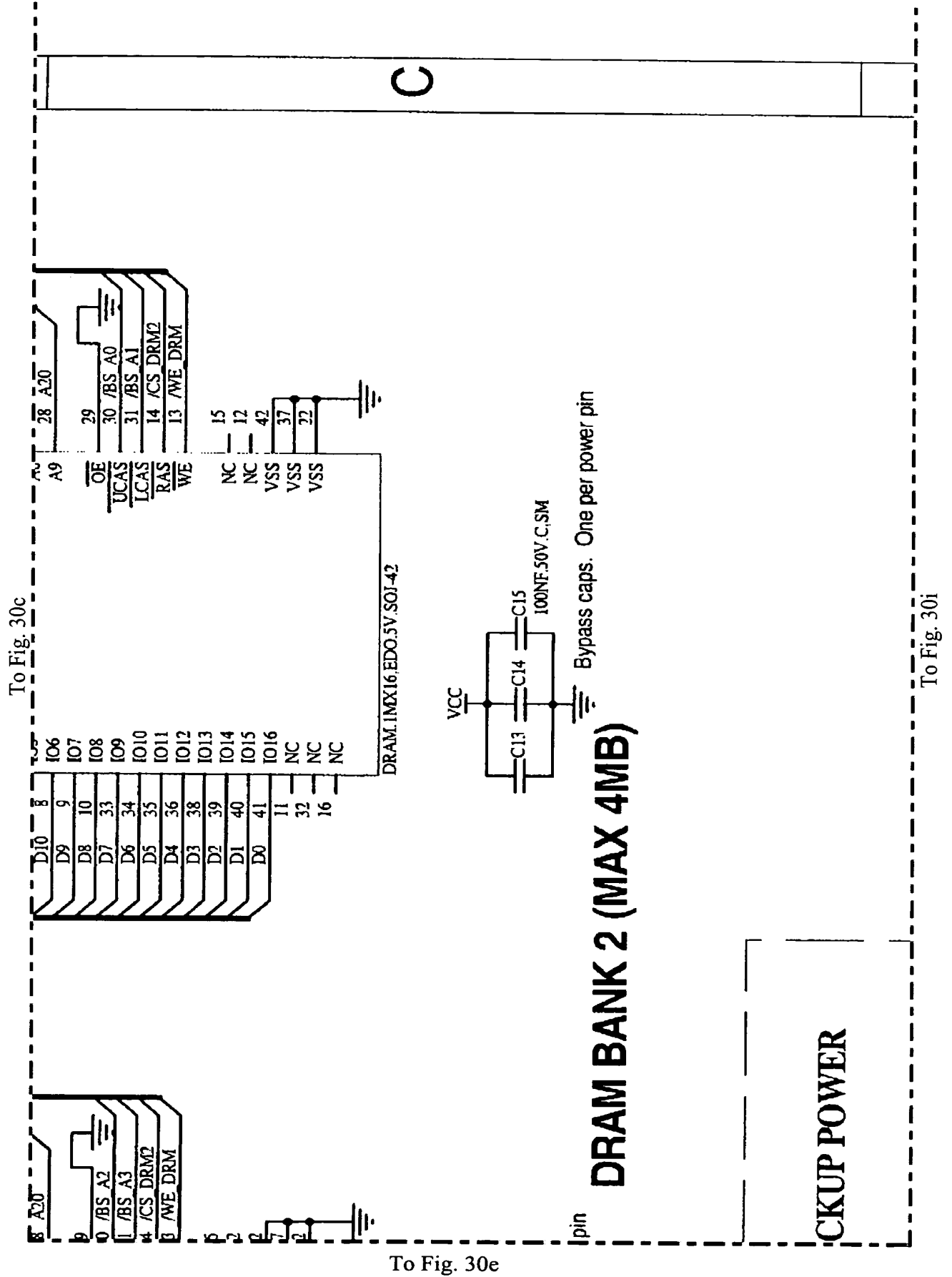
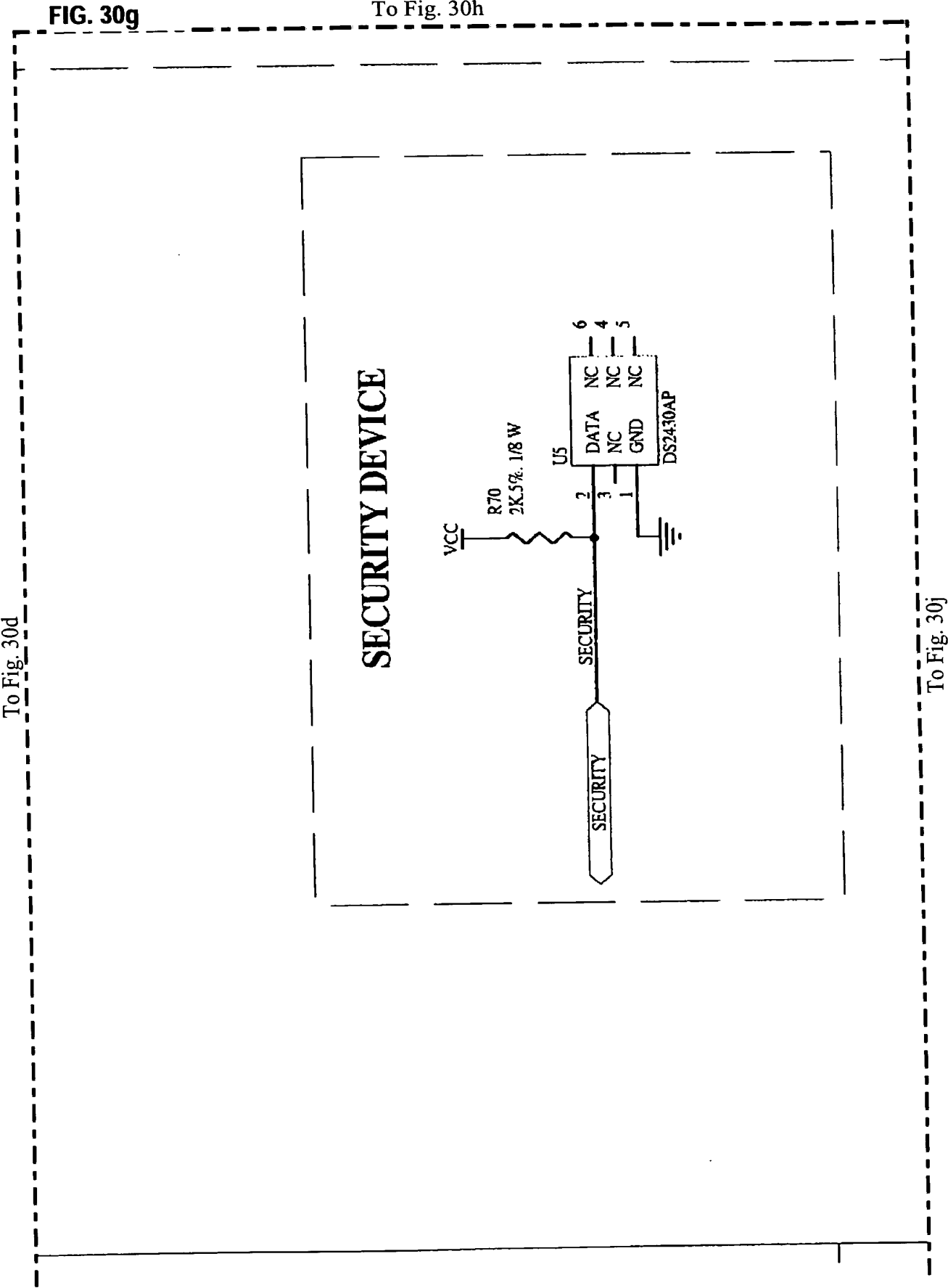


FIG. 30f





To Fig. 30i

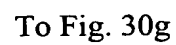
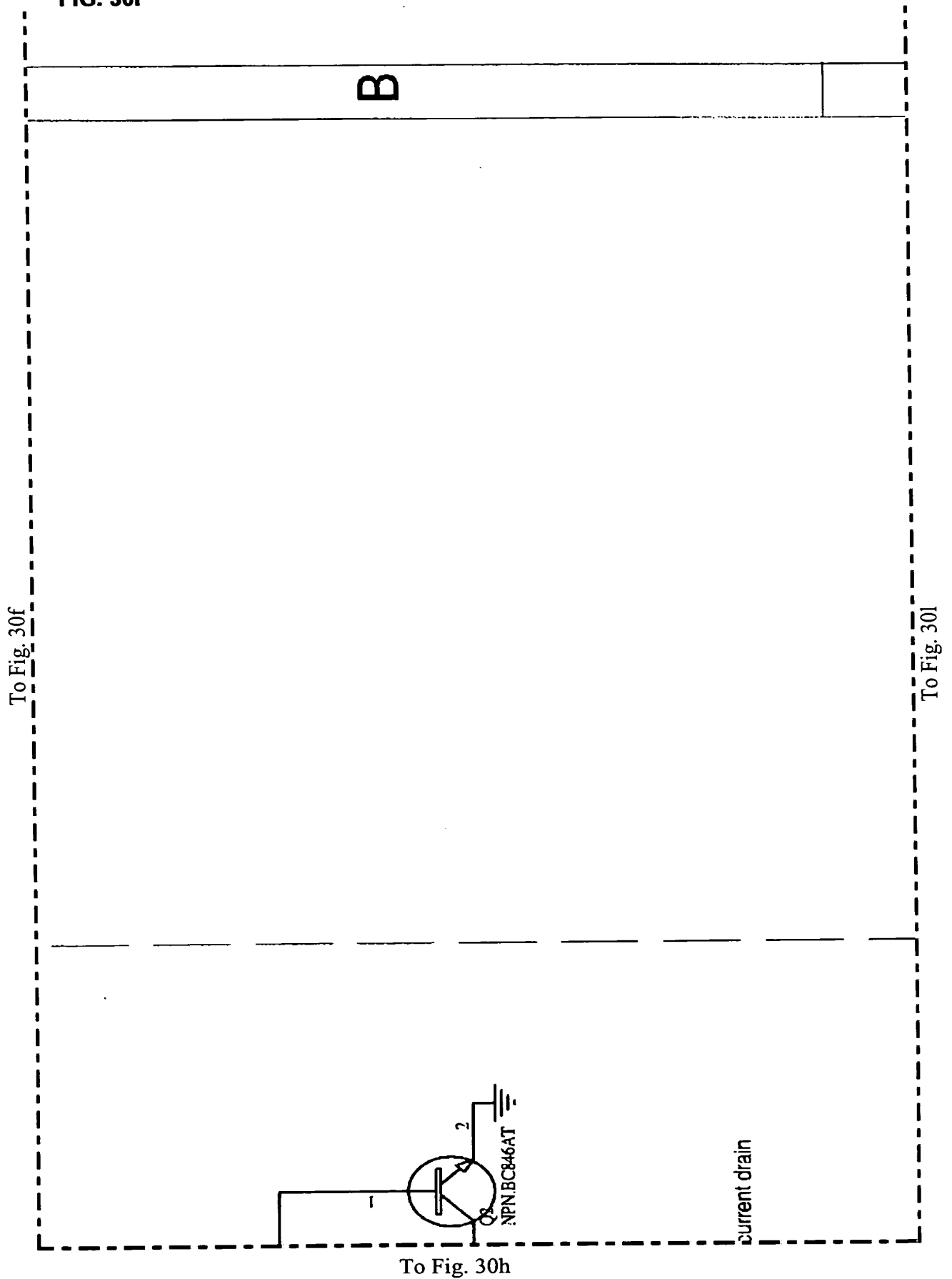
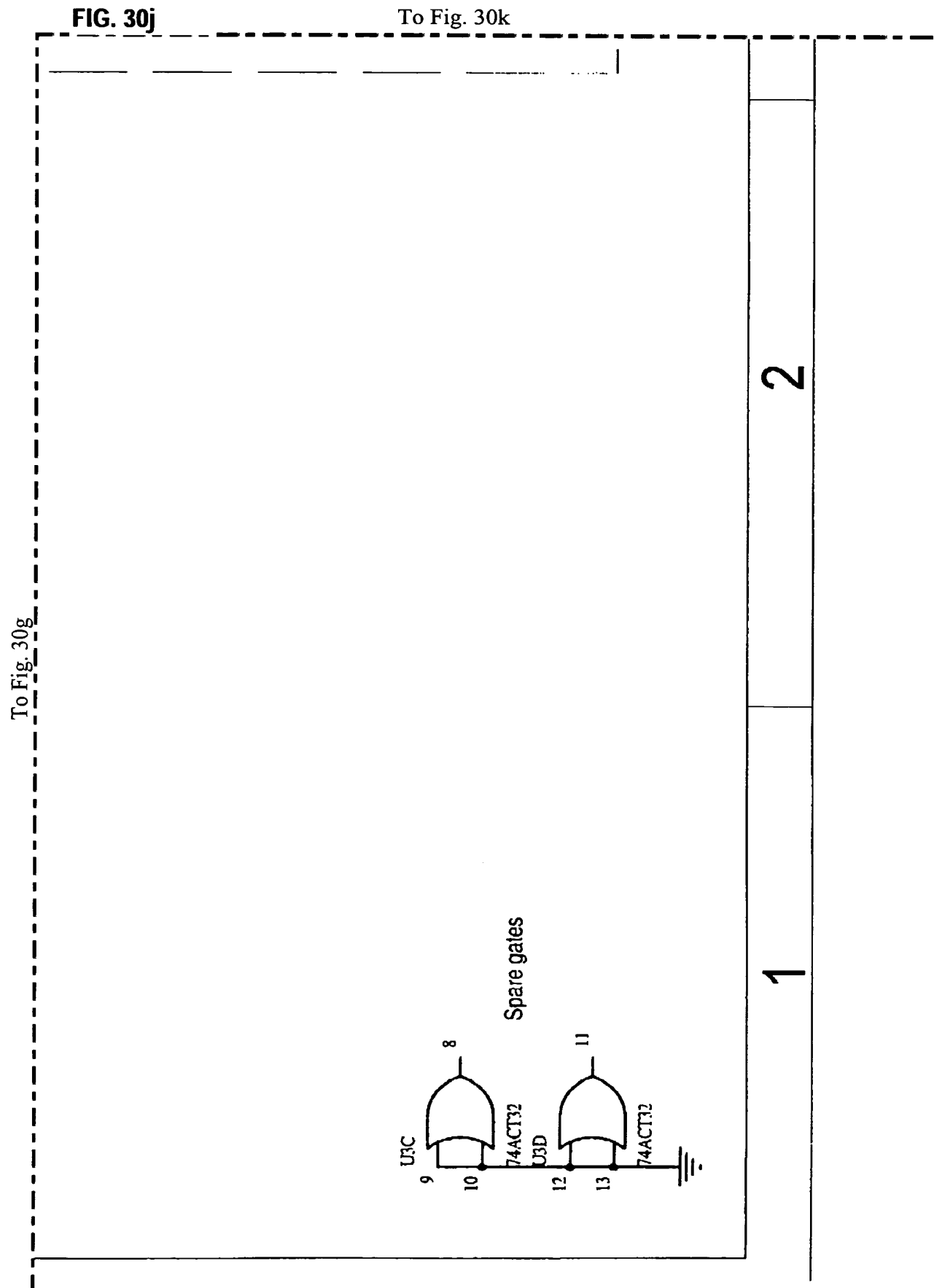


FIG. 30i





To Fig. 301

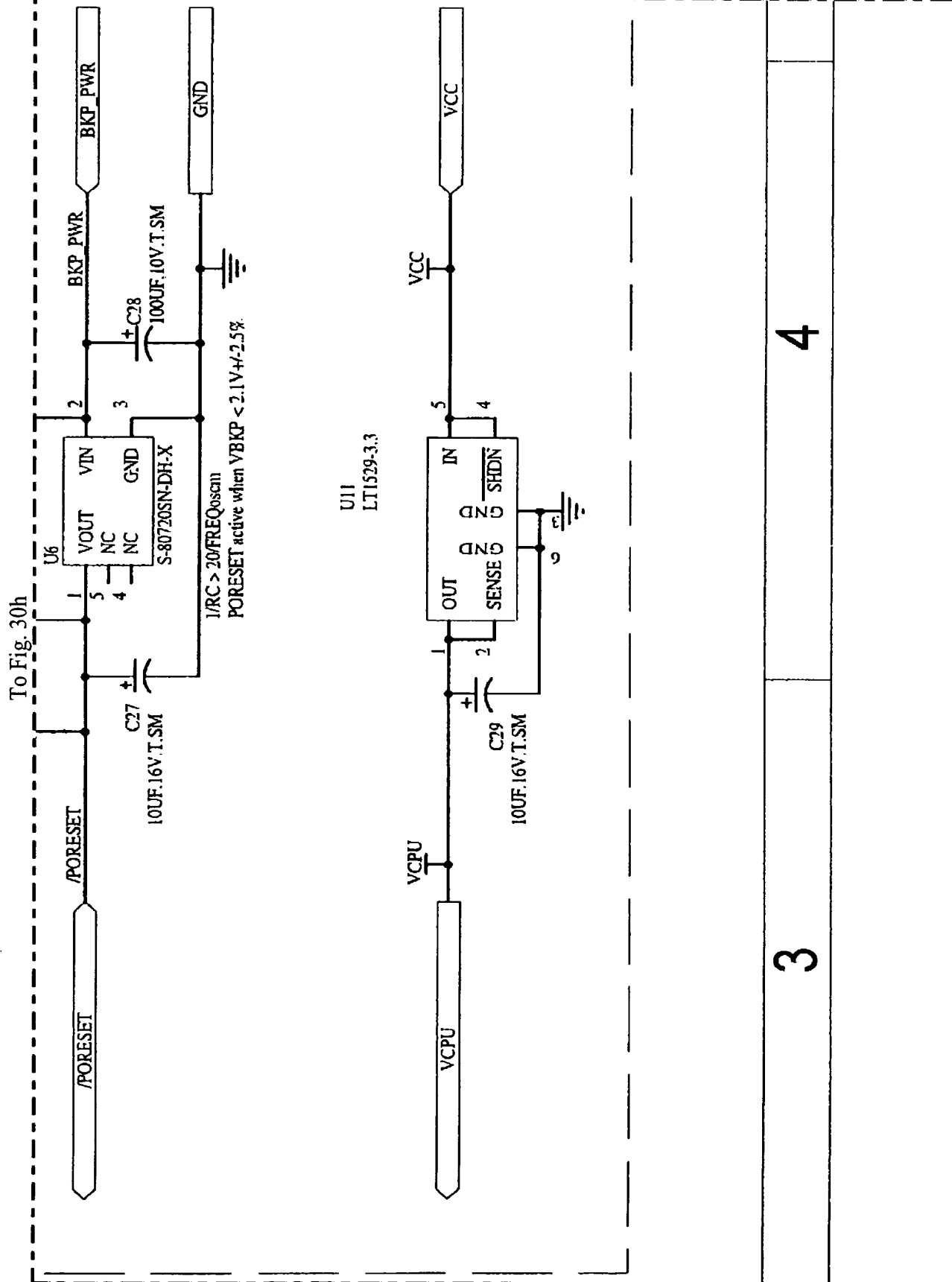


FIG. 30l

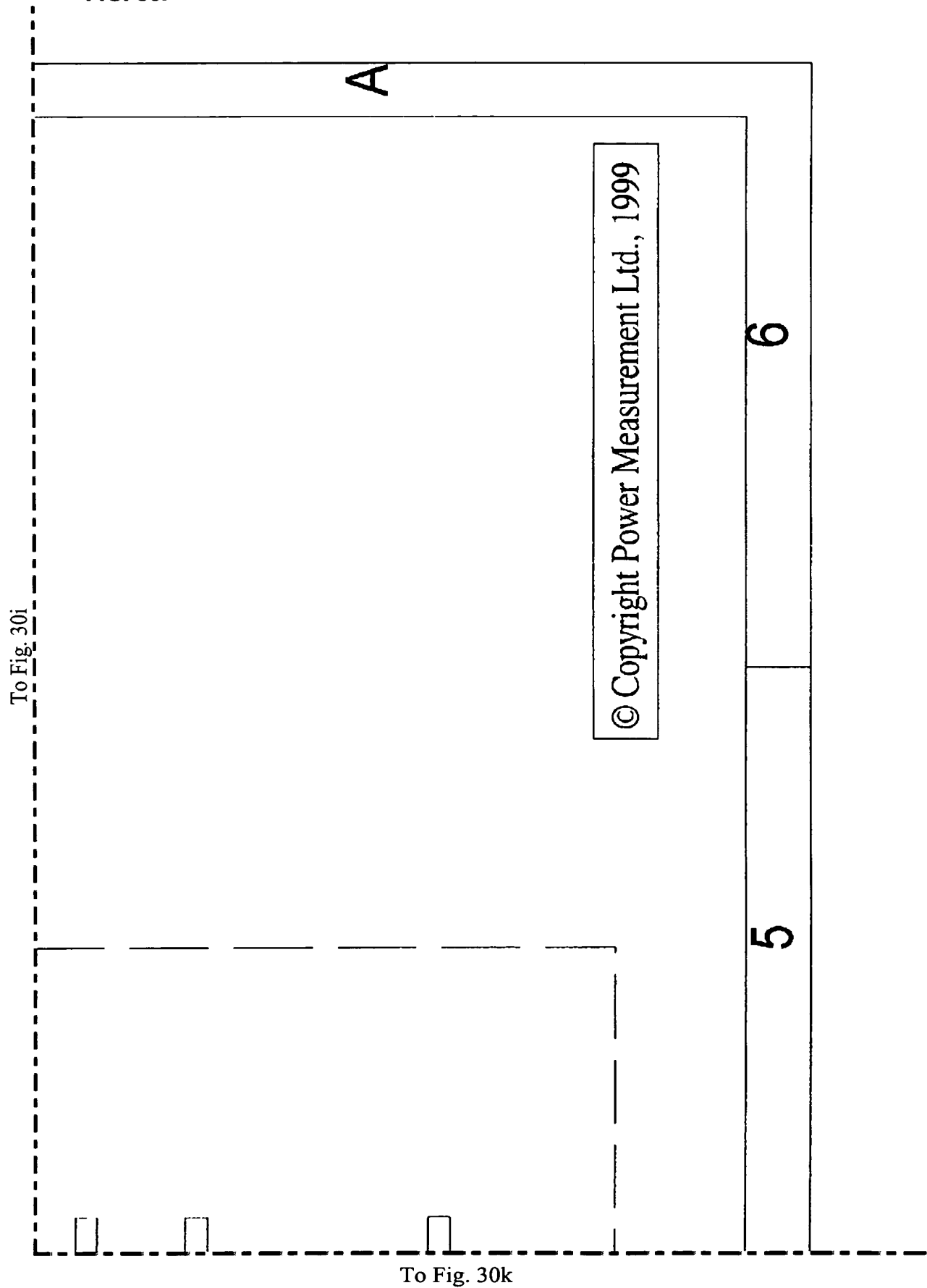


FIG. 31a

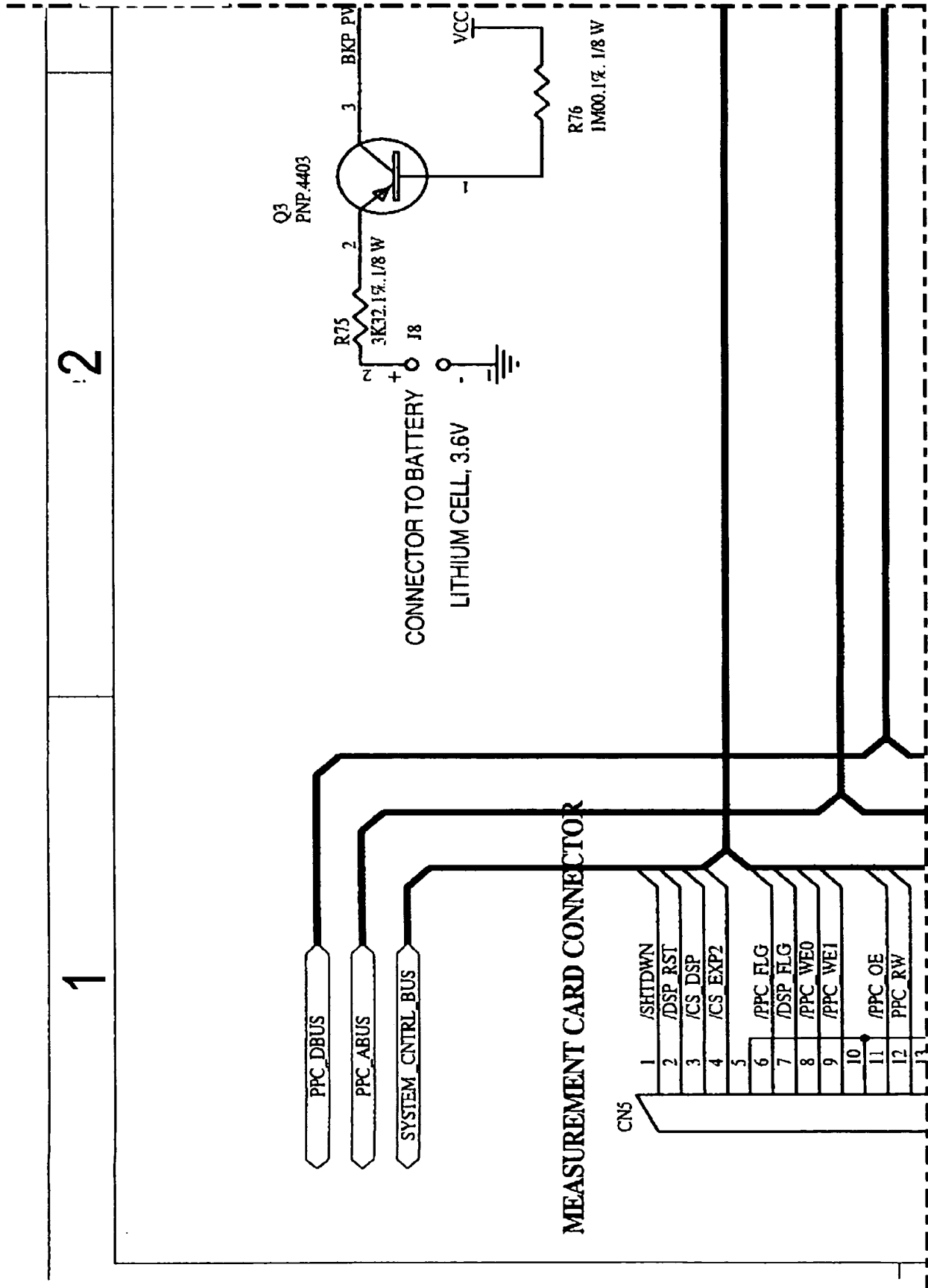


FIG. 31b

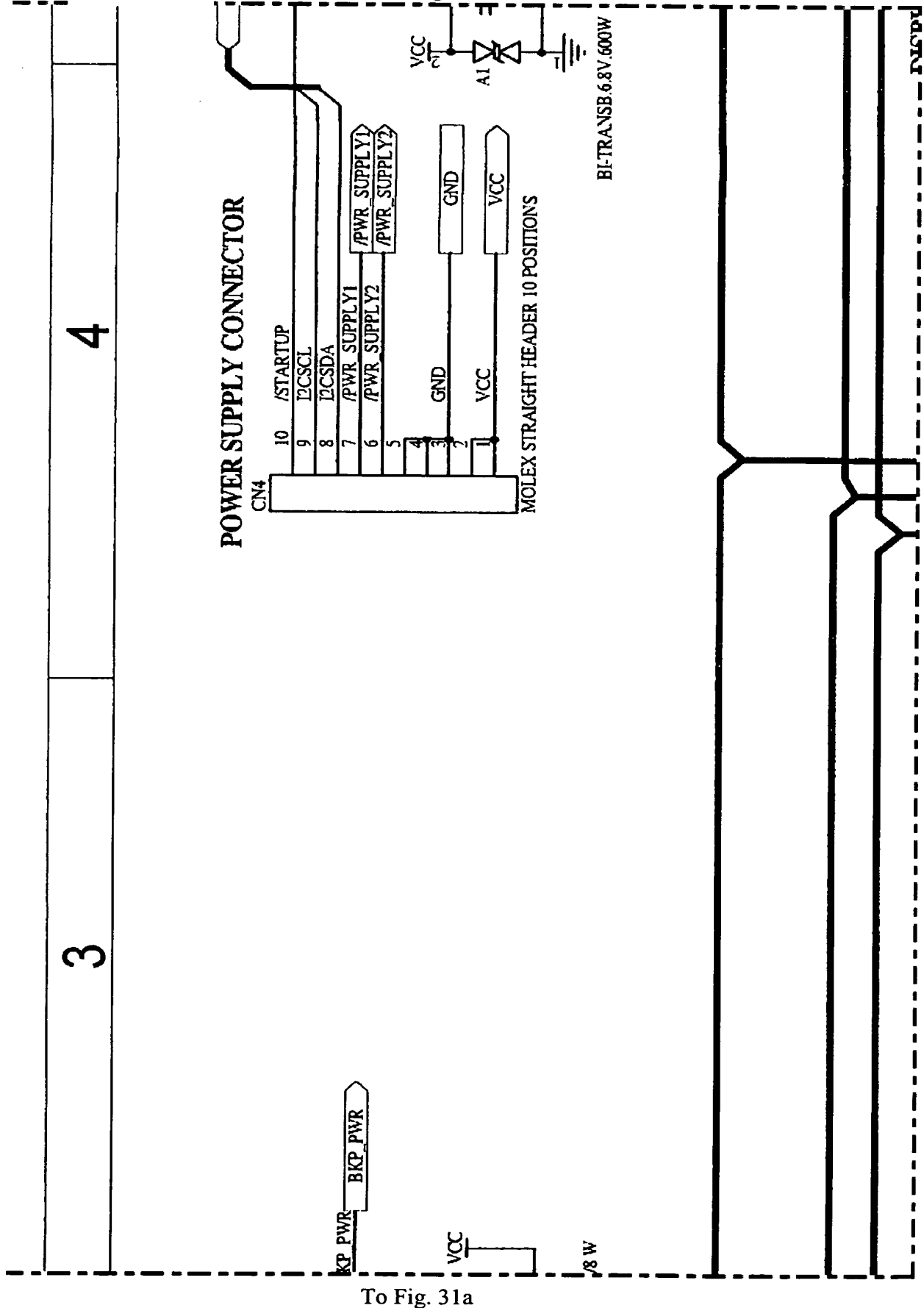
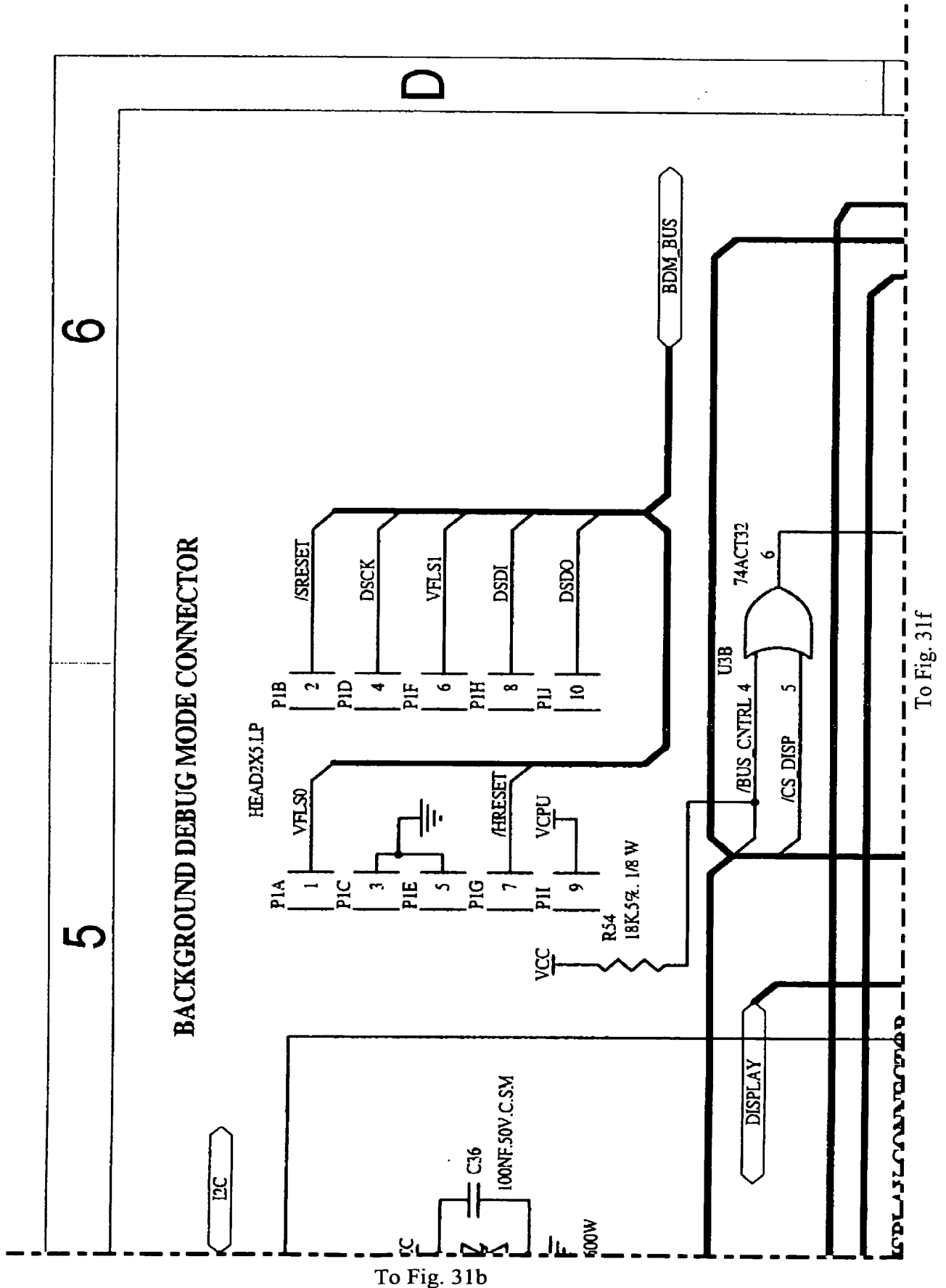
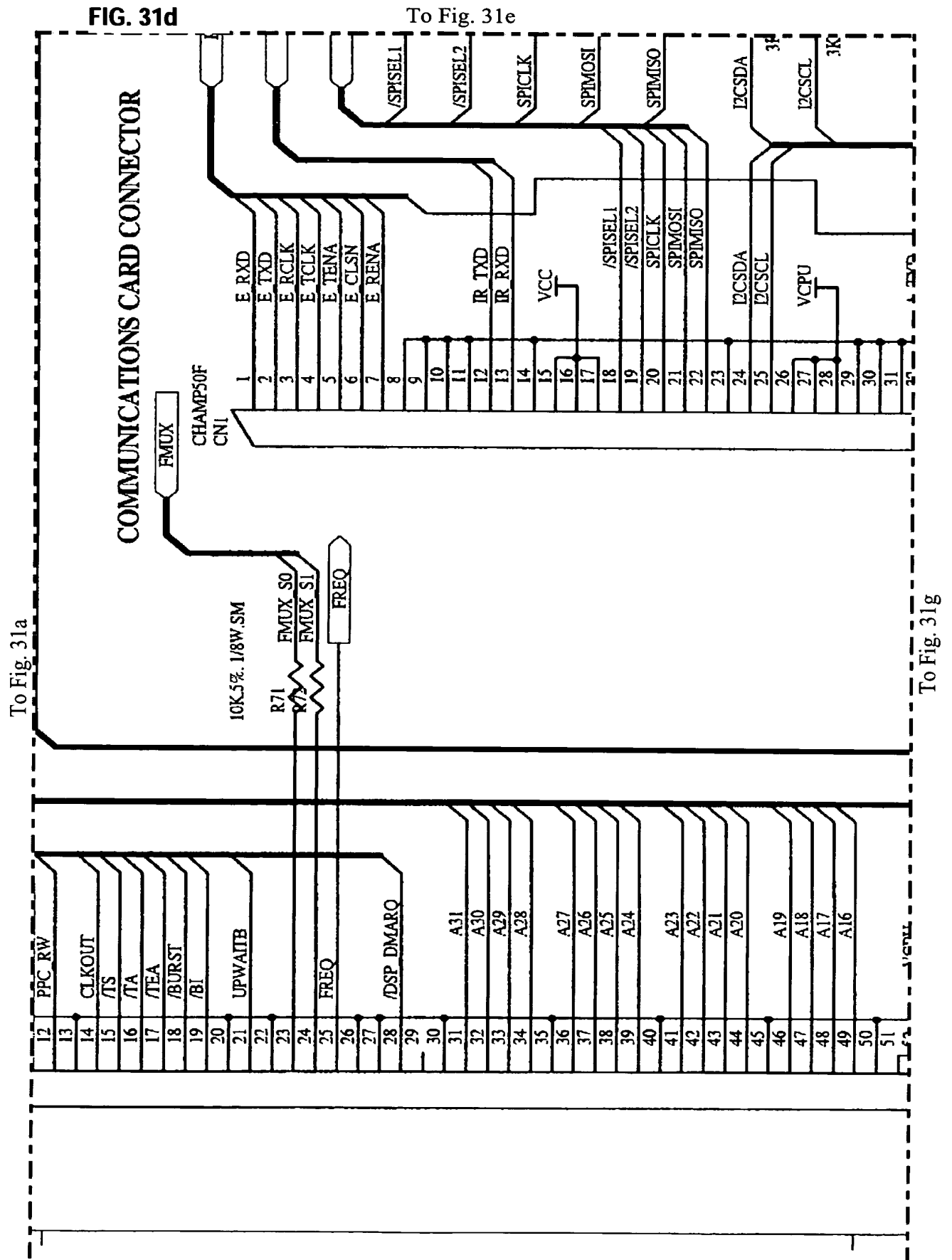


FIG. 31c





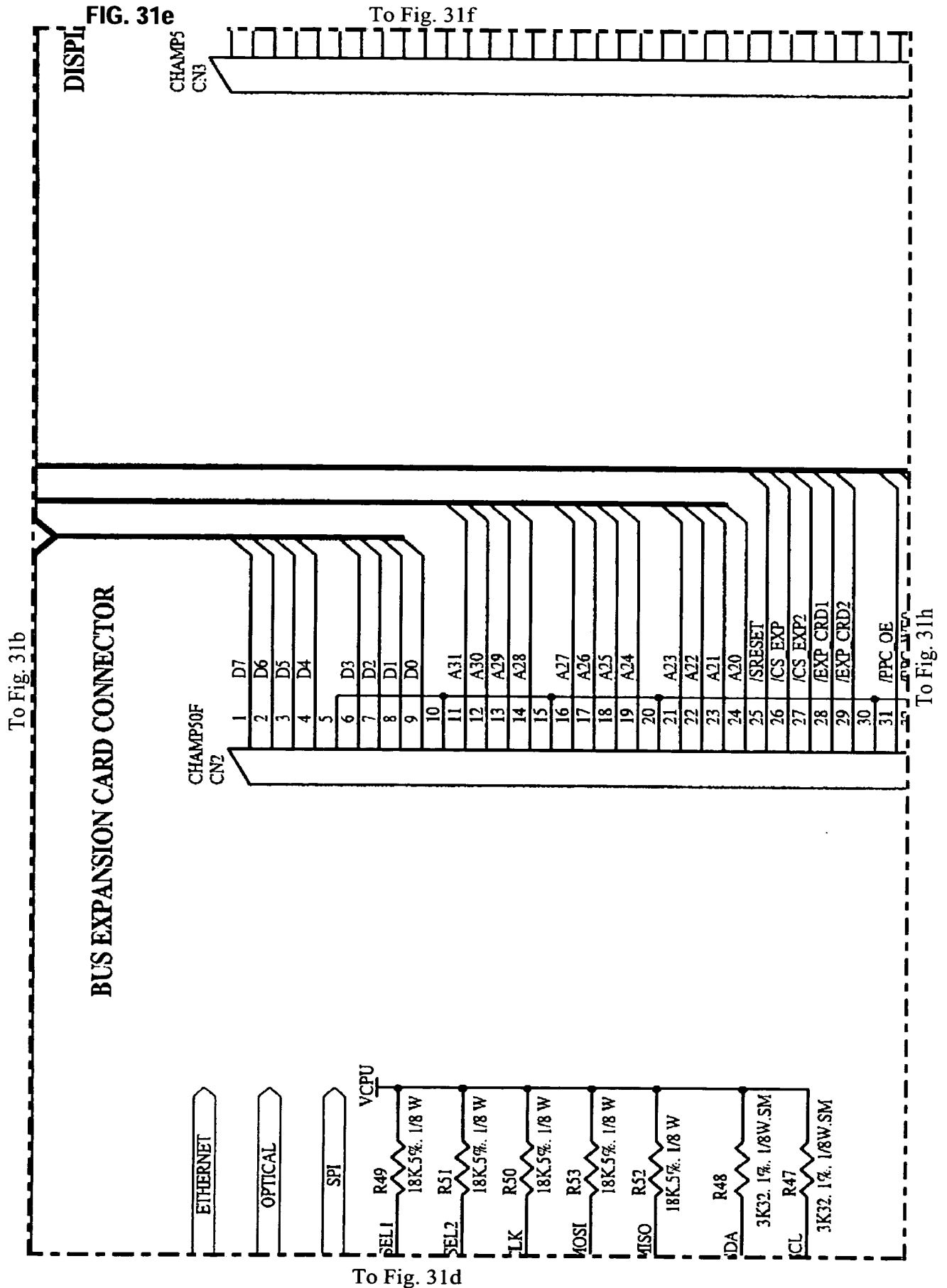
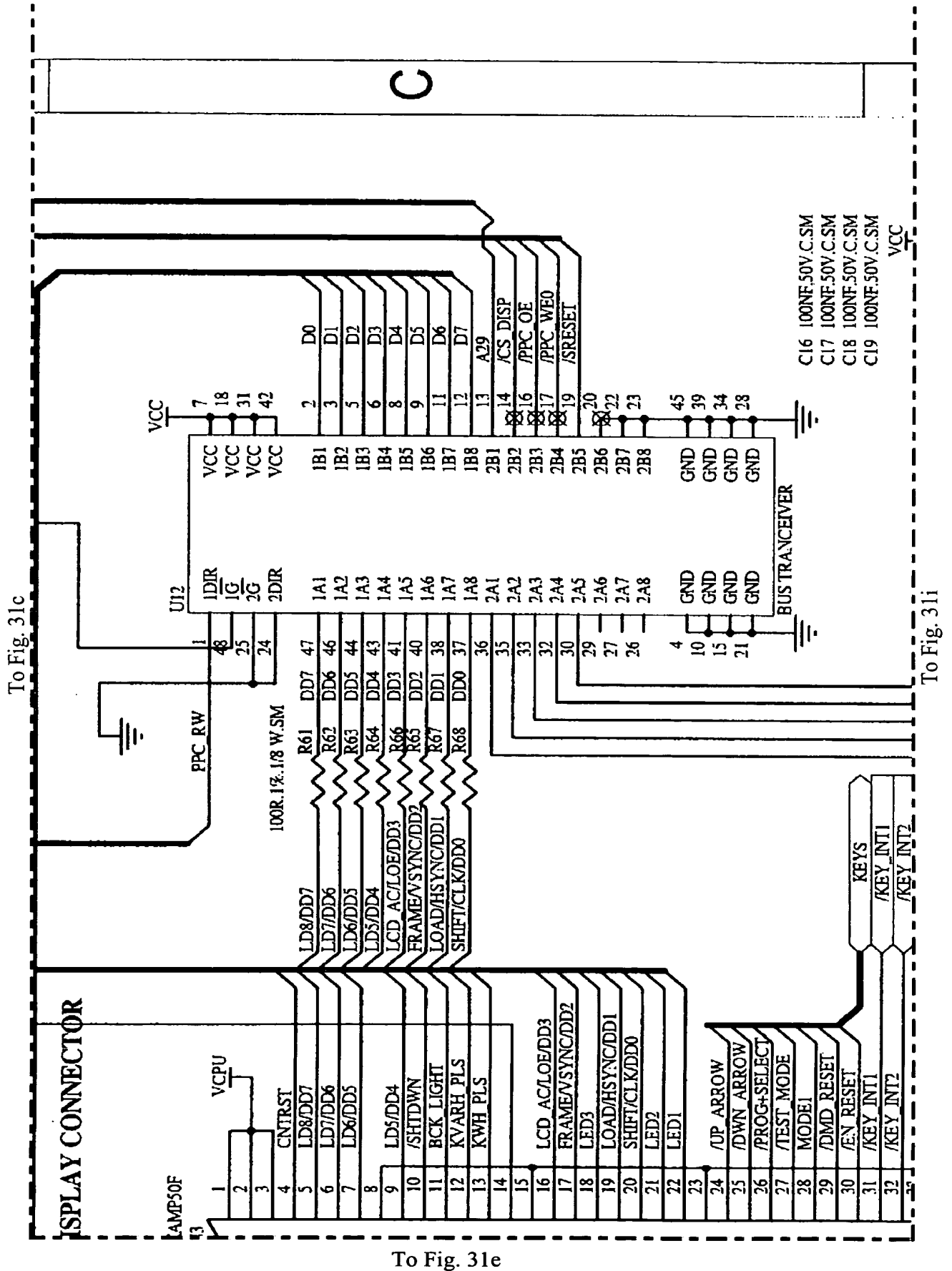
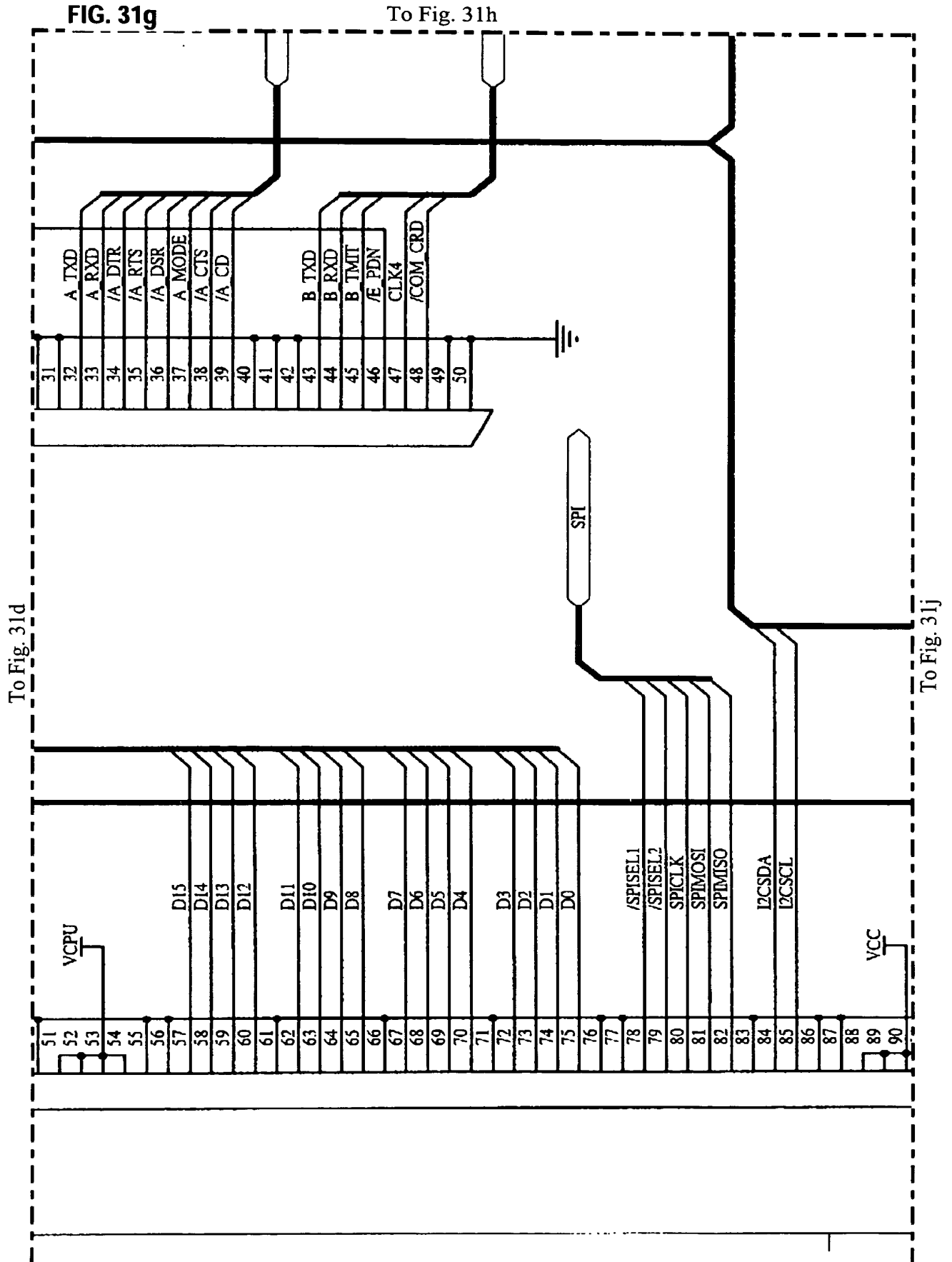


FIG. 31f





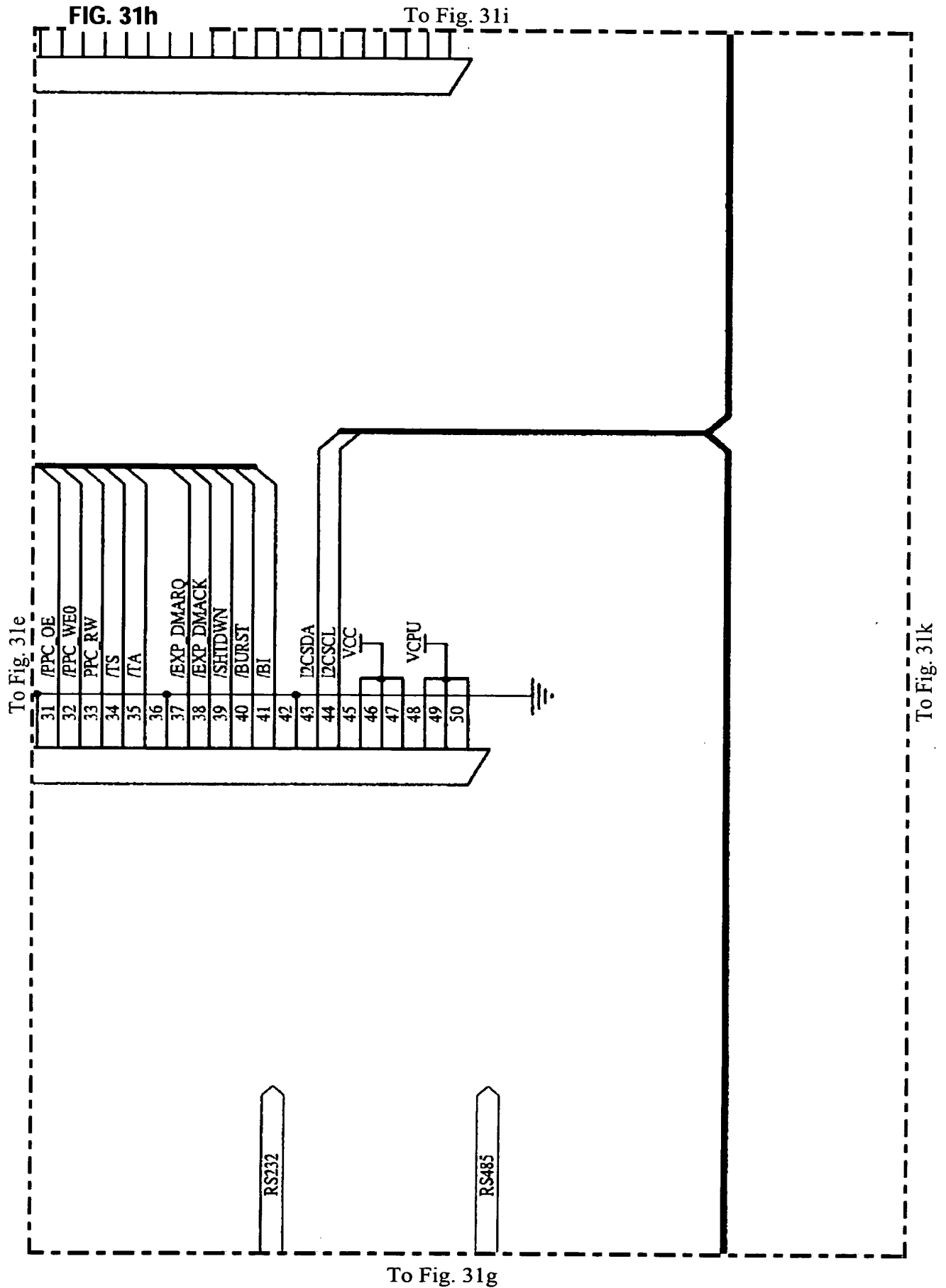


FIG. 31i

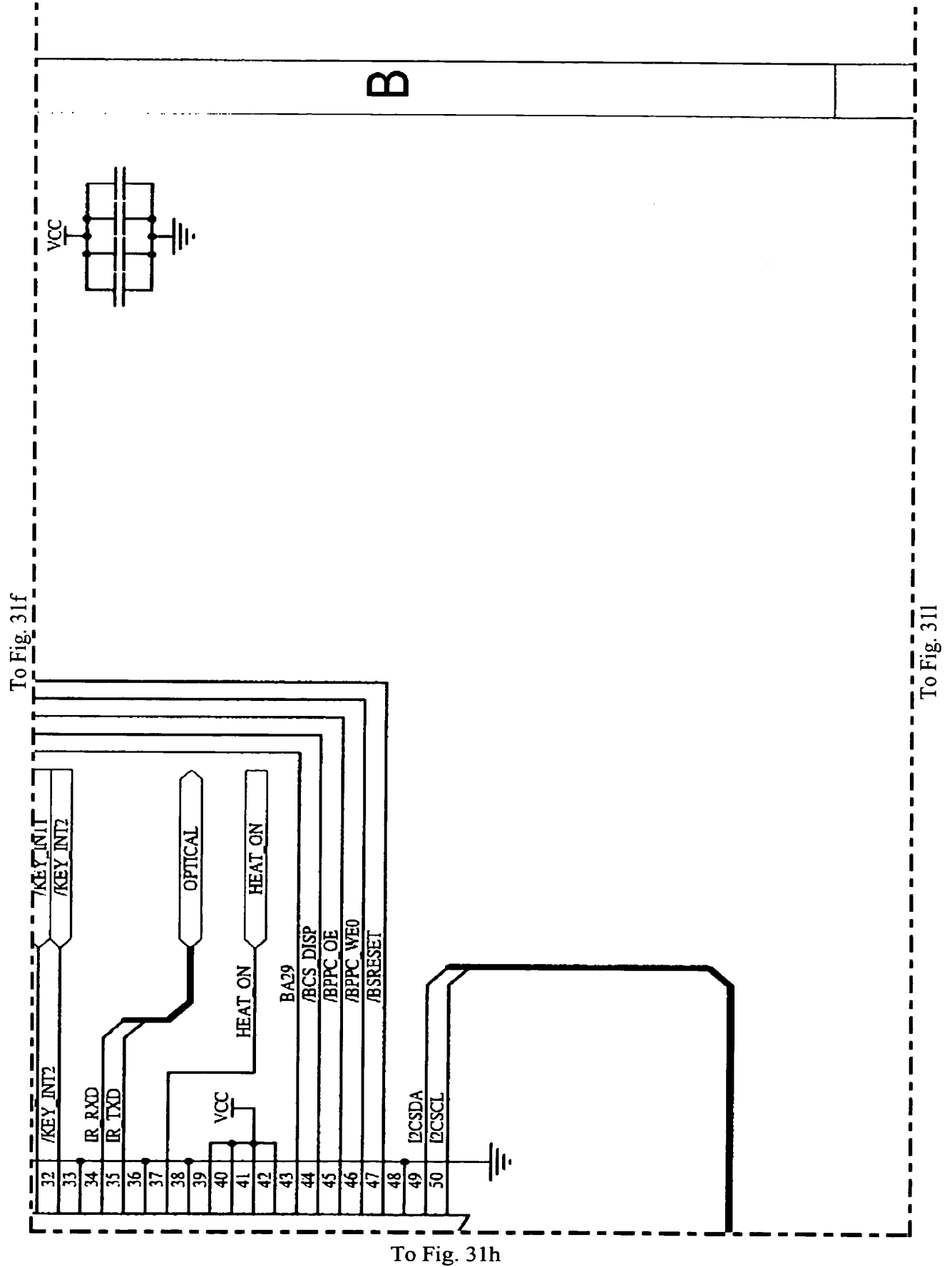
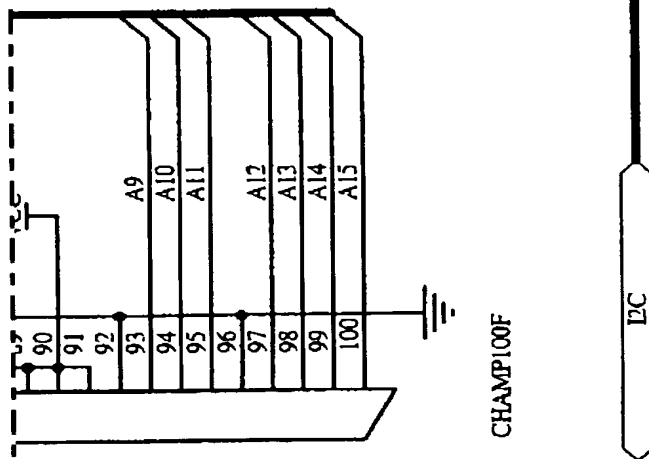


FIG. 31j

To Fig. 31k

To Fig. 31g



2

1

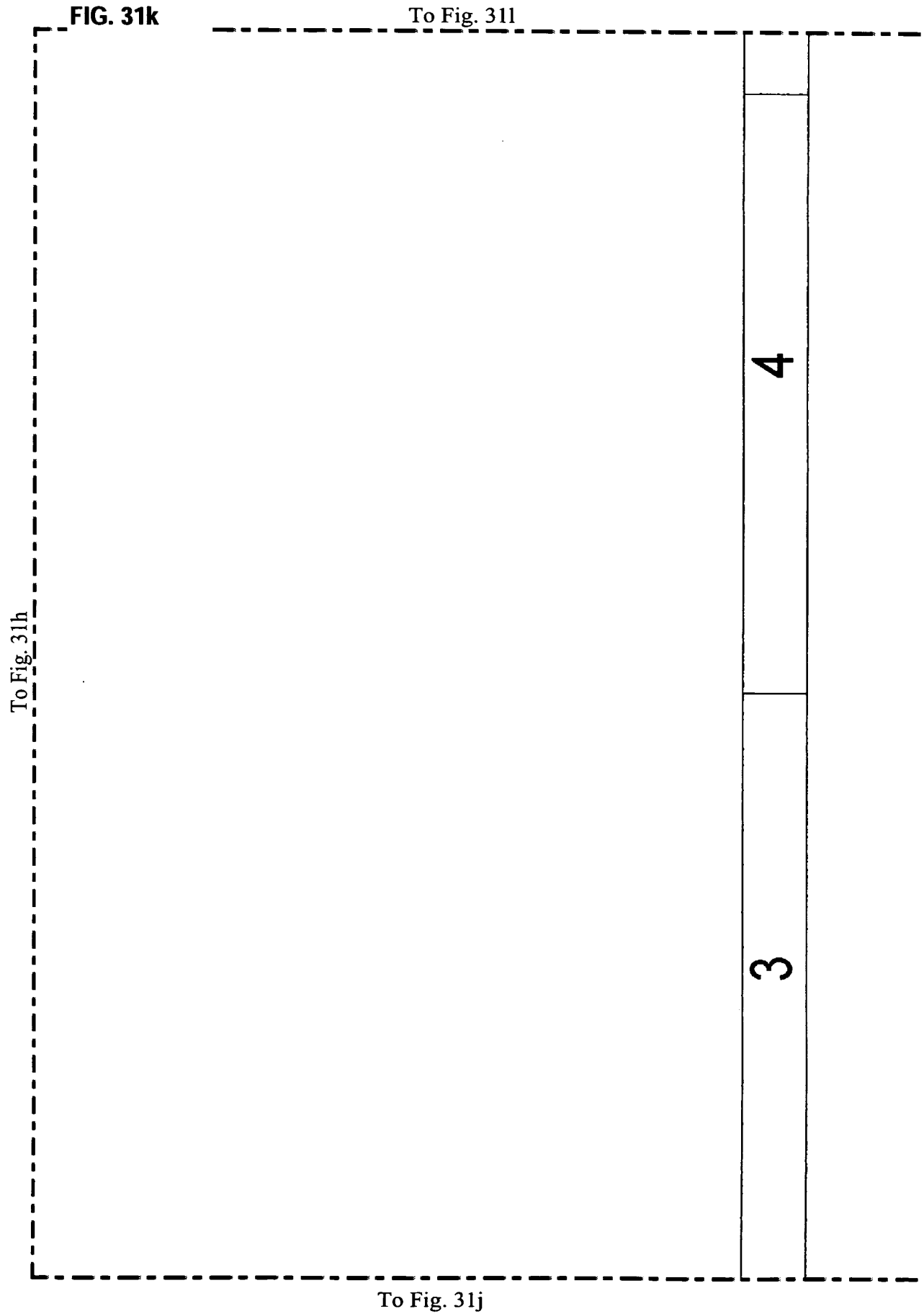
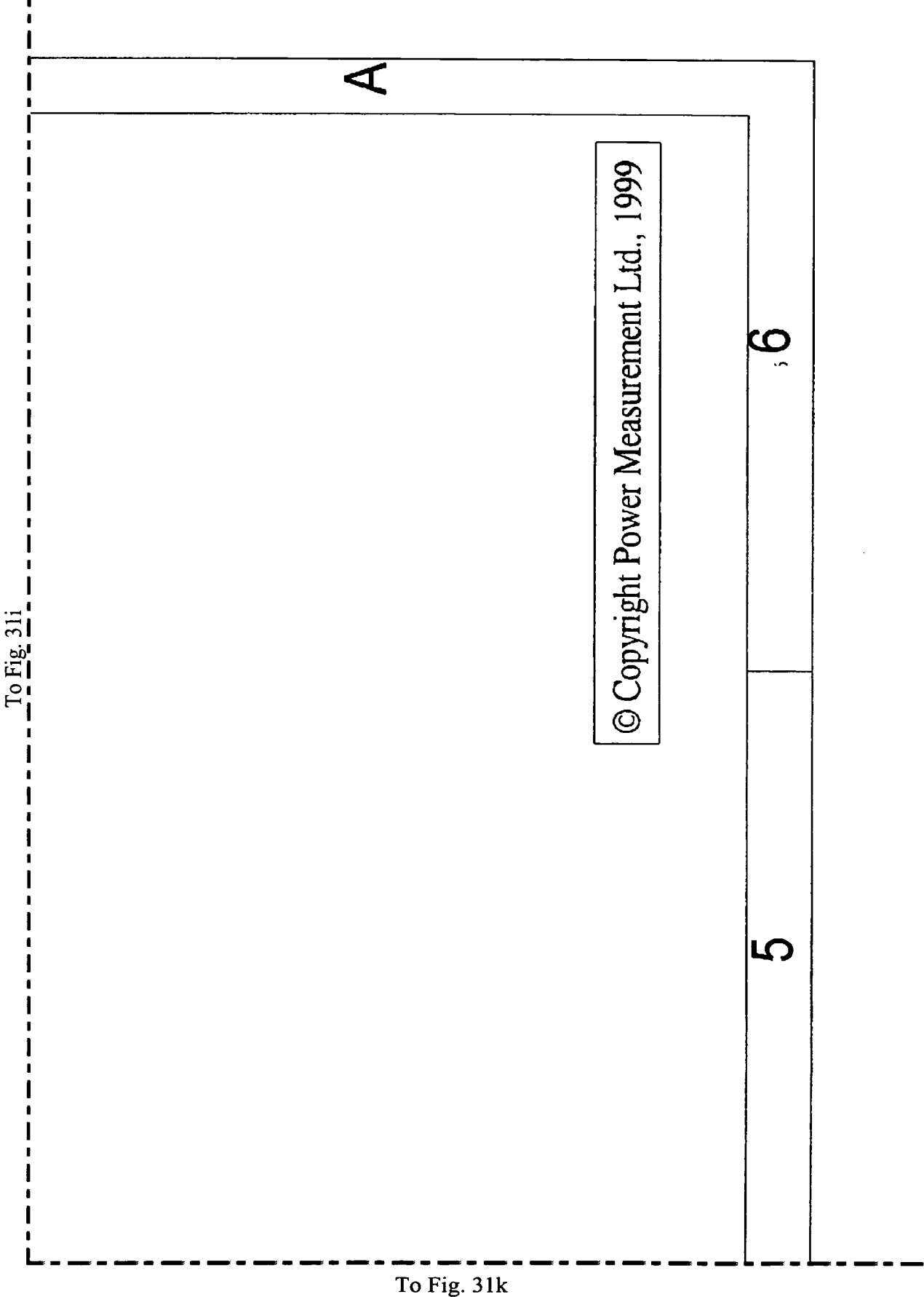
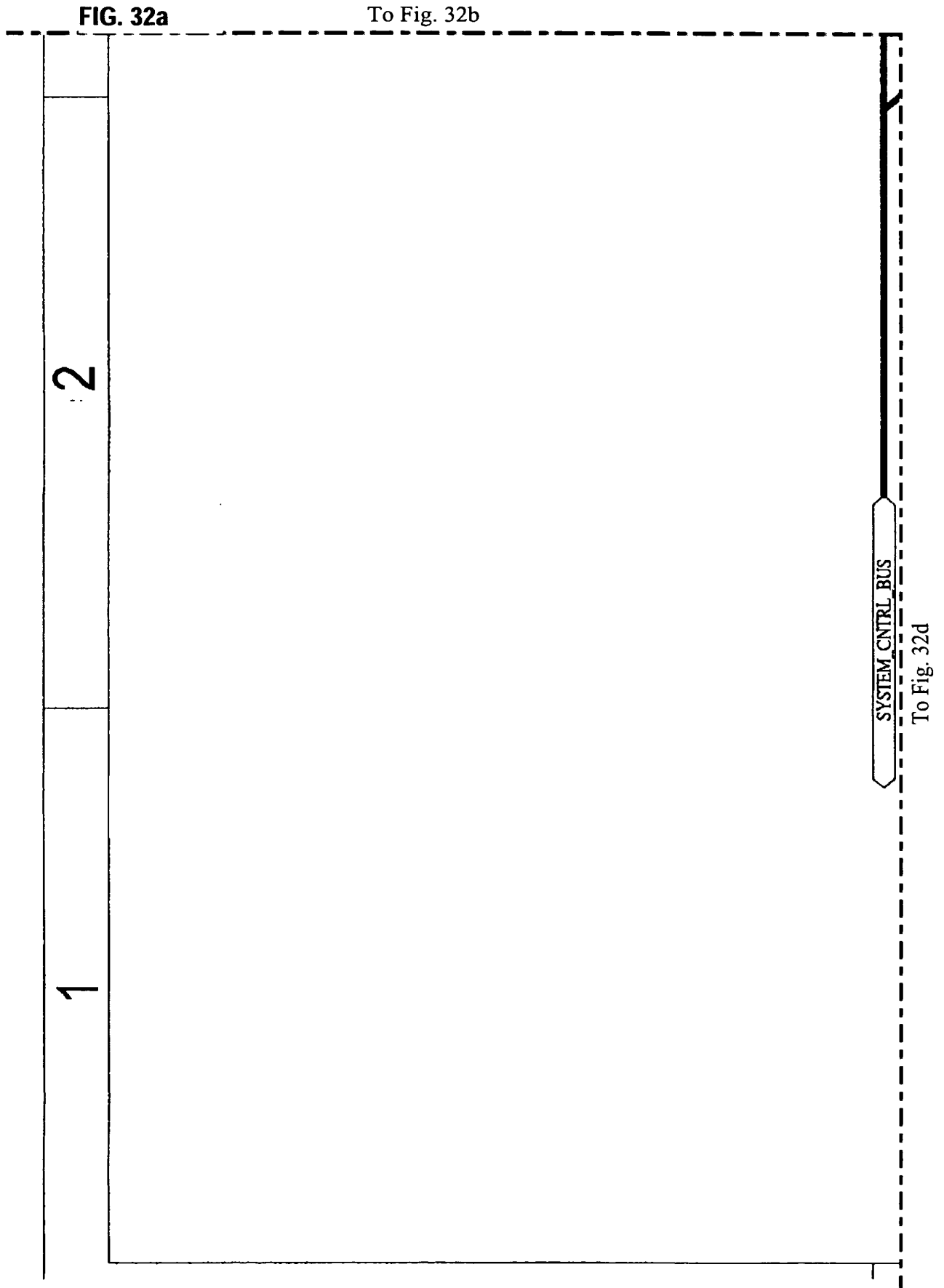


FIG. 31l





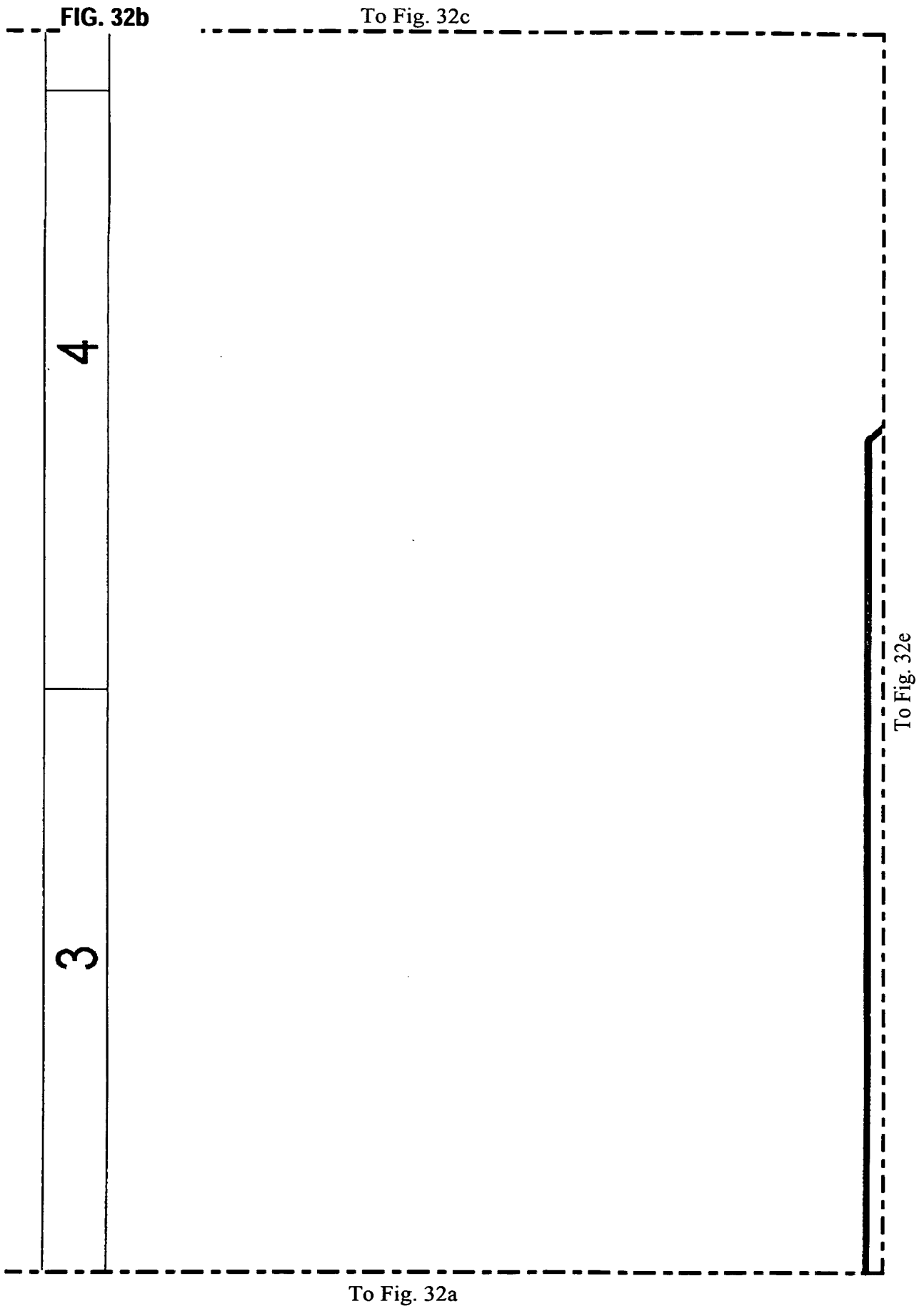
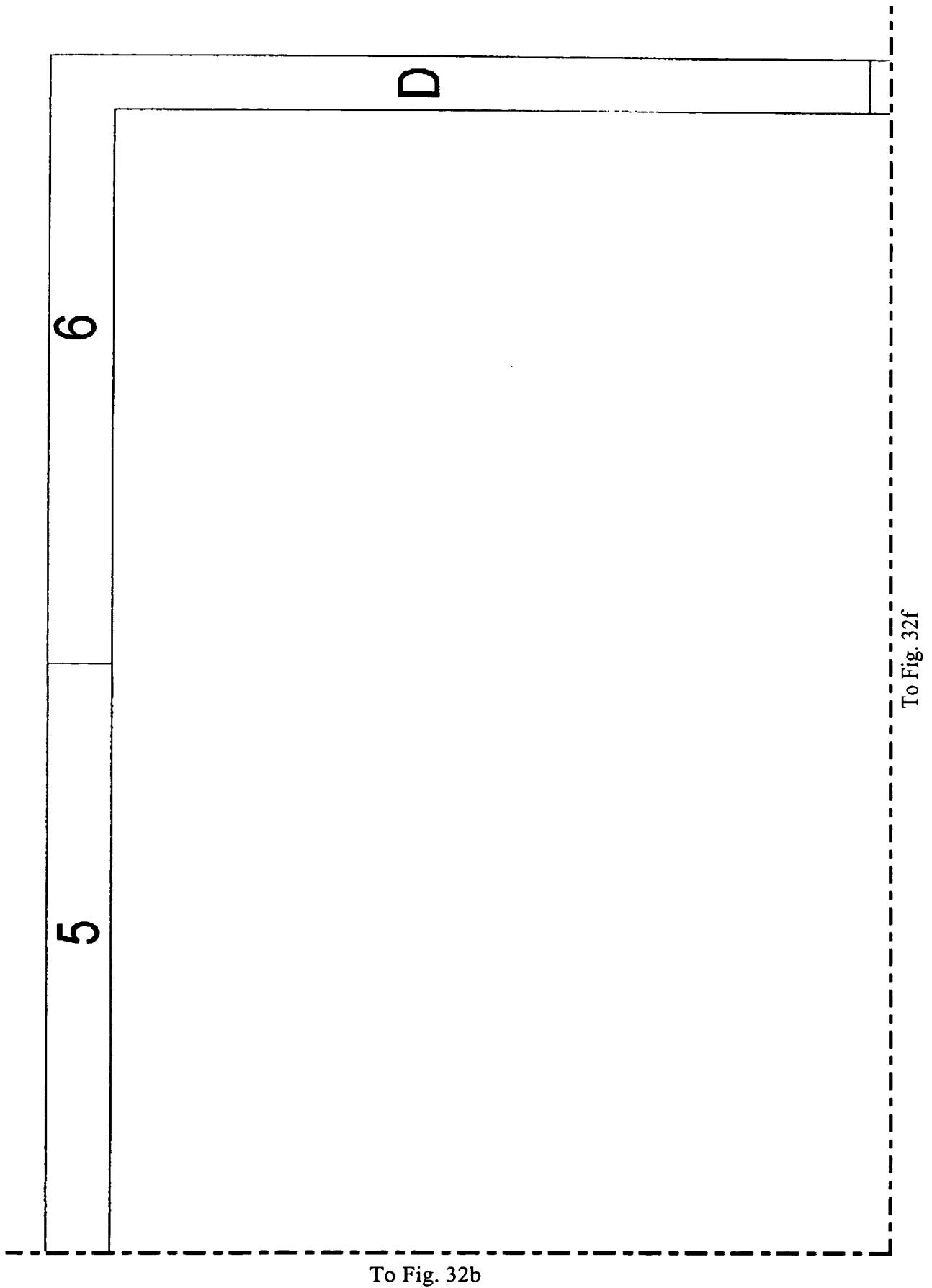
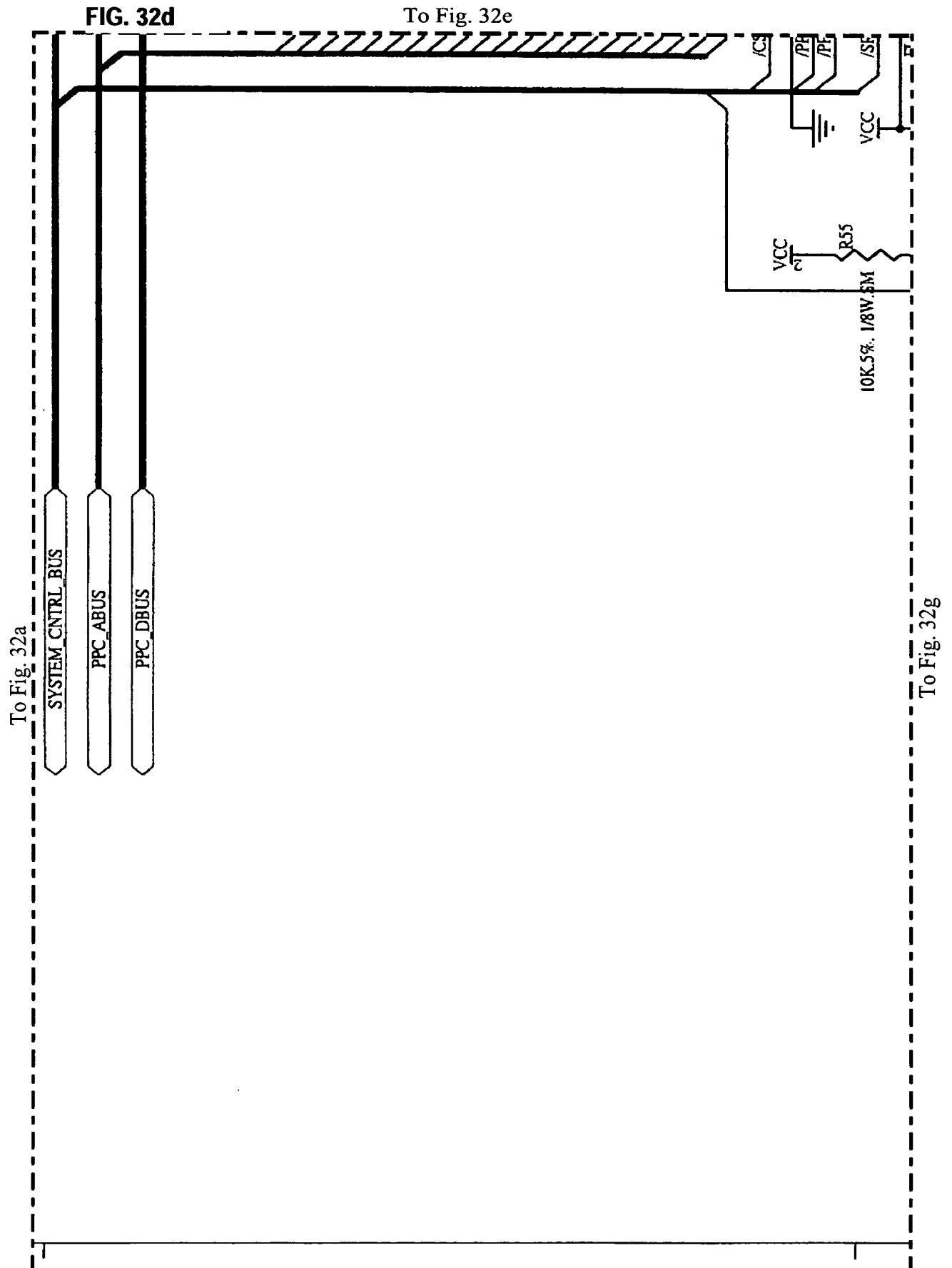


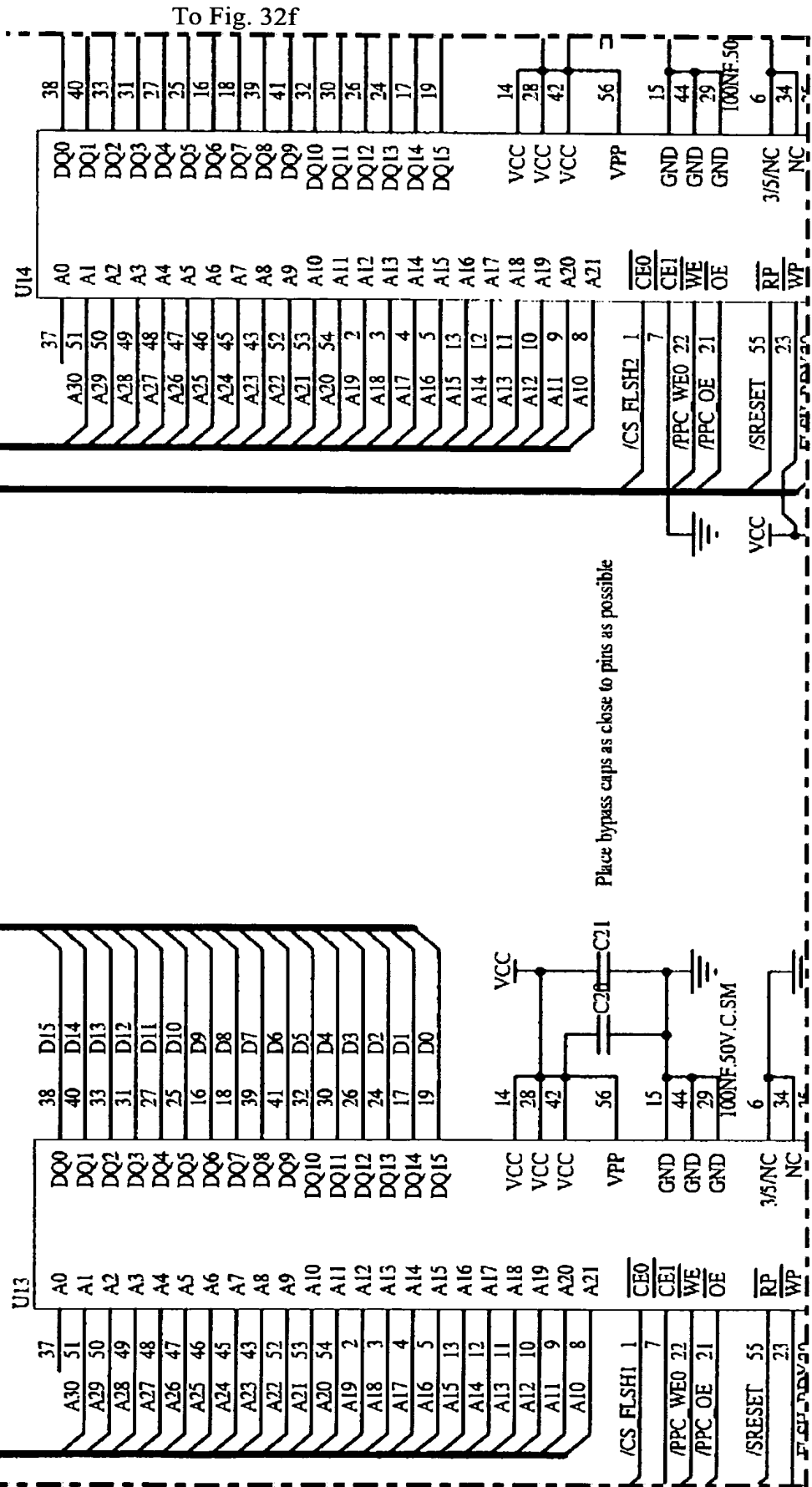
FIG. 32c





To Fig. 32b

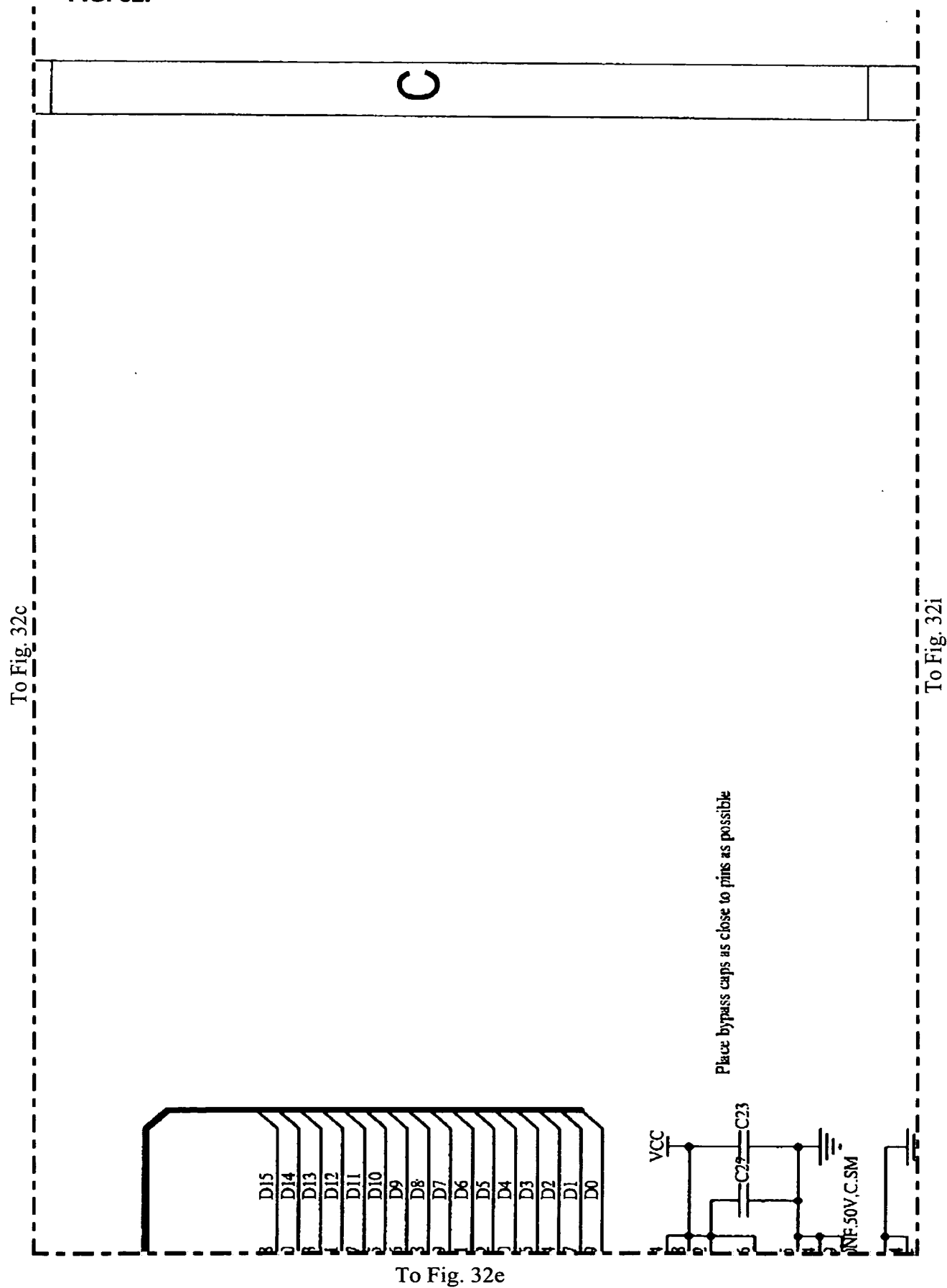
FIG. 32e



To Fig. 32d

To Fig. 32h

FIG. 32f



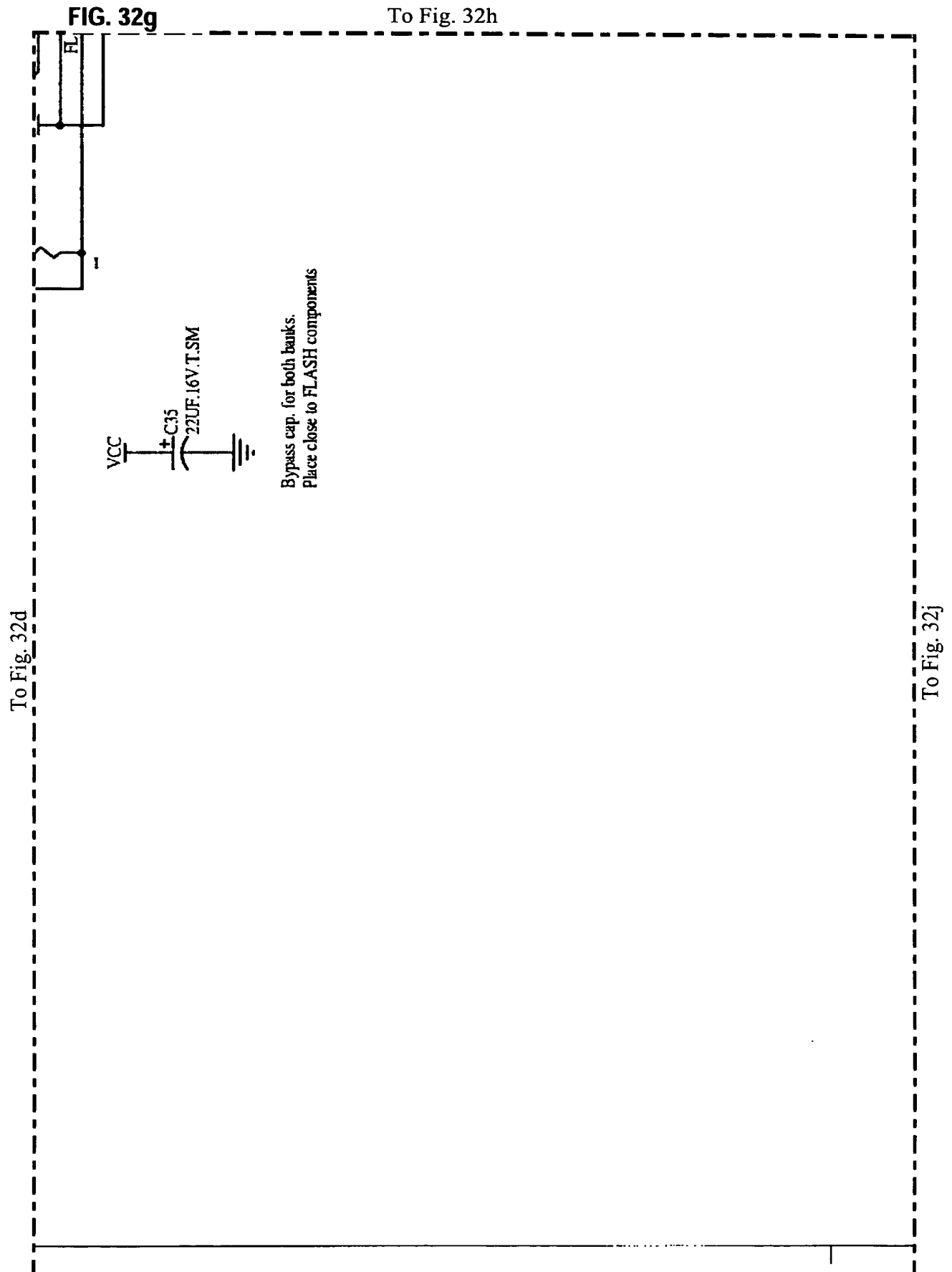
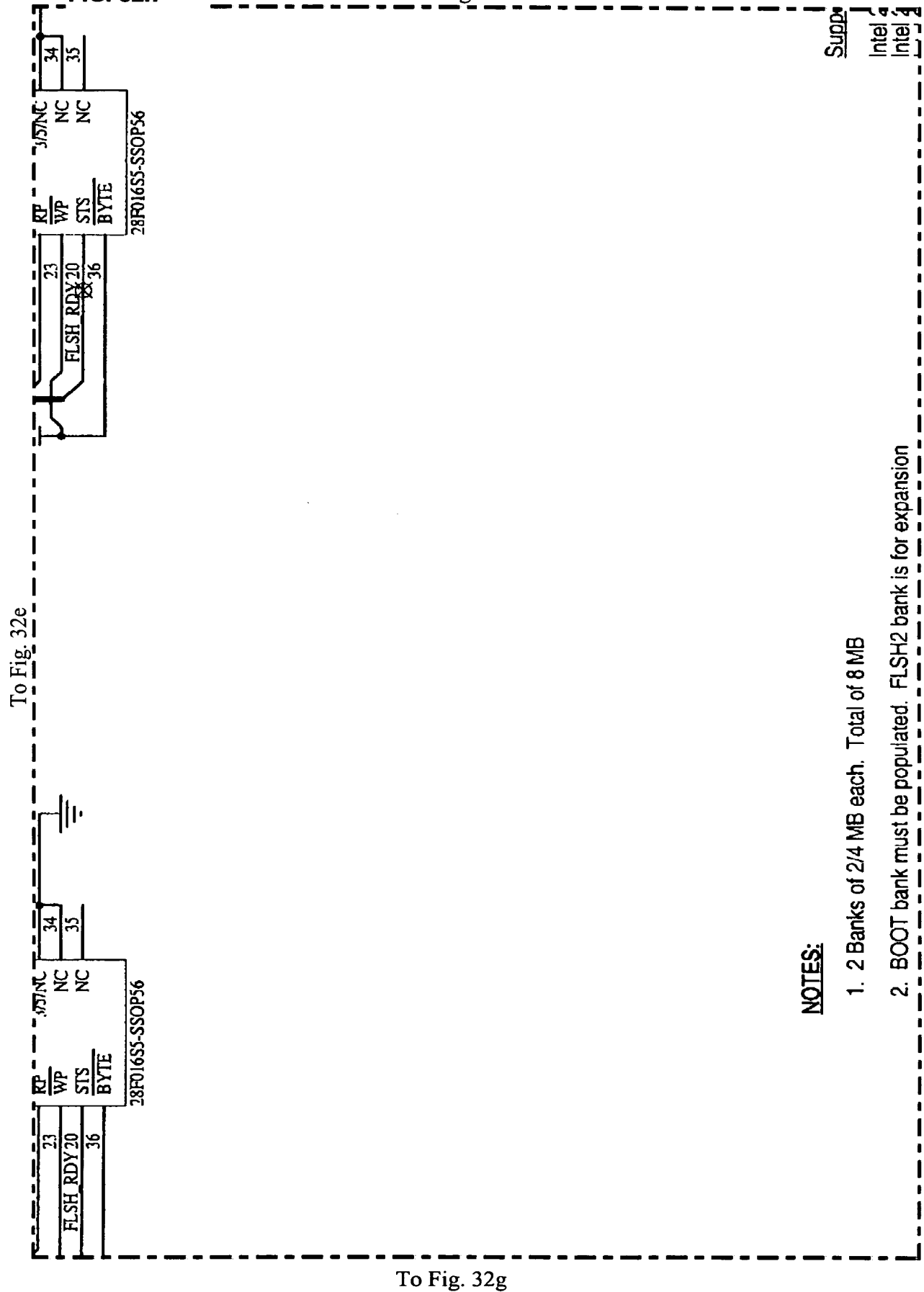


FIG. 32h



NOTES:

1. 2 Banks of 2/4 MB each. Total of 8 MB
2. BOOT bank must be populated. FLSH2 bank is for expansion

FIG. 32i

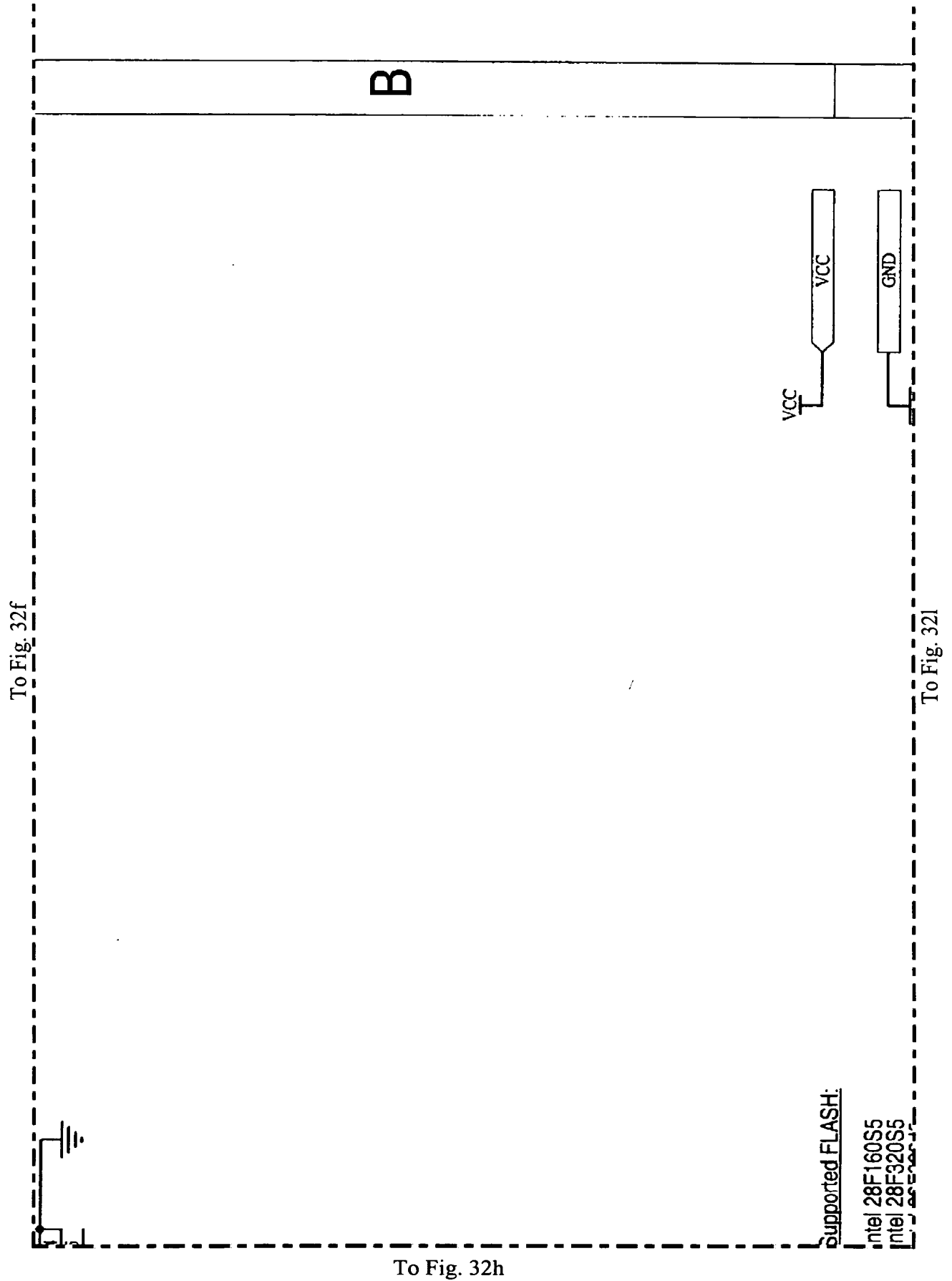


FIG. 32j

To Fig. 32k

To Fig. 32g

2

1

FIG. 32k

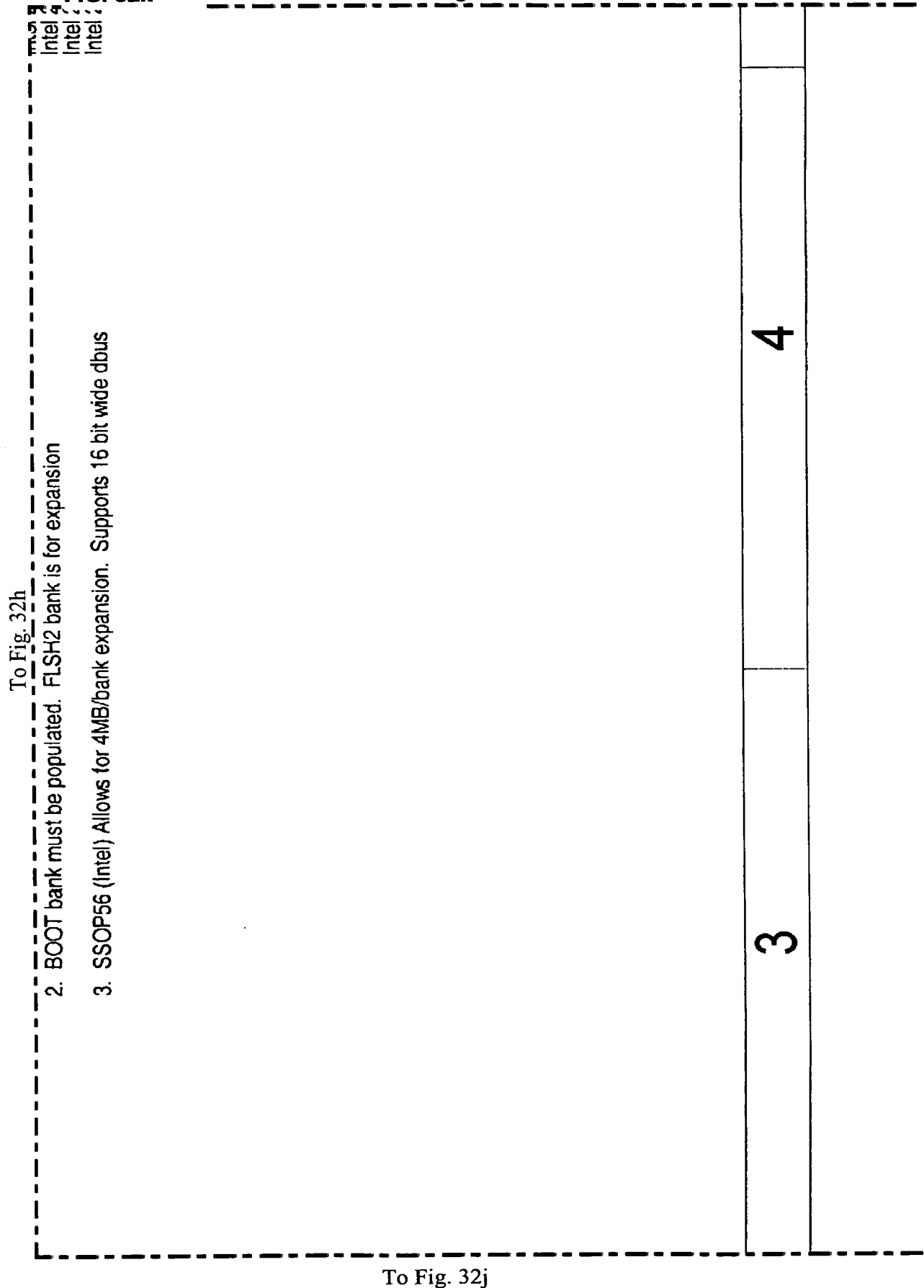
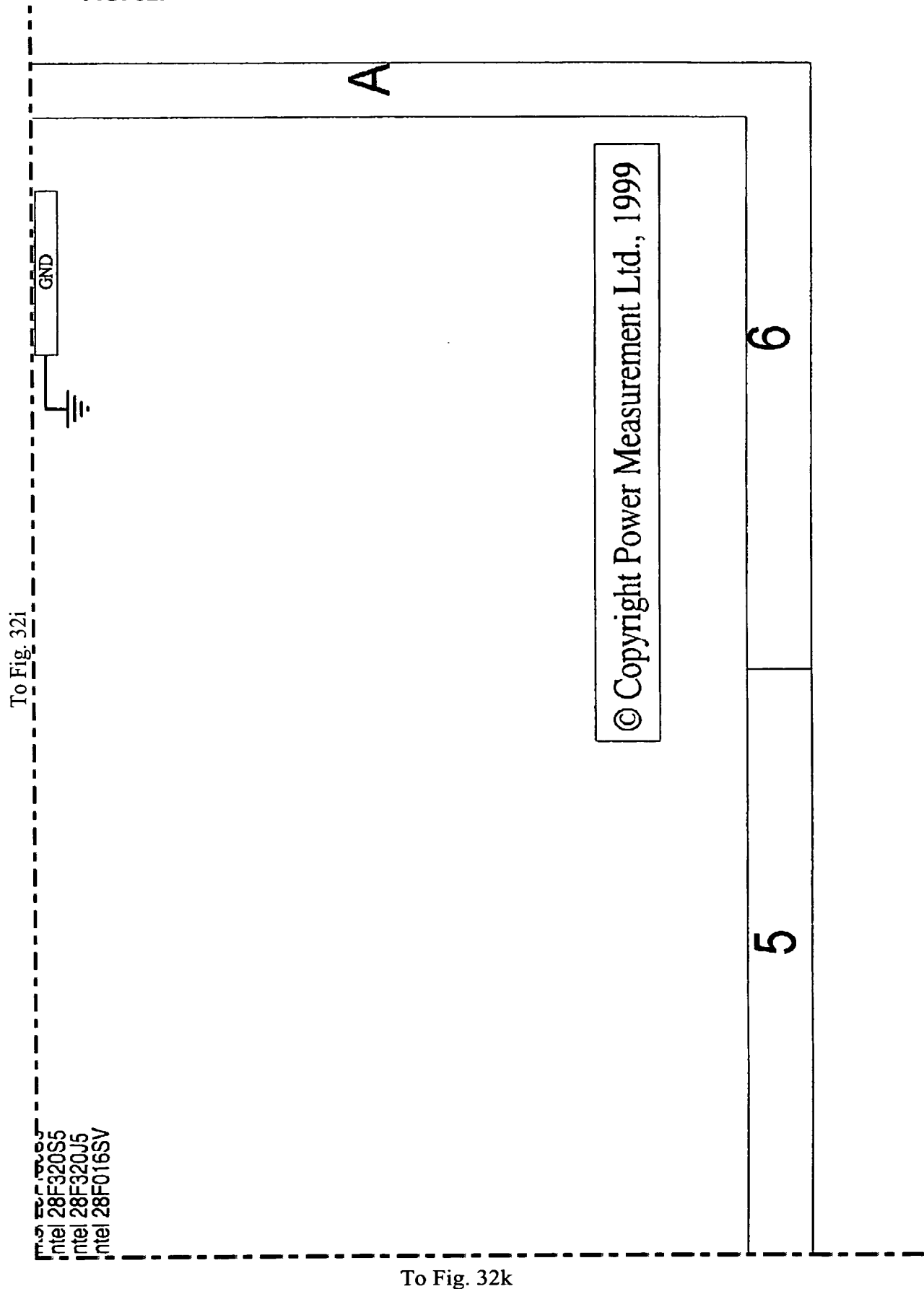
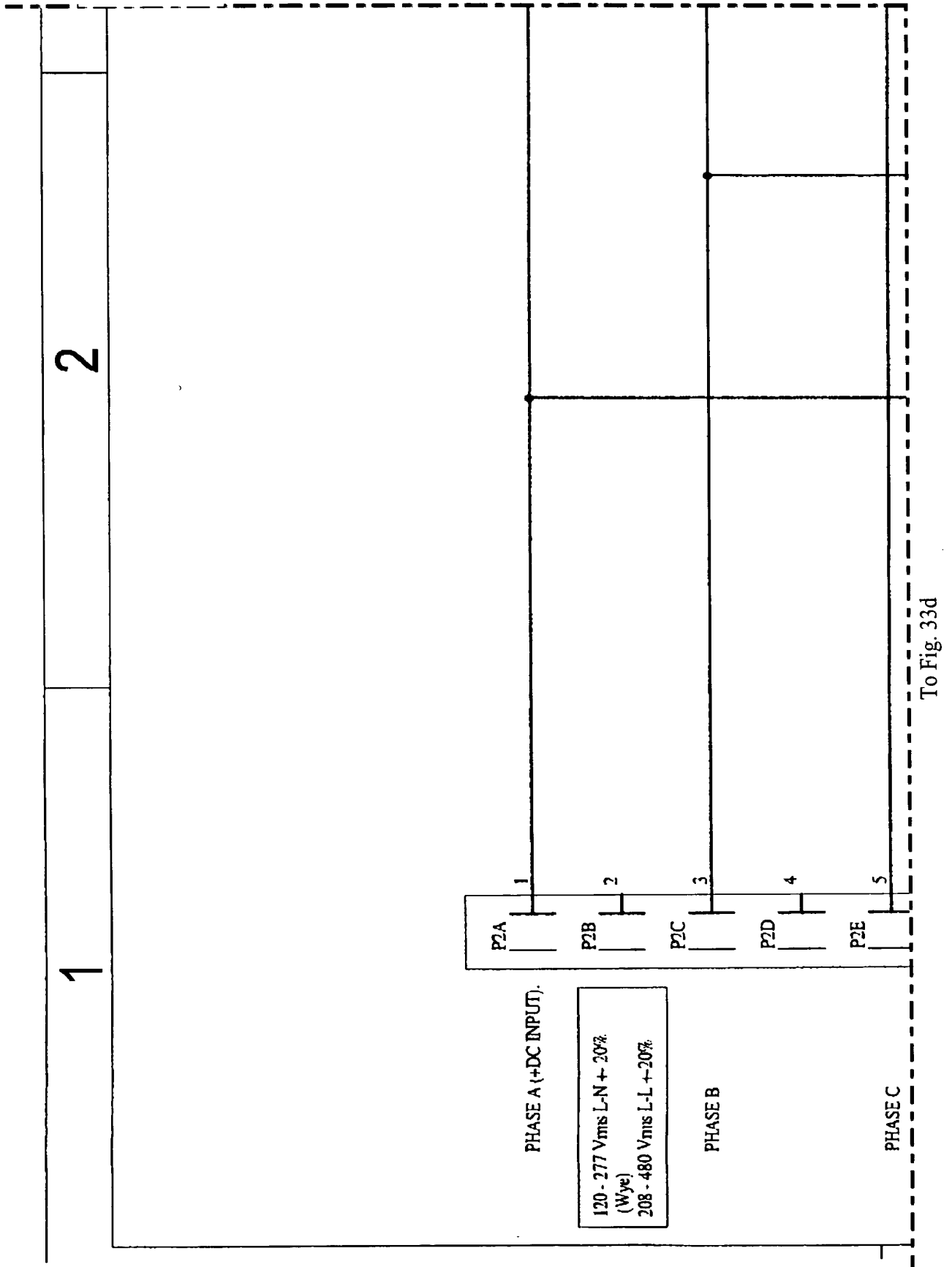


FIG. 32l



To Fig. 33b



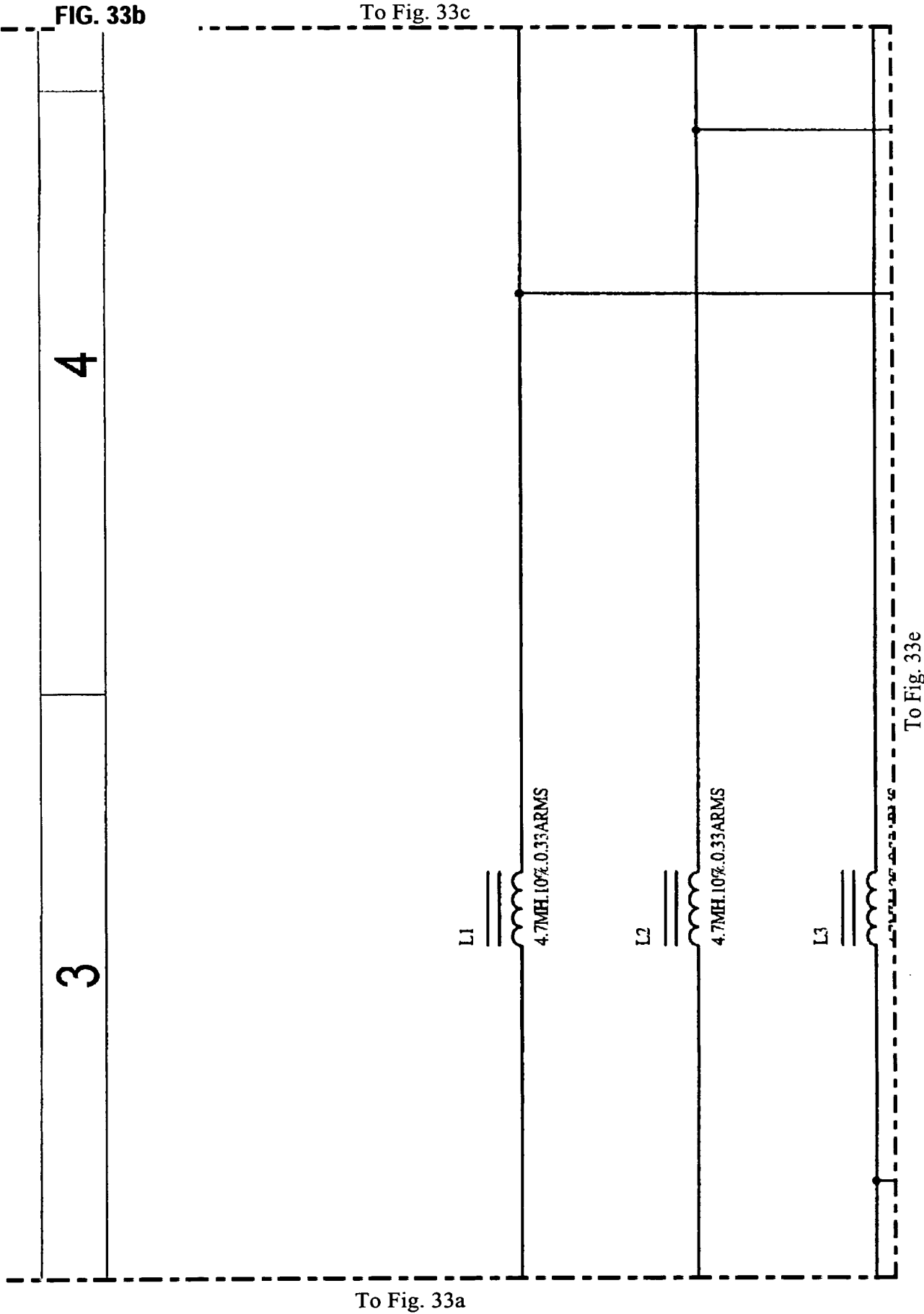
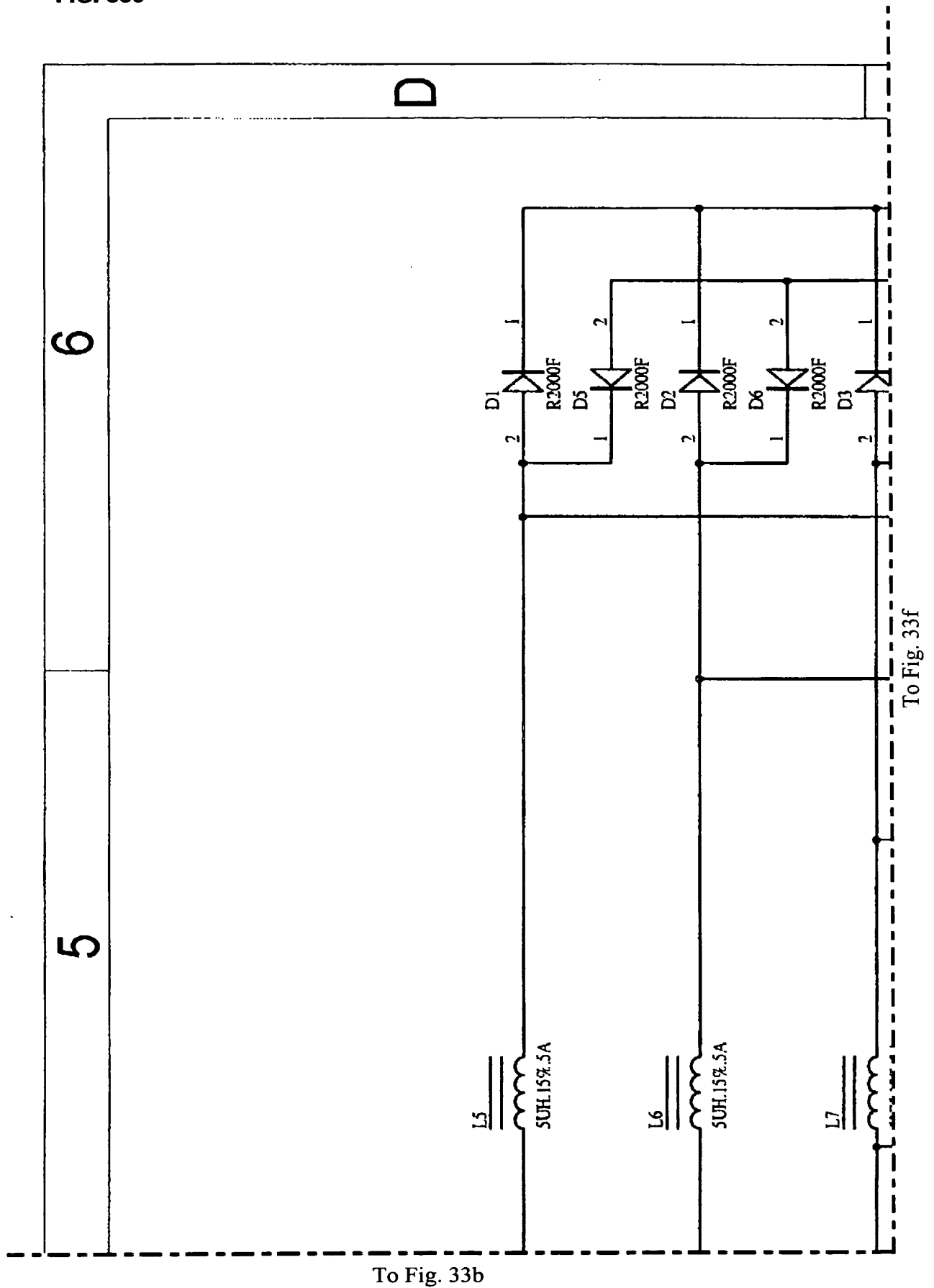
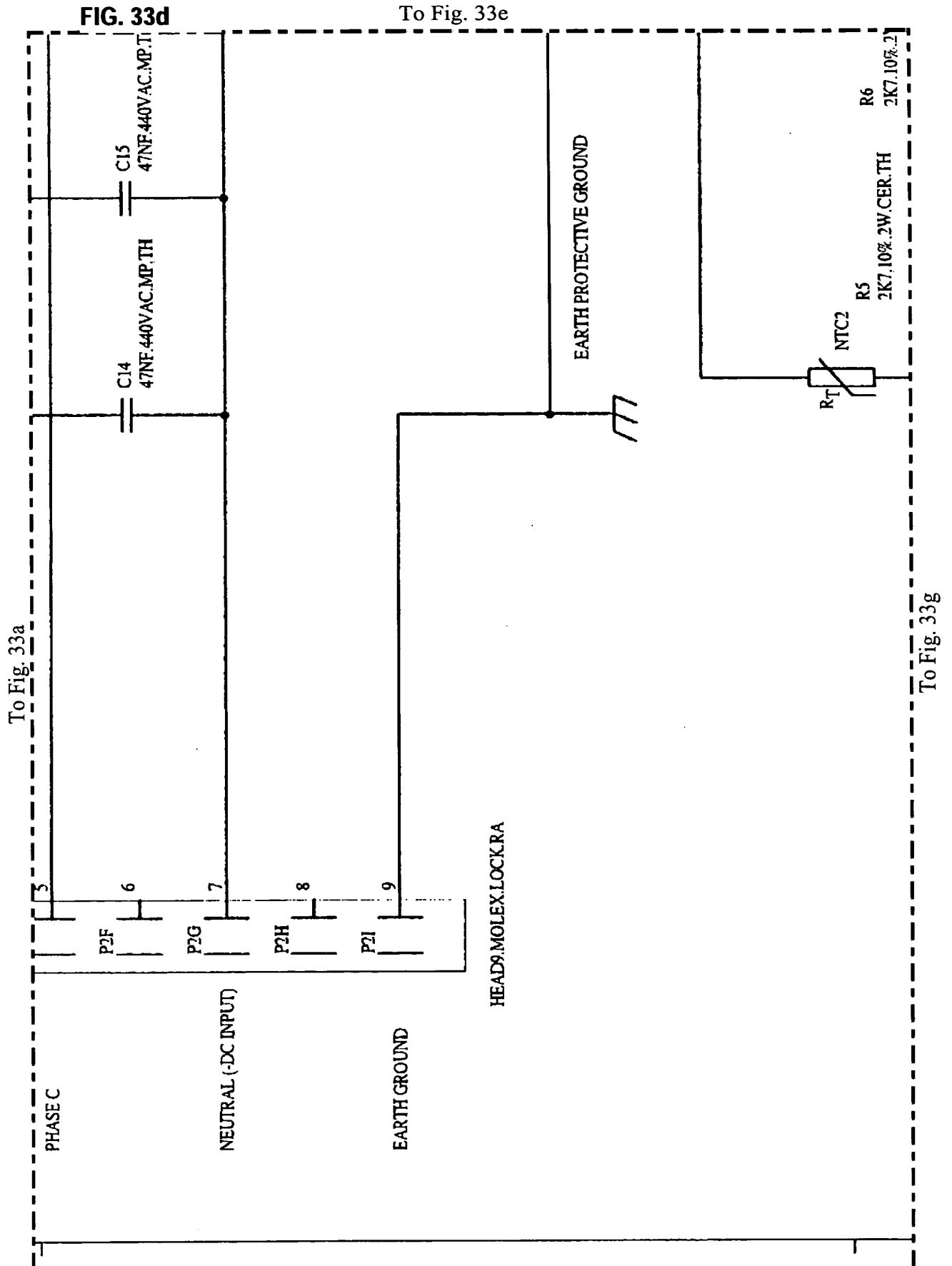


FIG. 33c





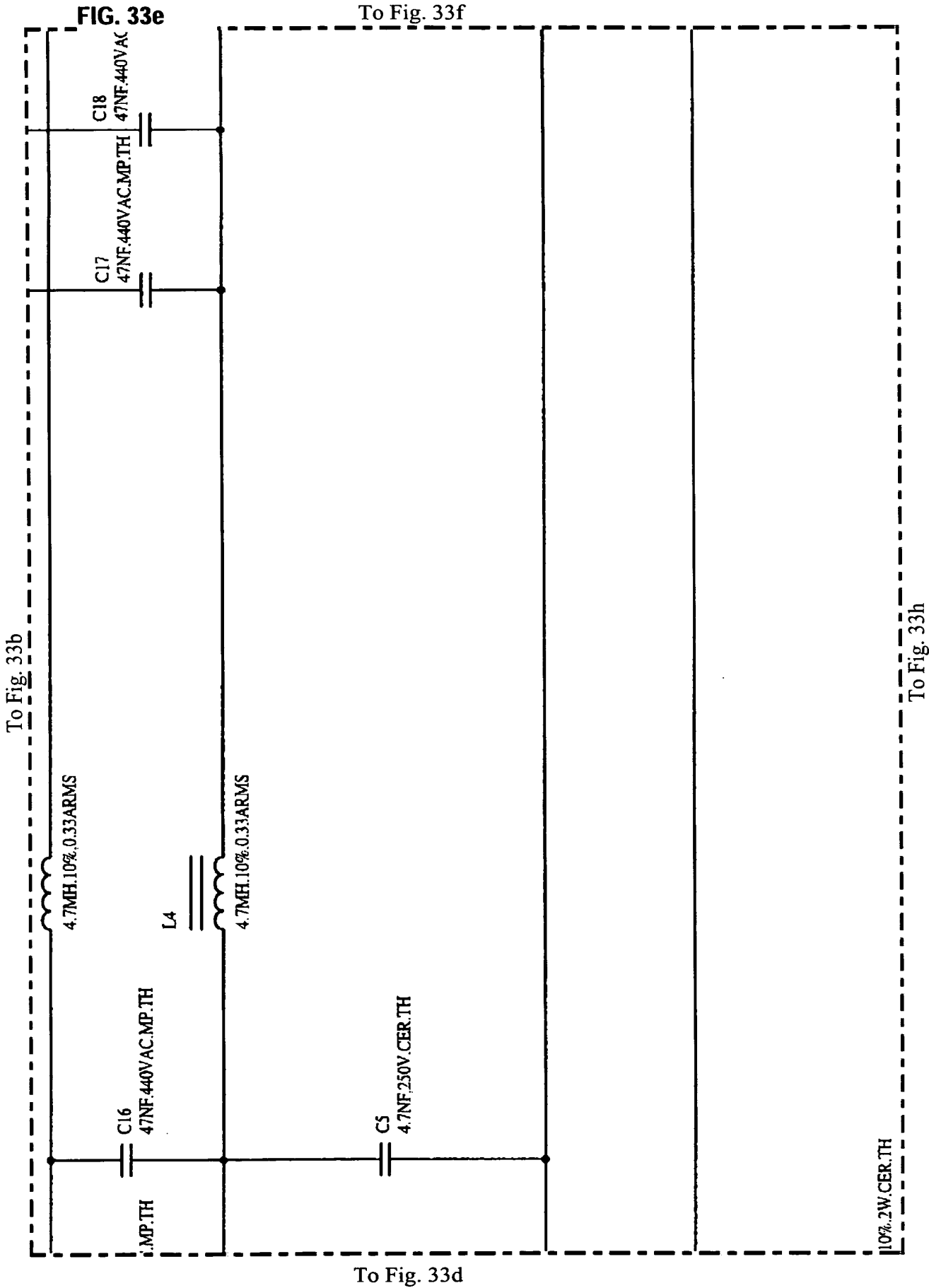
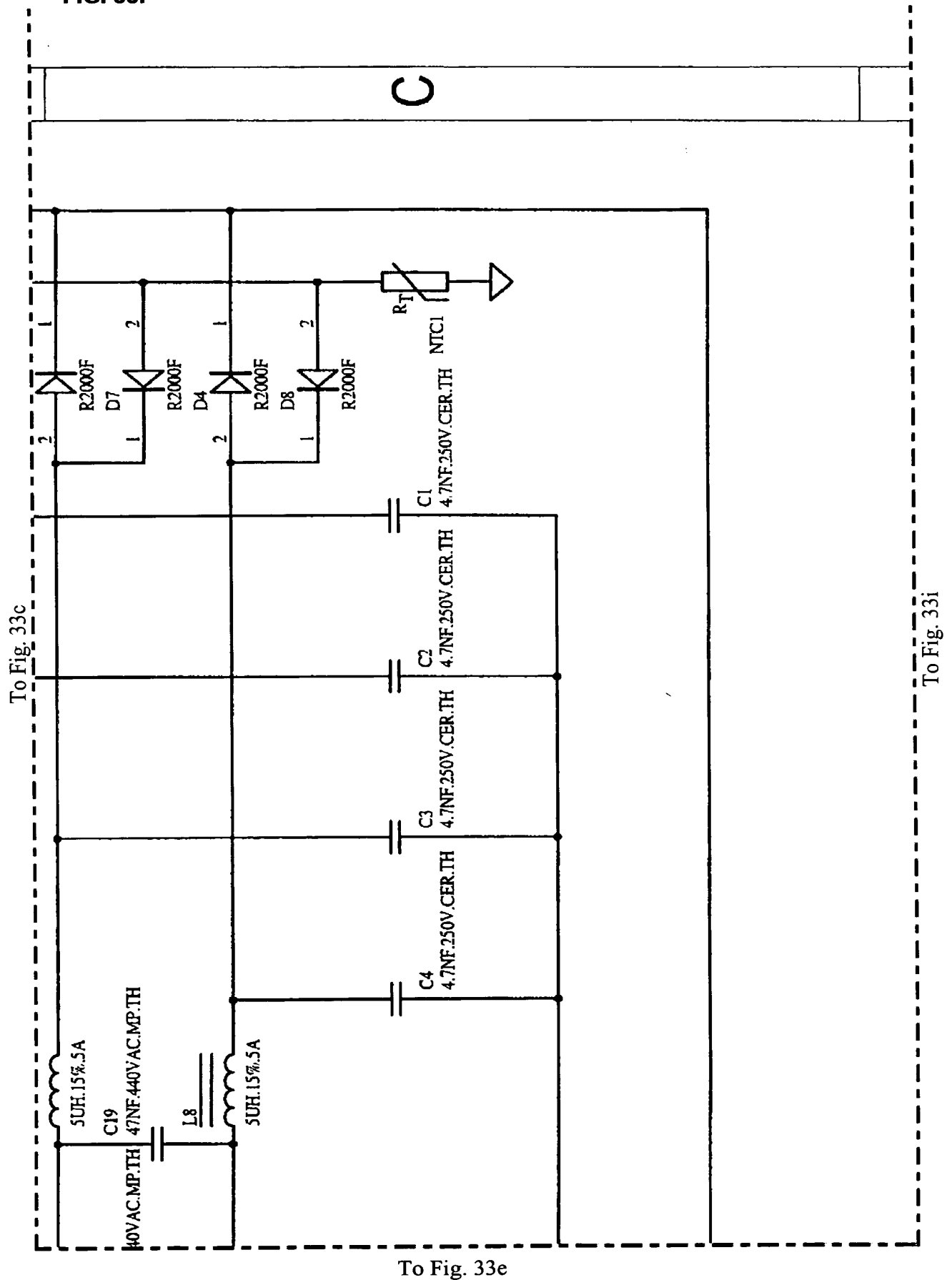
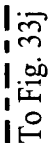


FIG. 33f





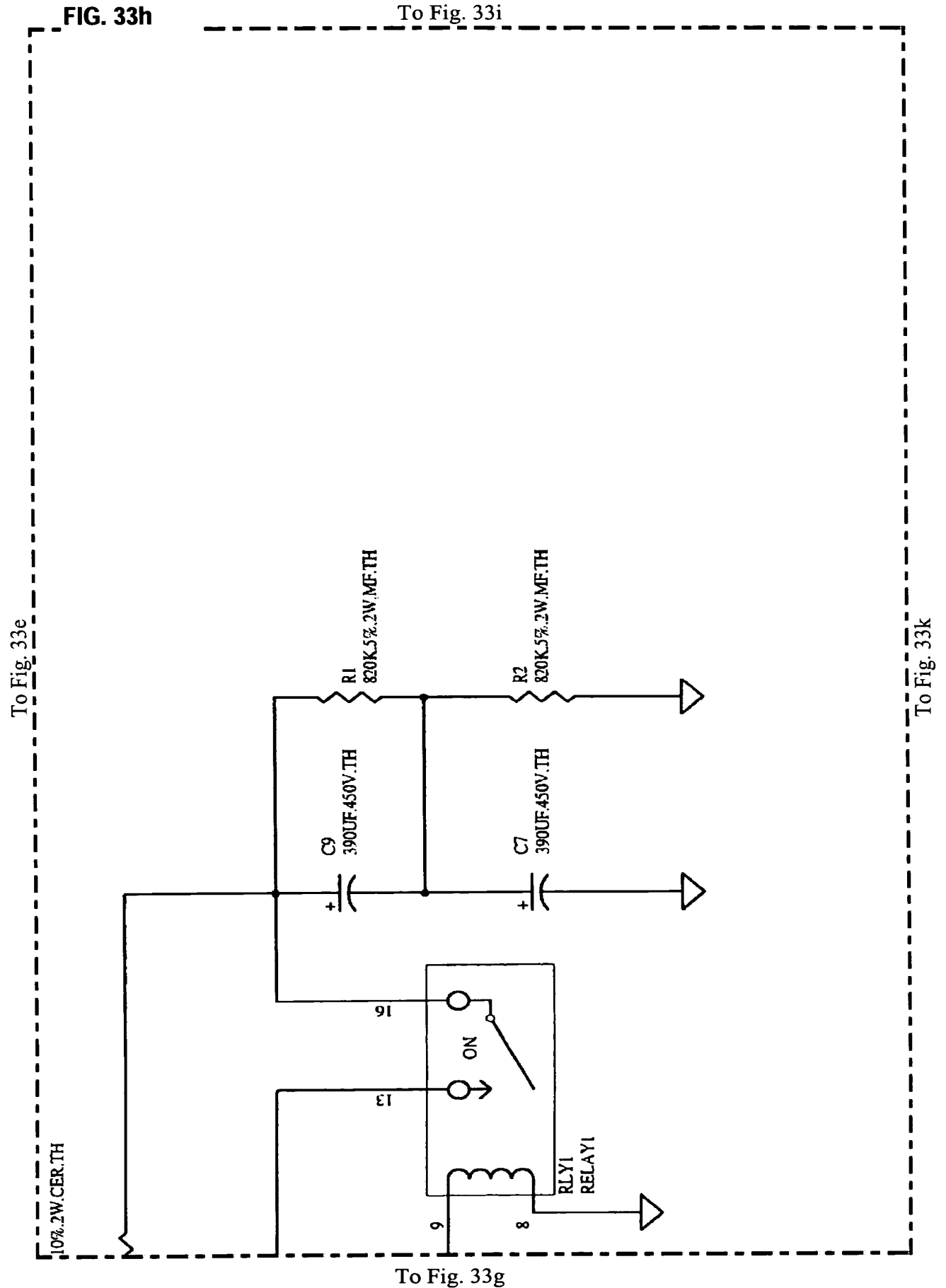
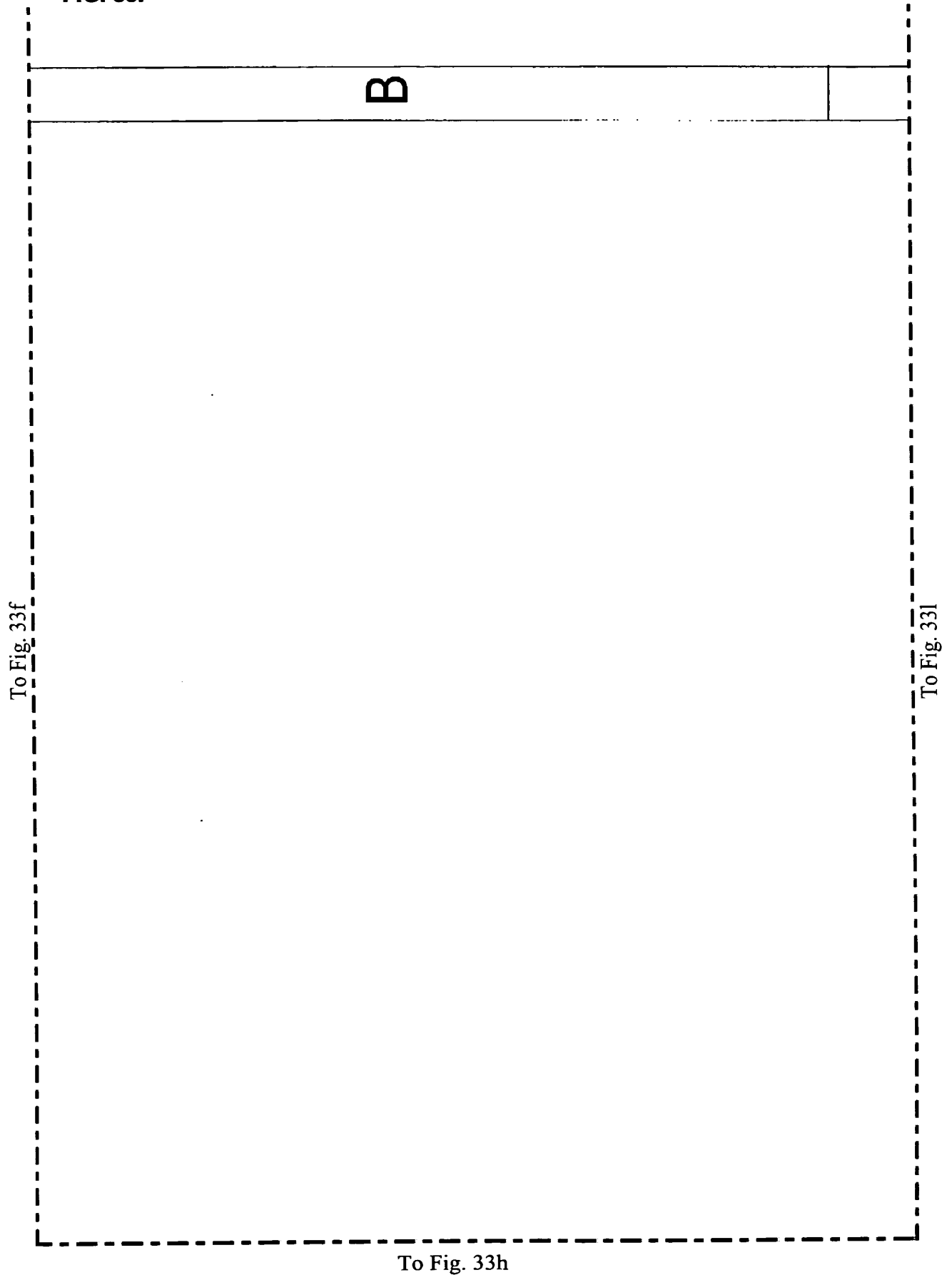
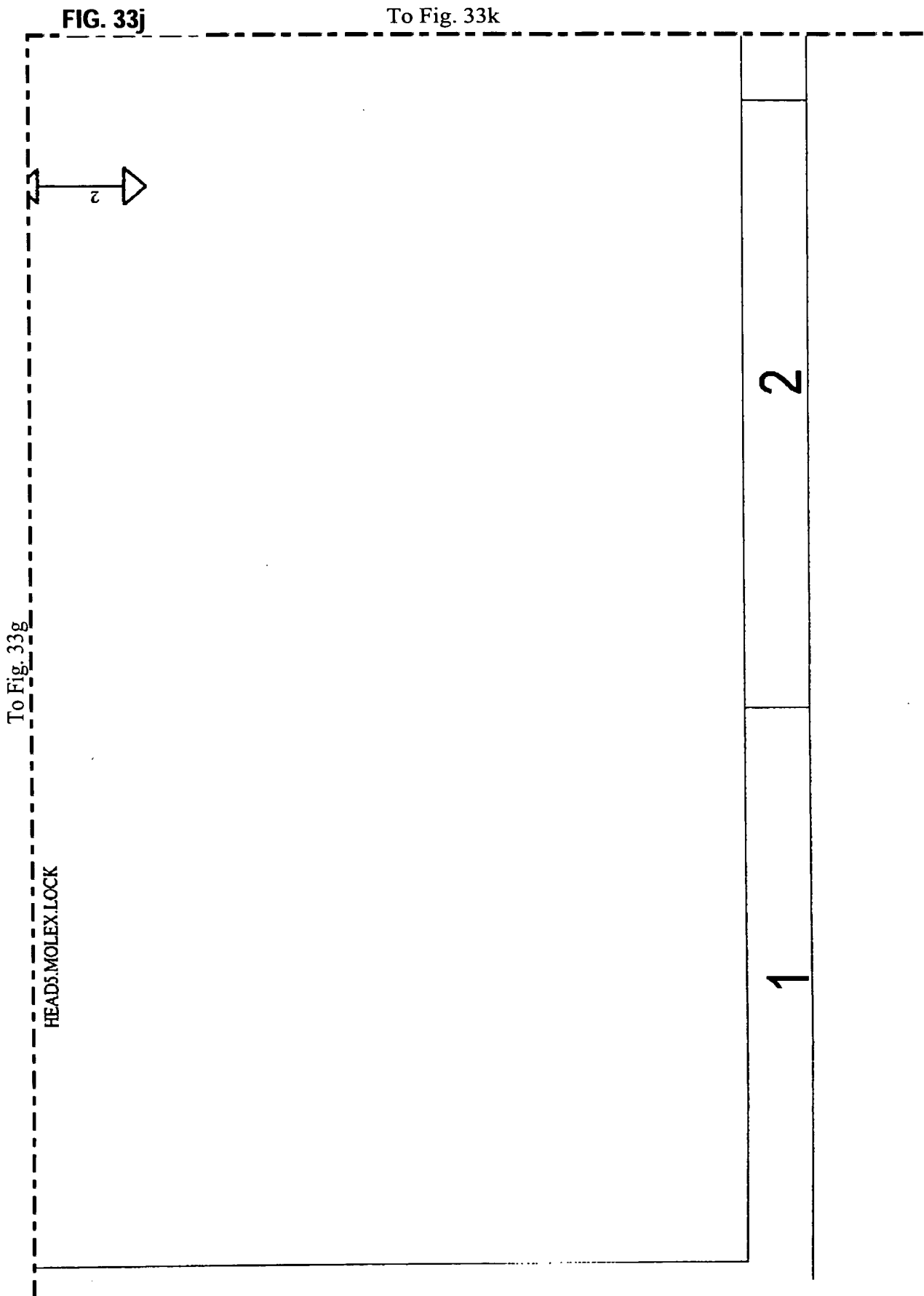


FIG. 33i





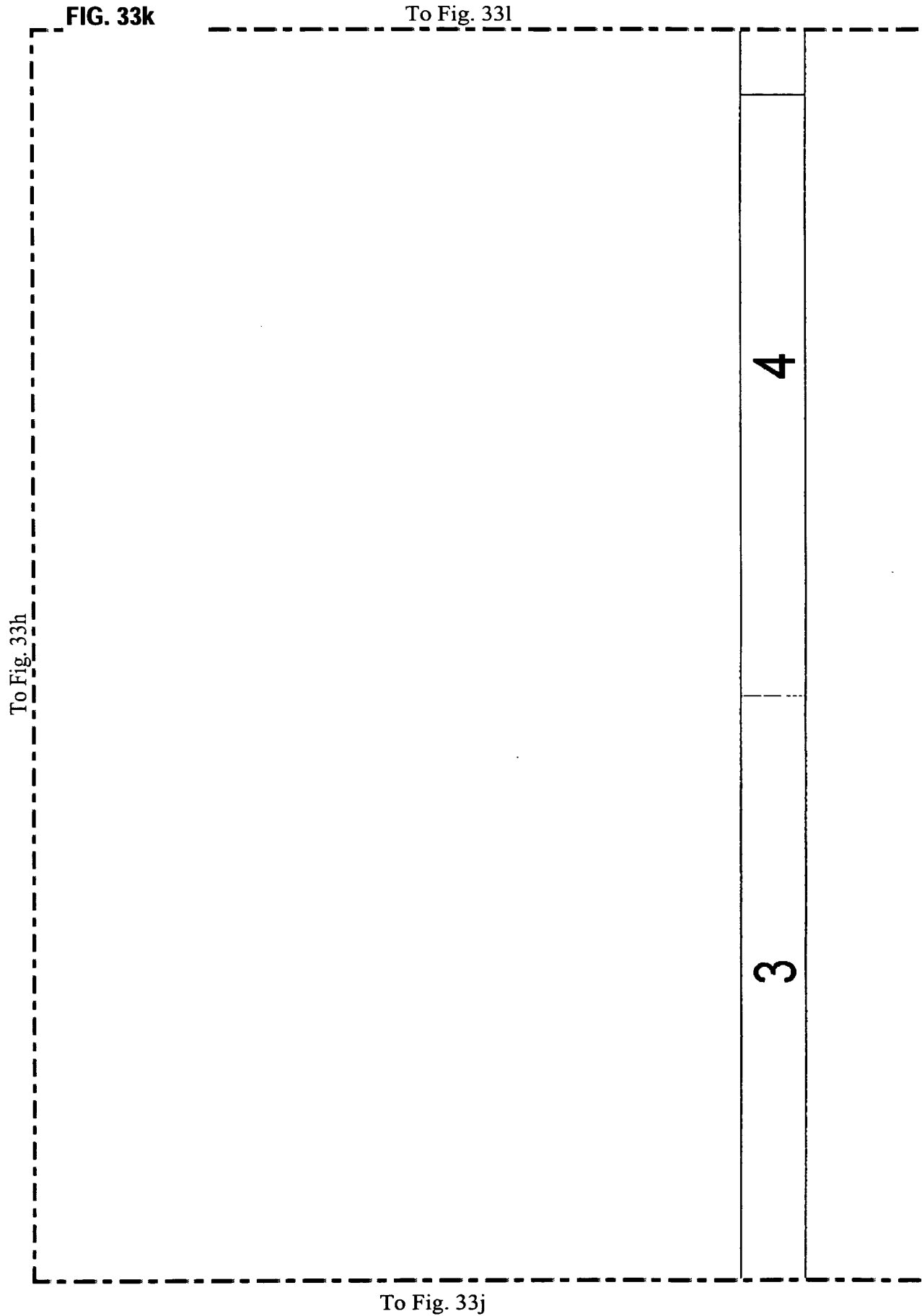
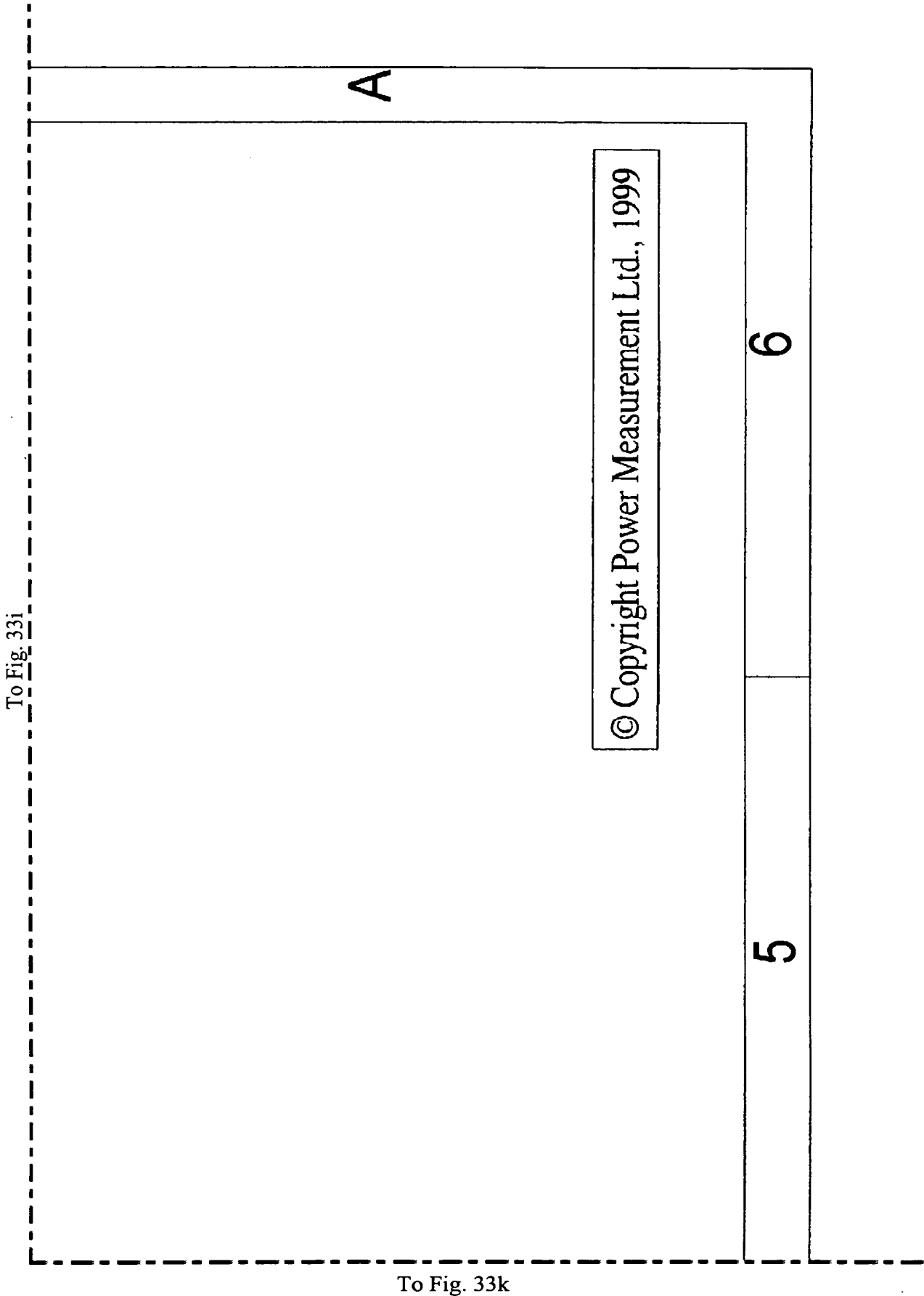


FIG. 33i



To Fig. 34b

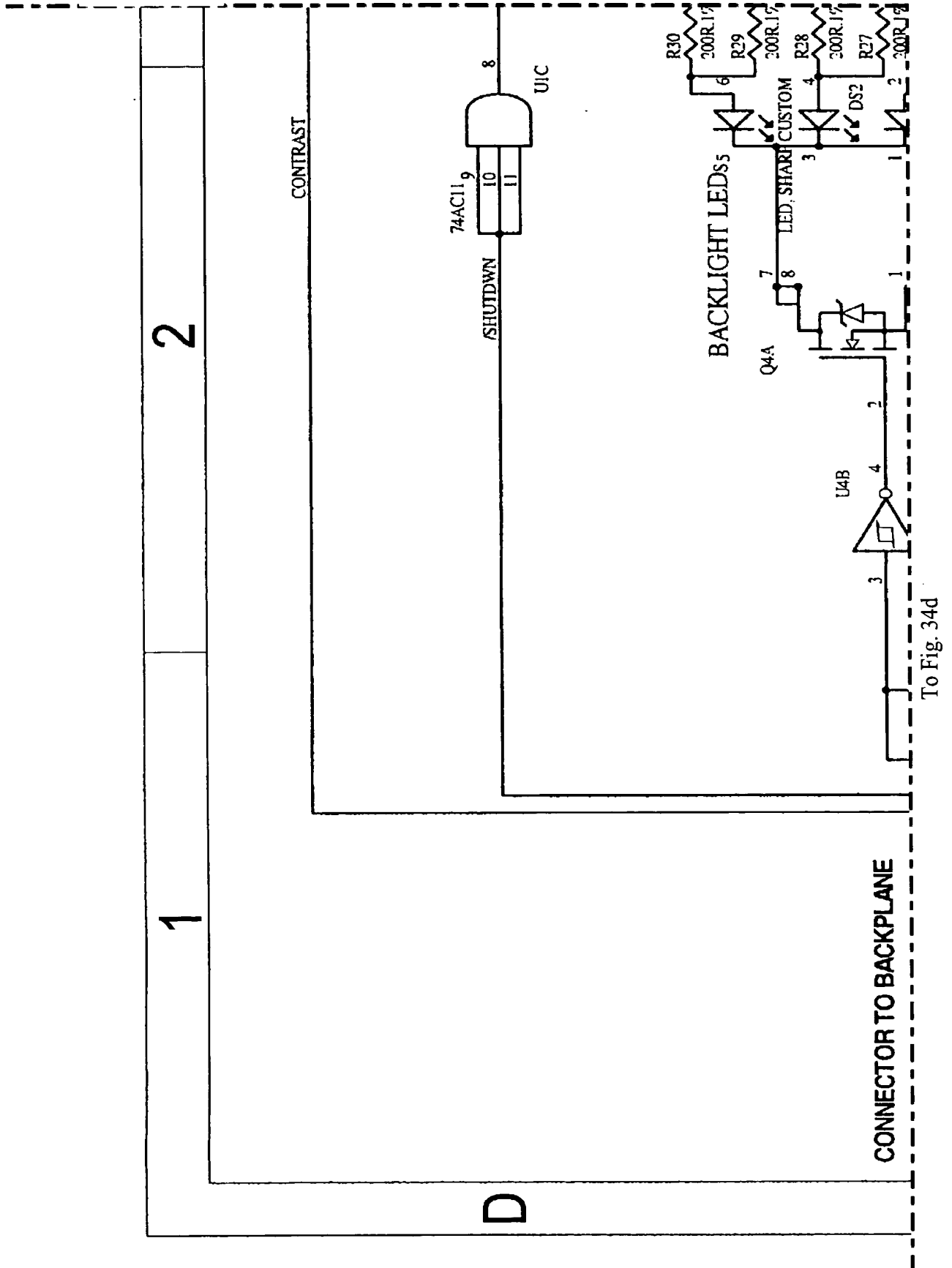
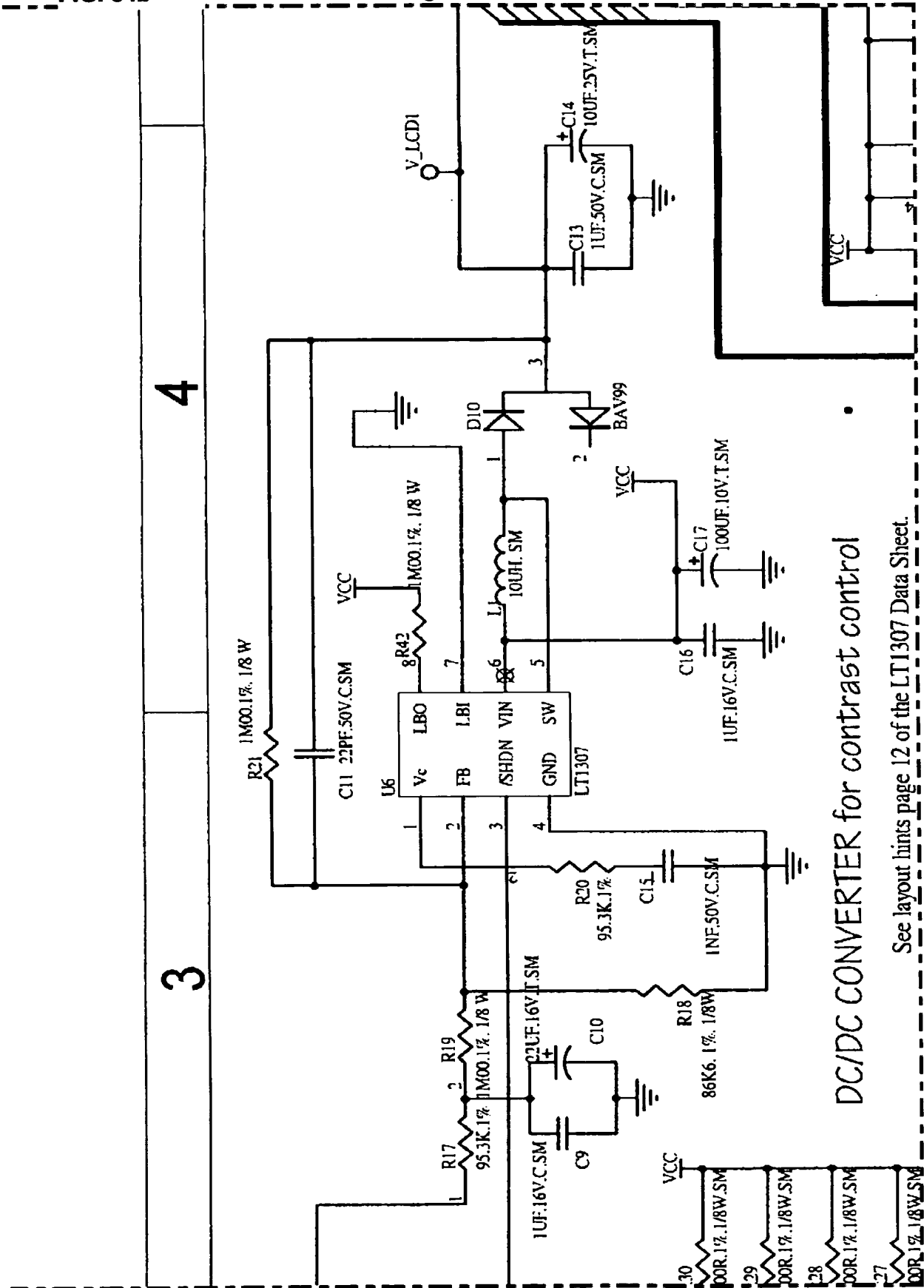


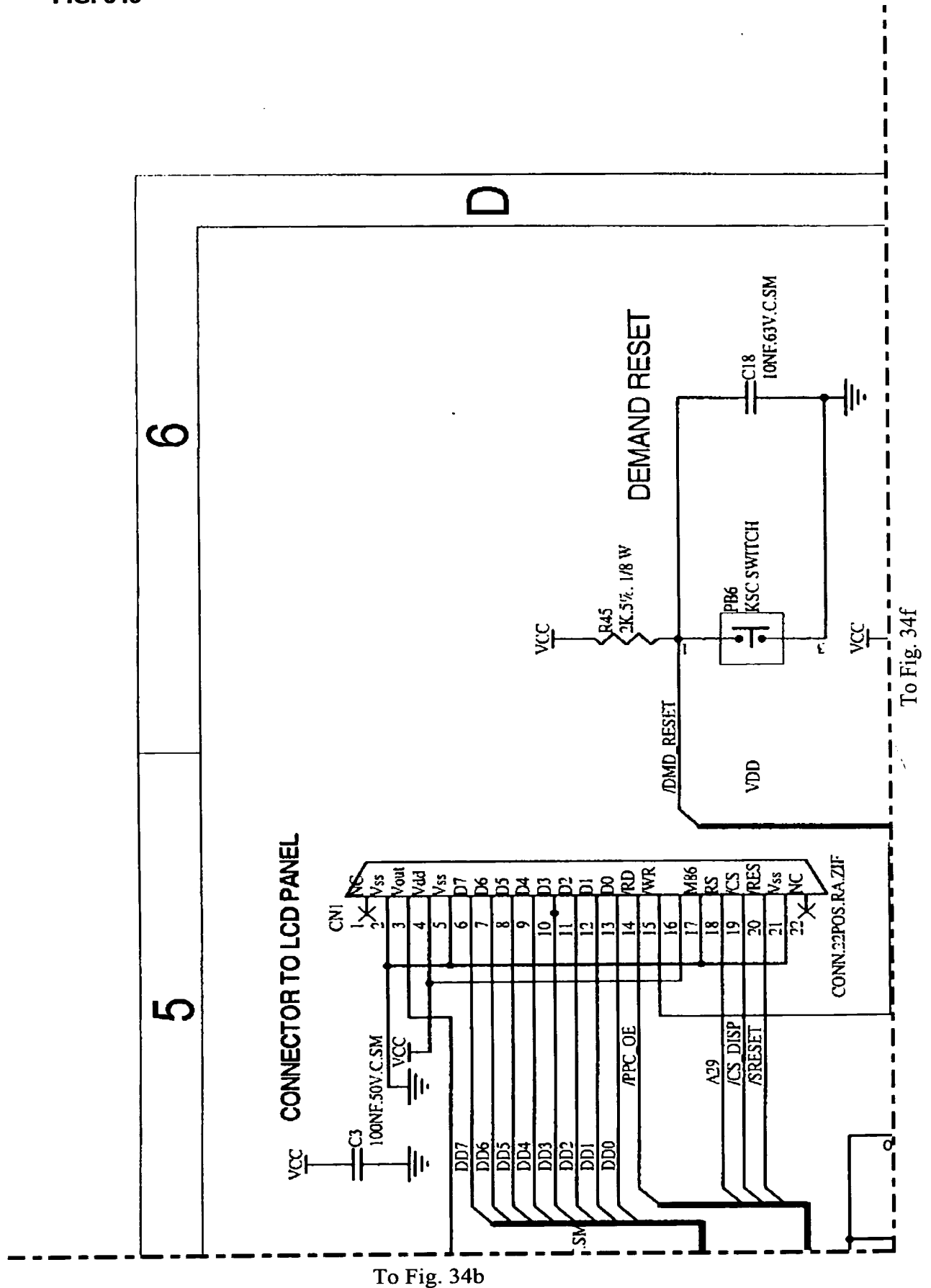
FIG. 34b

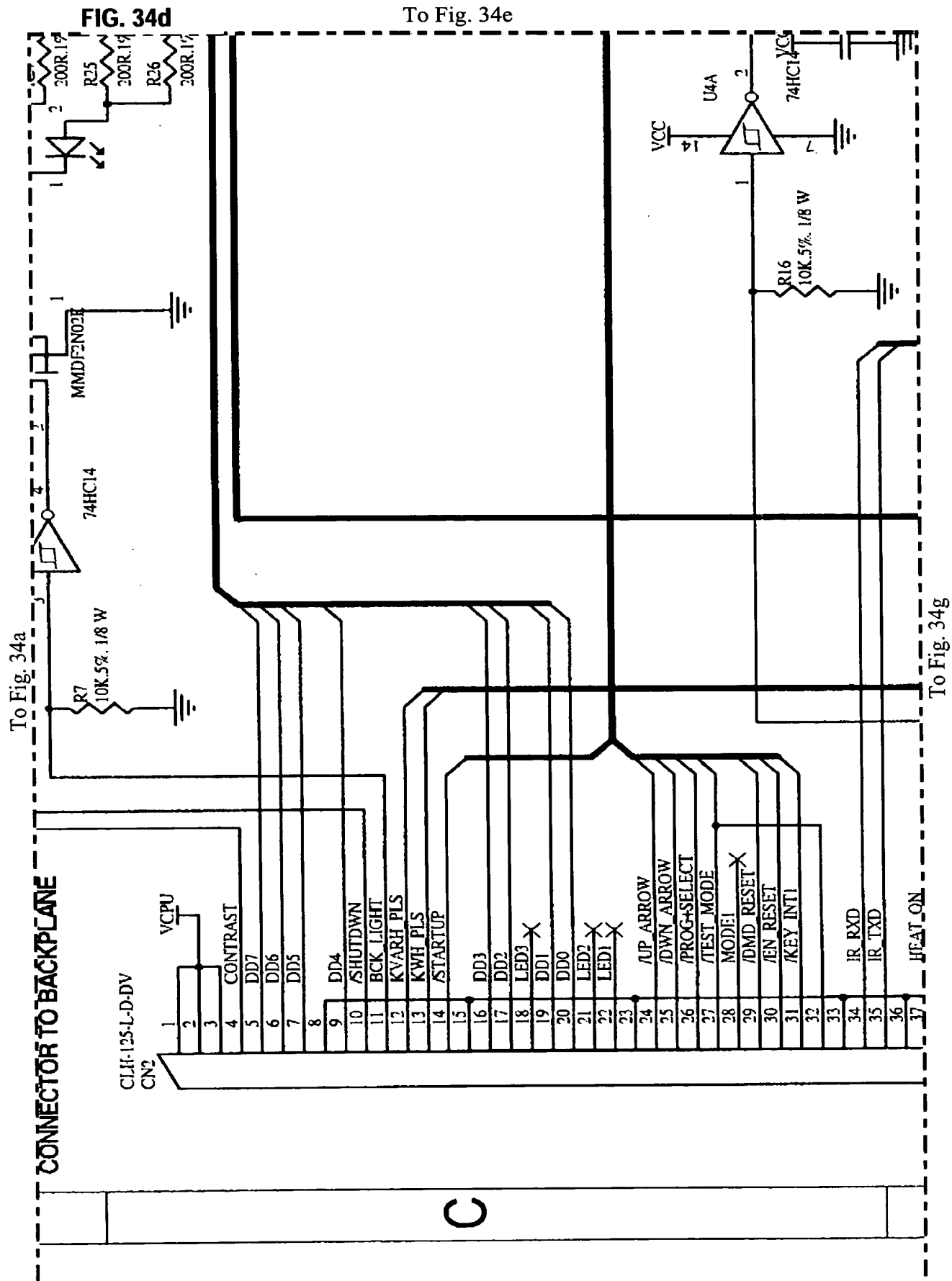
To Fig. 34c



To Fig. 34a

FIG. 34c





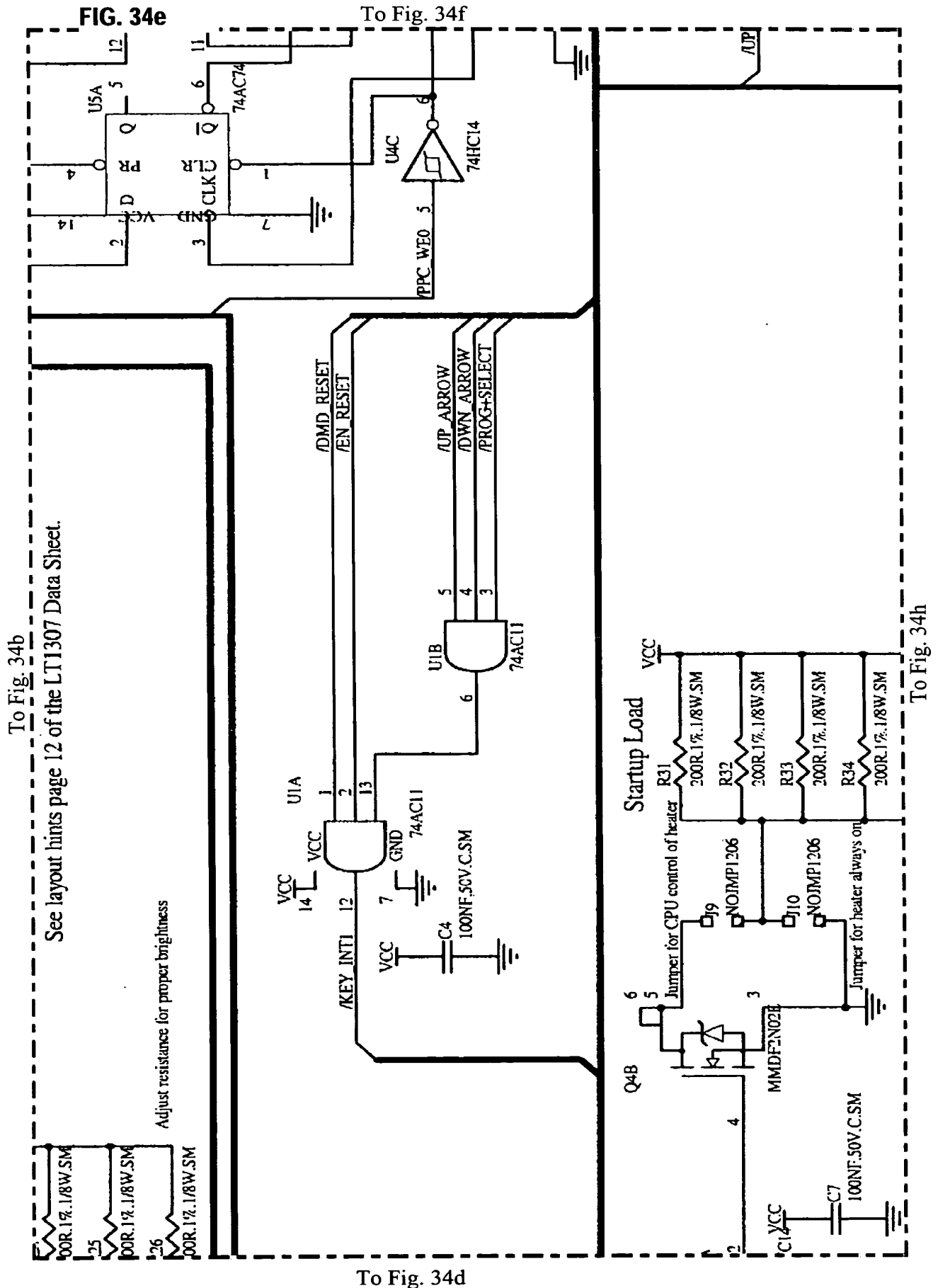
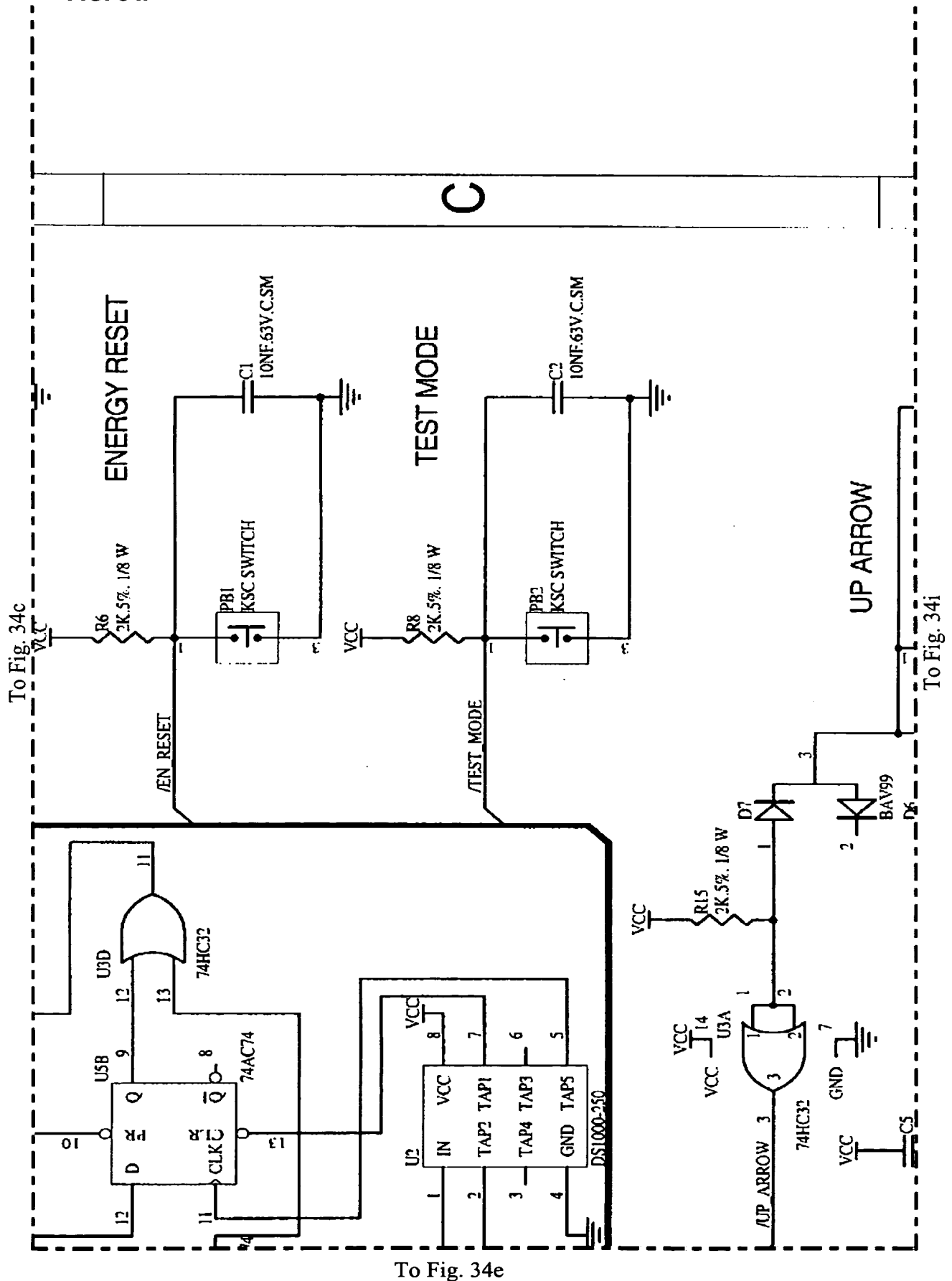
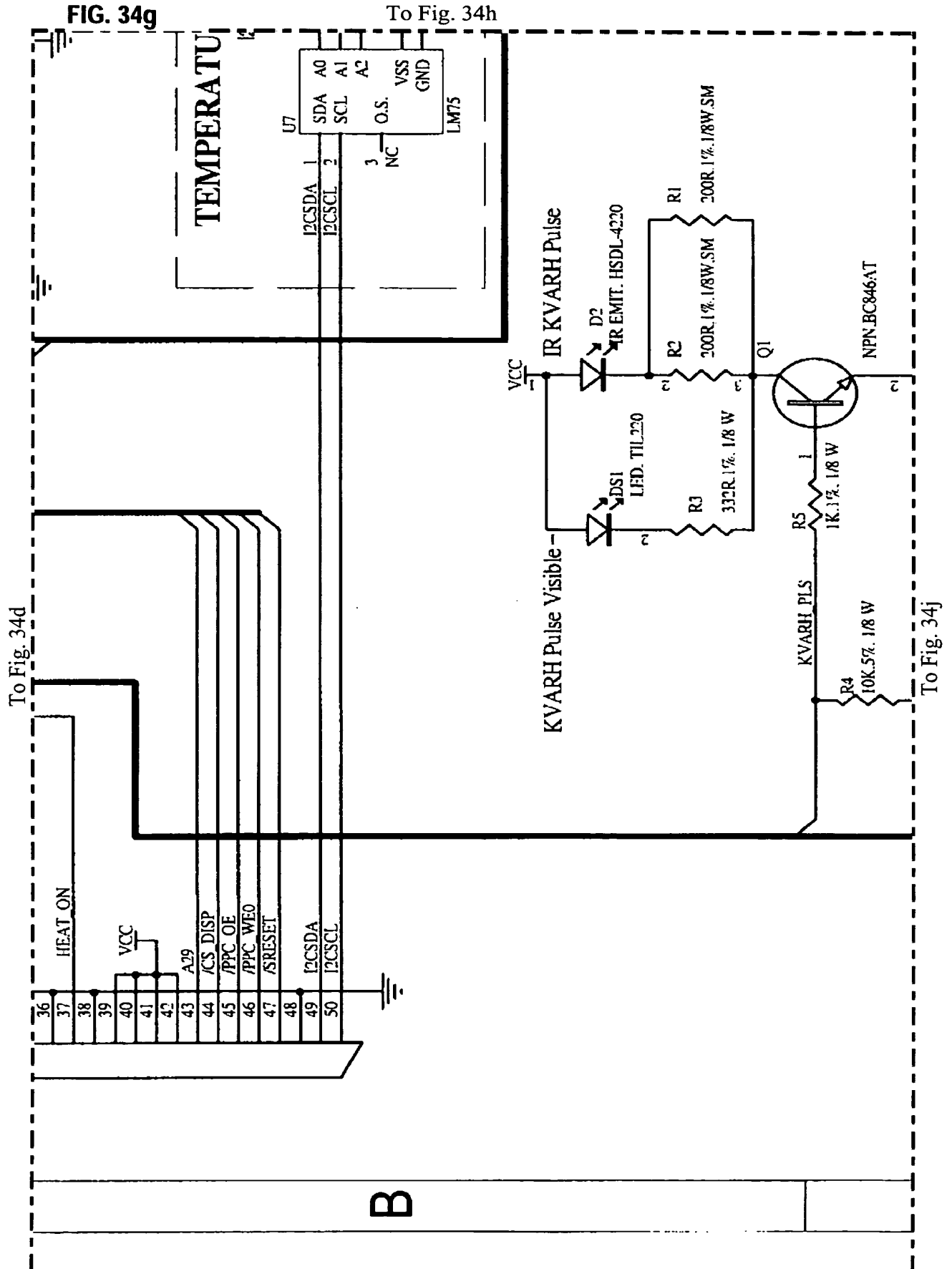


FIG. 34f





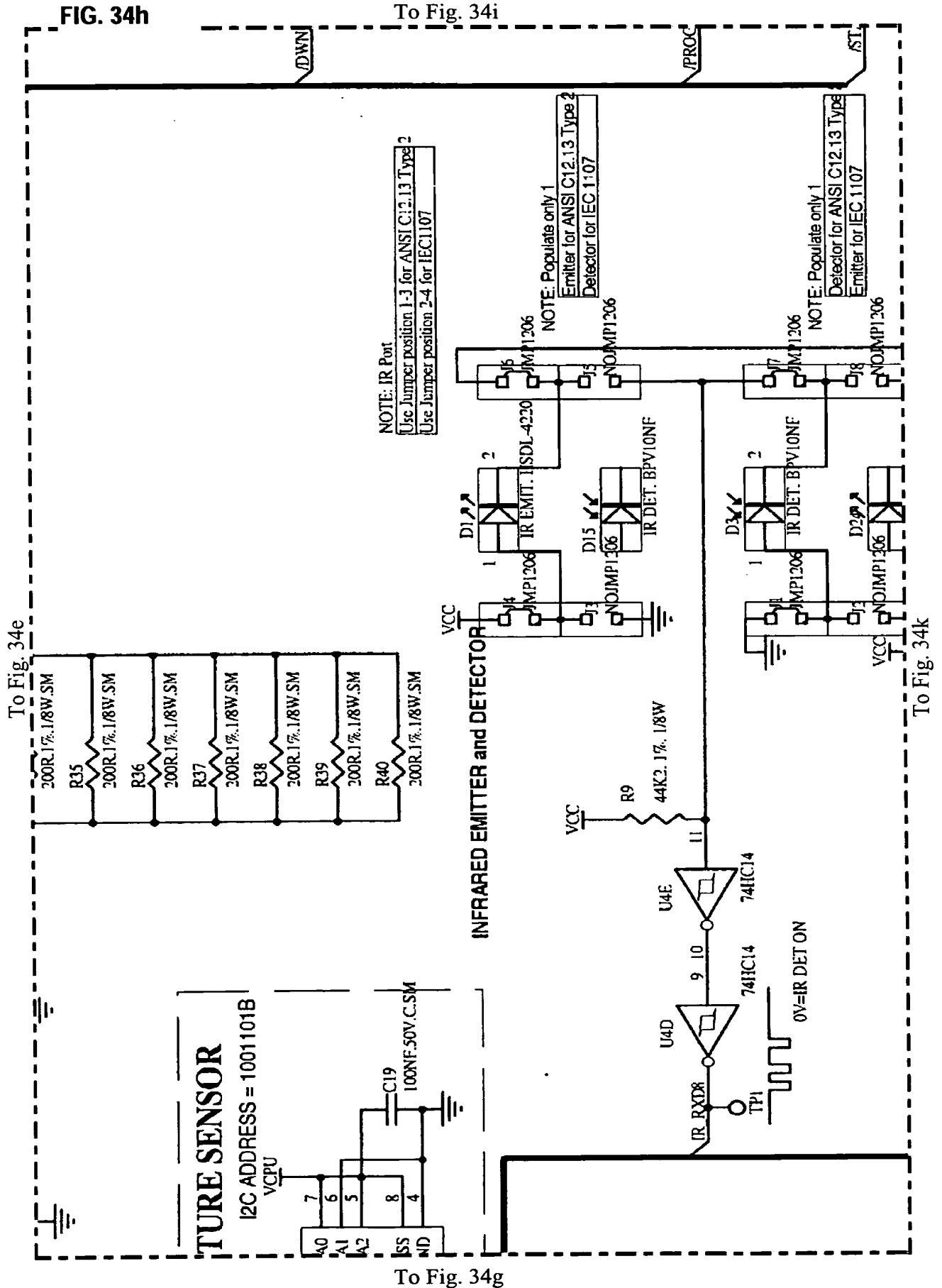
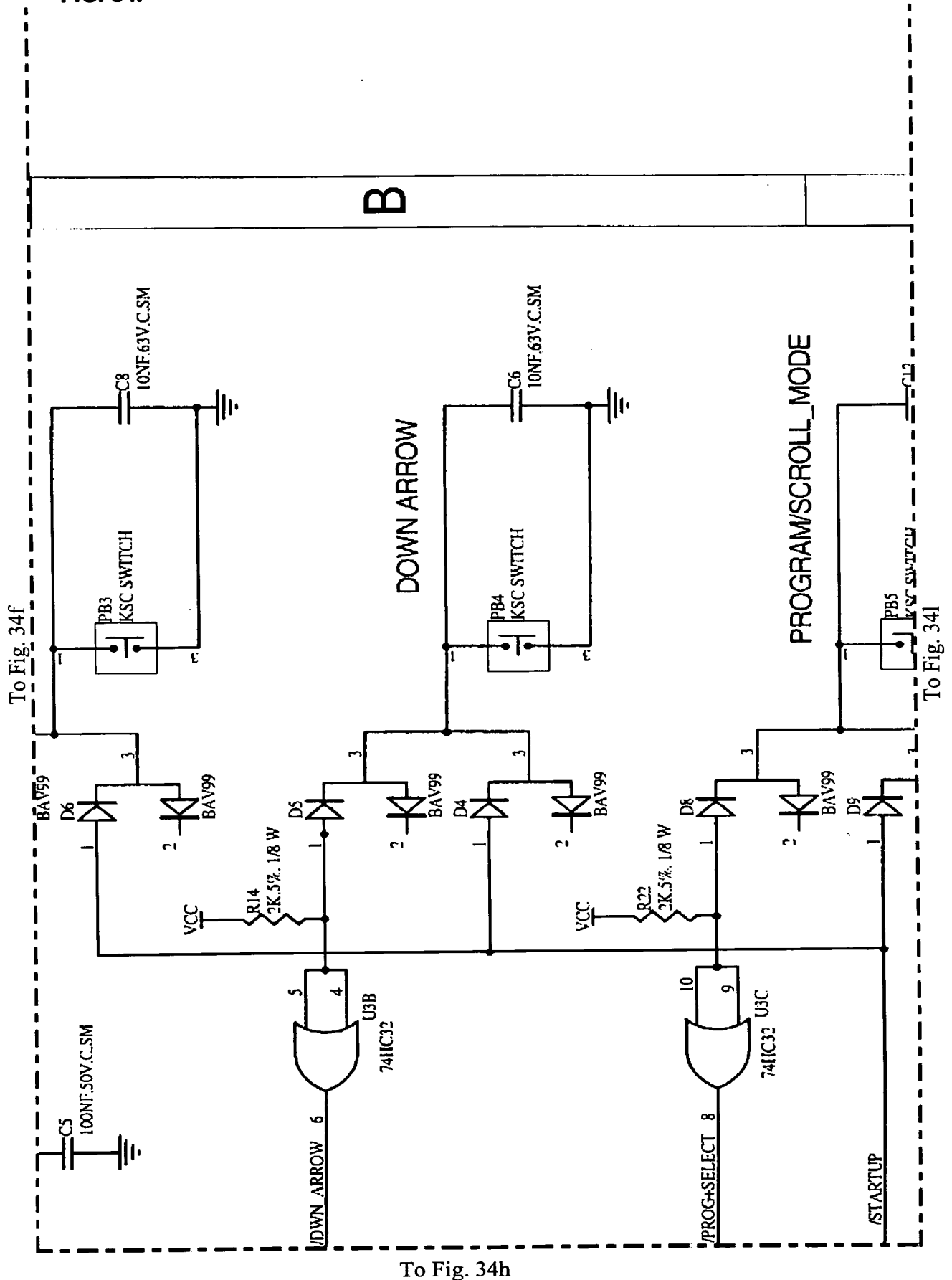
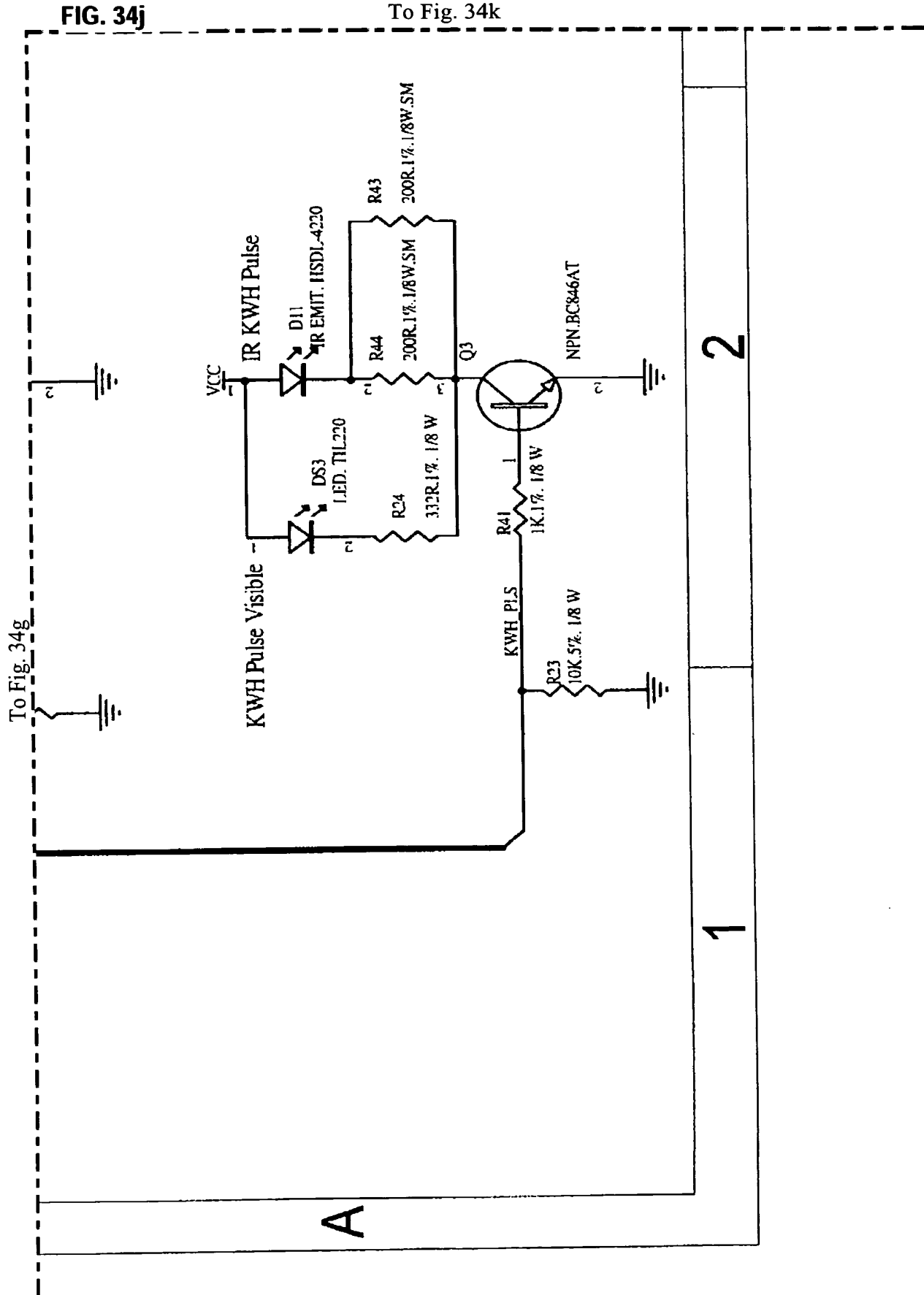


FIG. 34i





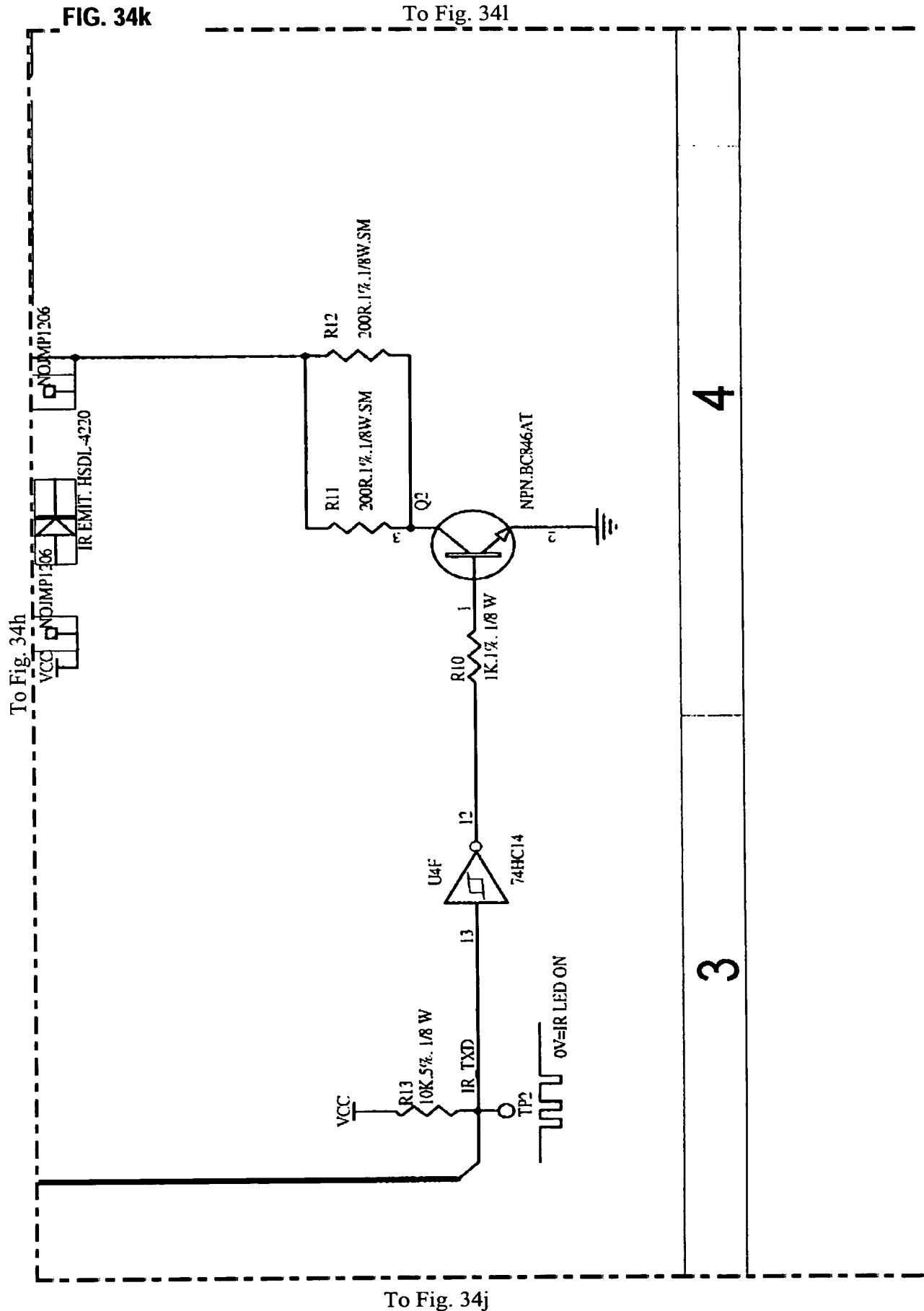


FIG. 34I

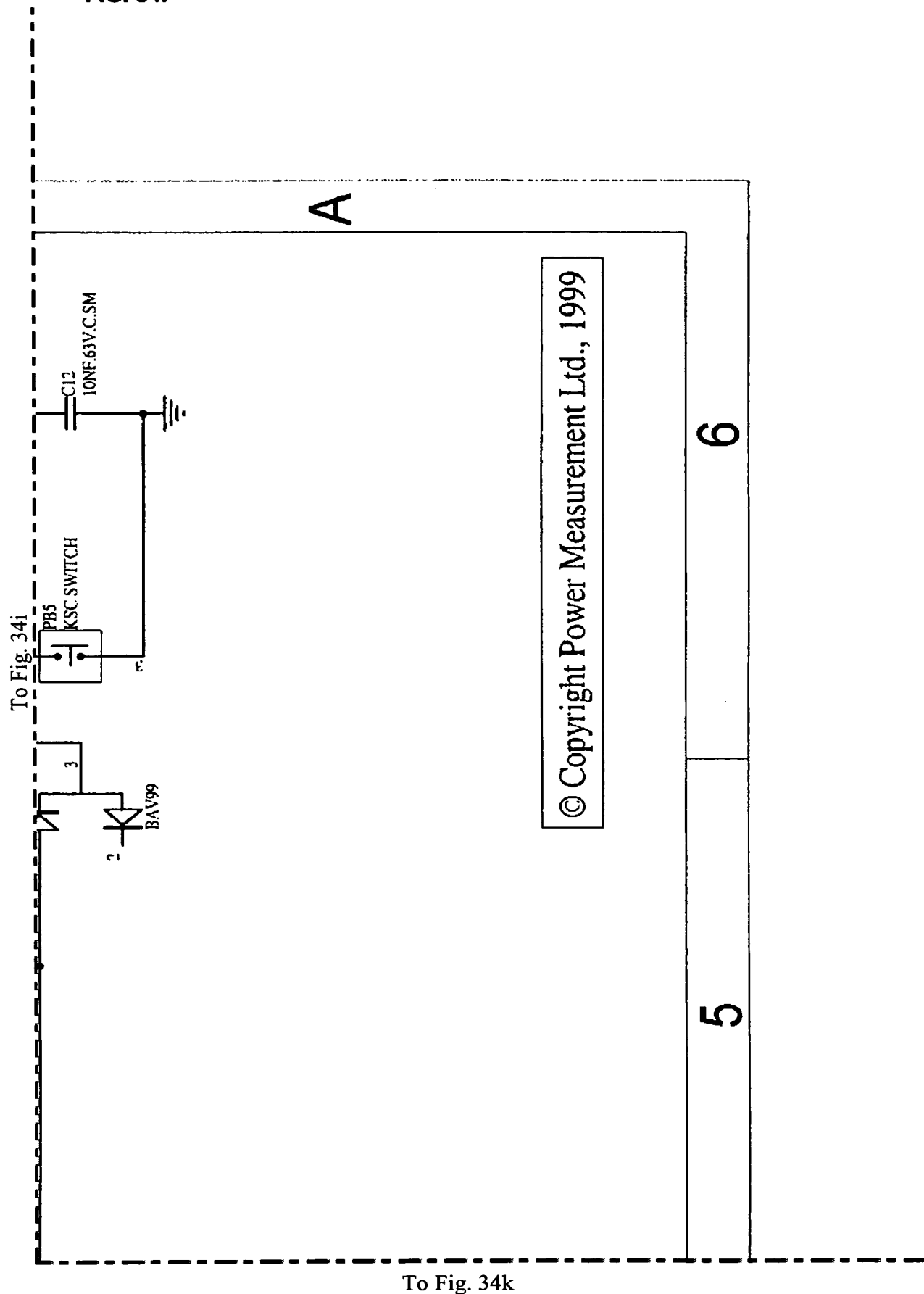


FIG. 35a

To Fig. 35b

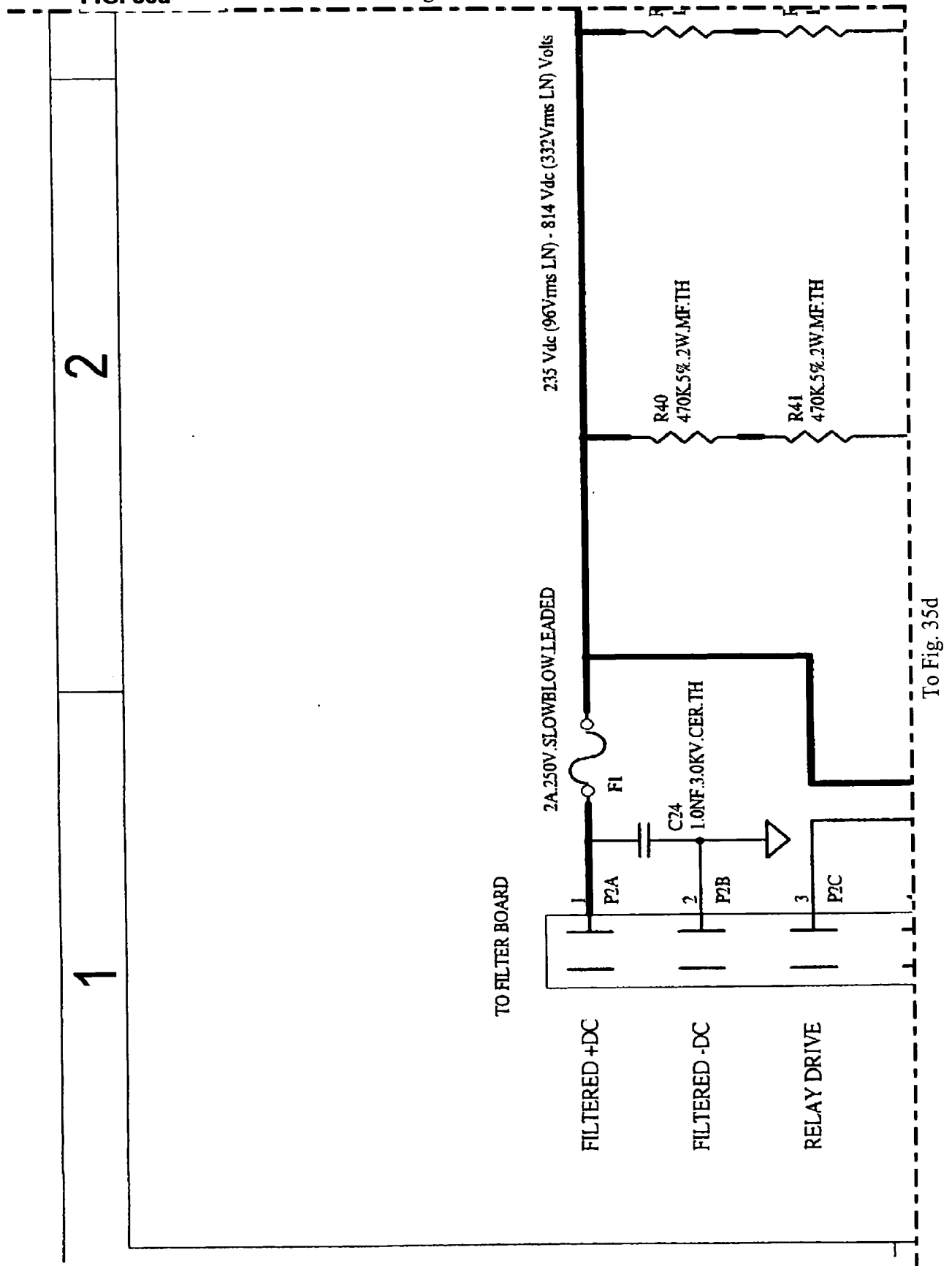


FIG. 35b

To Fig. 35c

4

3

MAINTAIN CLEARANCES ** HIGH VOLTAGES **

nlb

R4
100K.5%.2W.MF.TH

R3
100K.5%.2W.MF.TH

R34
8K2.5%.2W.MF.TH

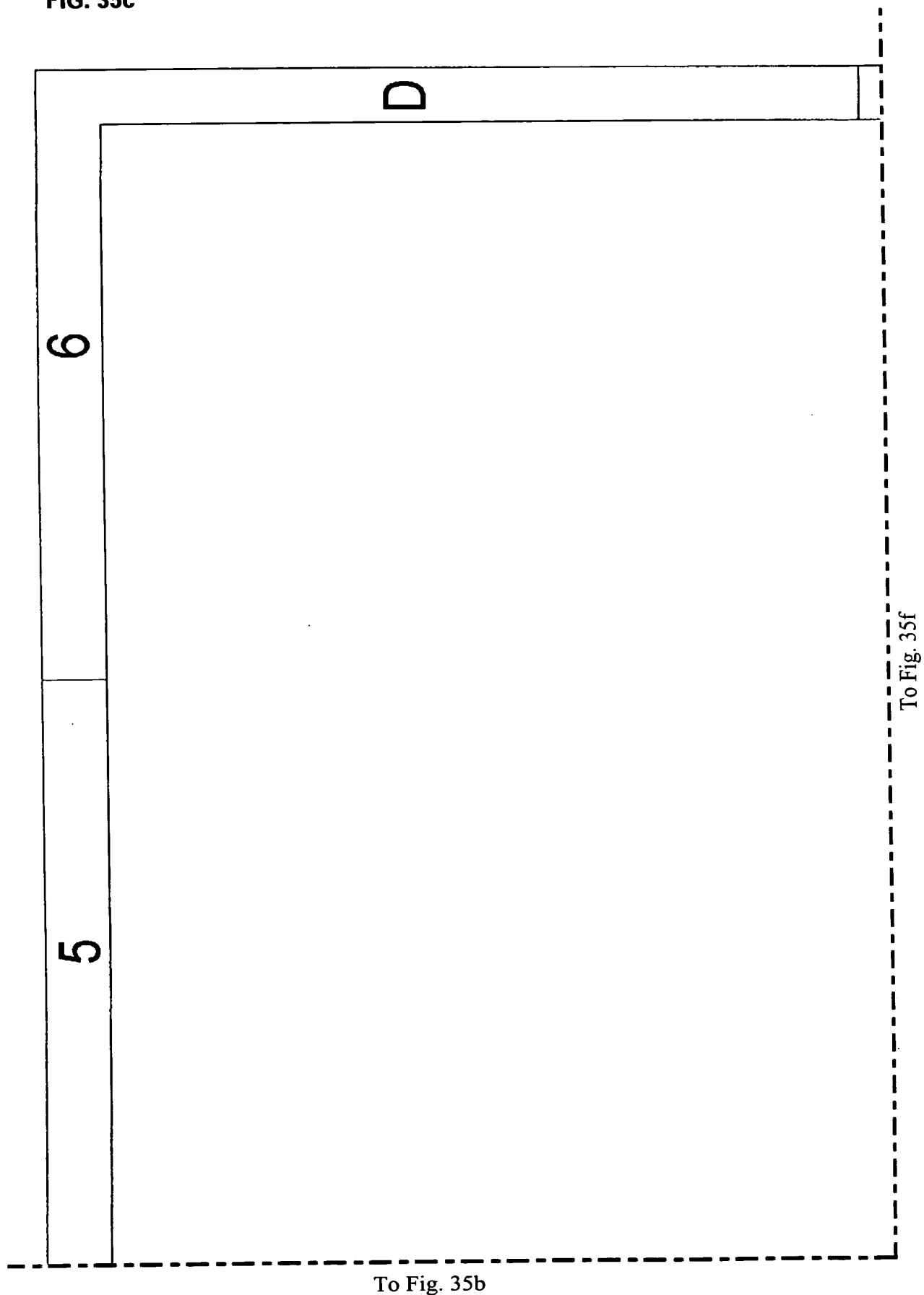
R5
100K.5%.2W.MF.TH

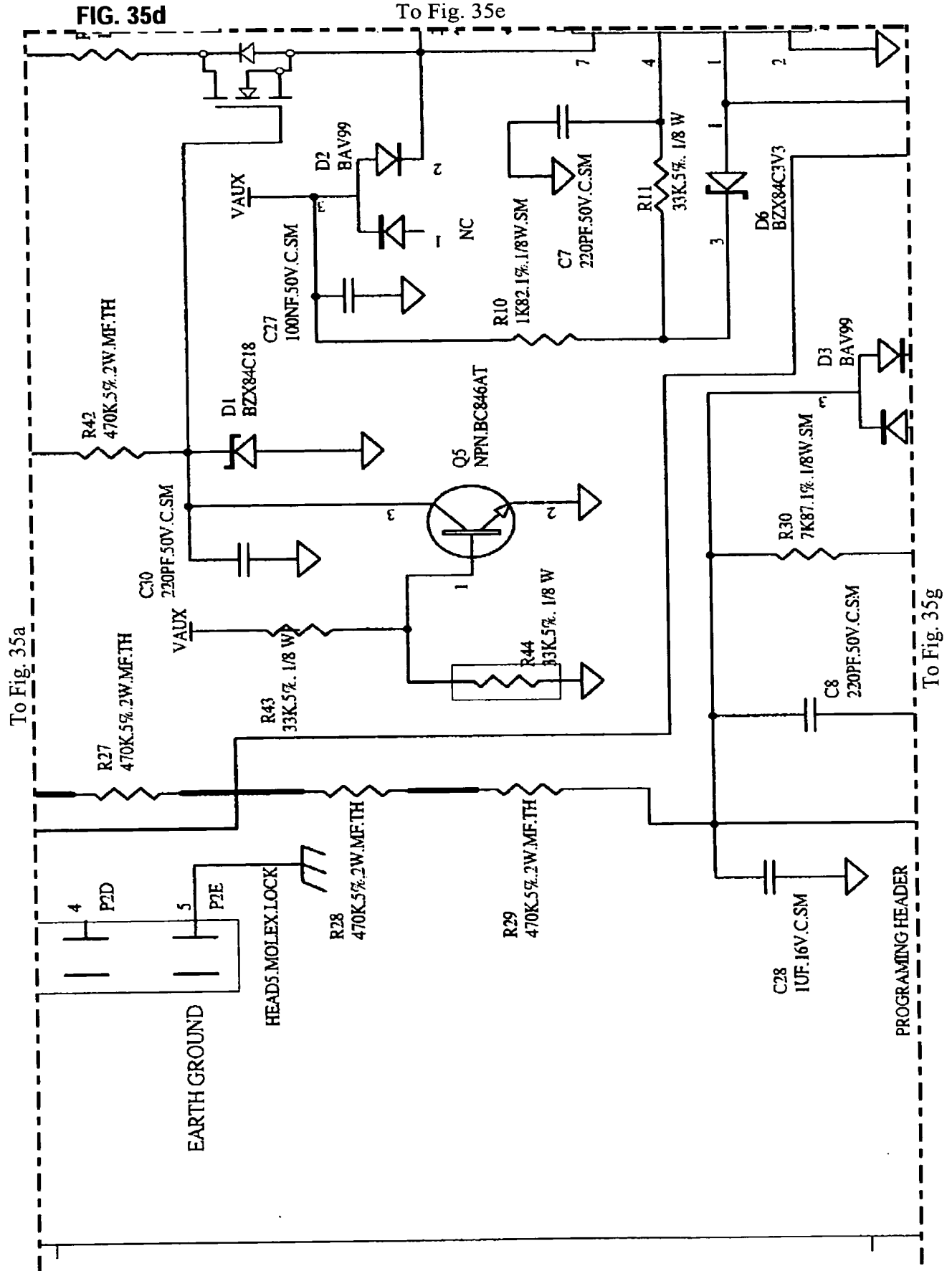
C1
1.0NF.3.0KV.CER.TH

To Fig. 35e

To Fig. 35a

FIG. 35c





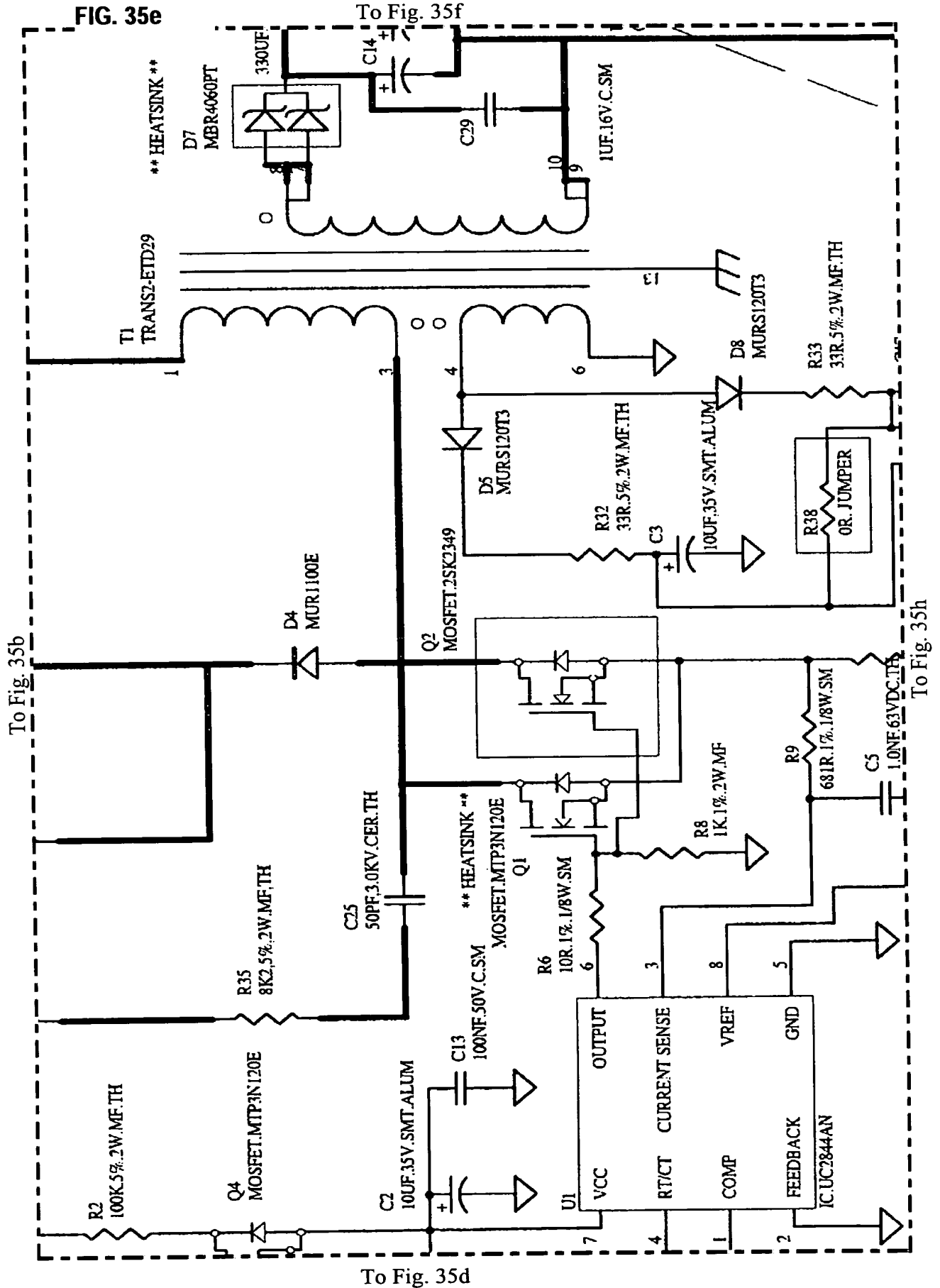
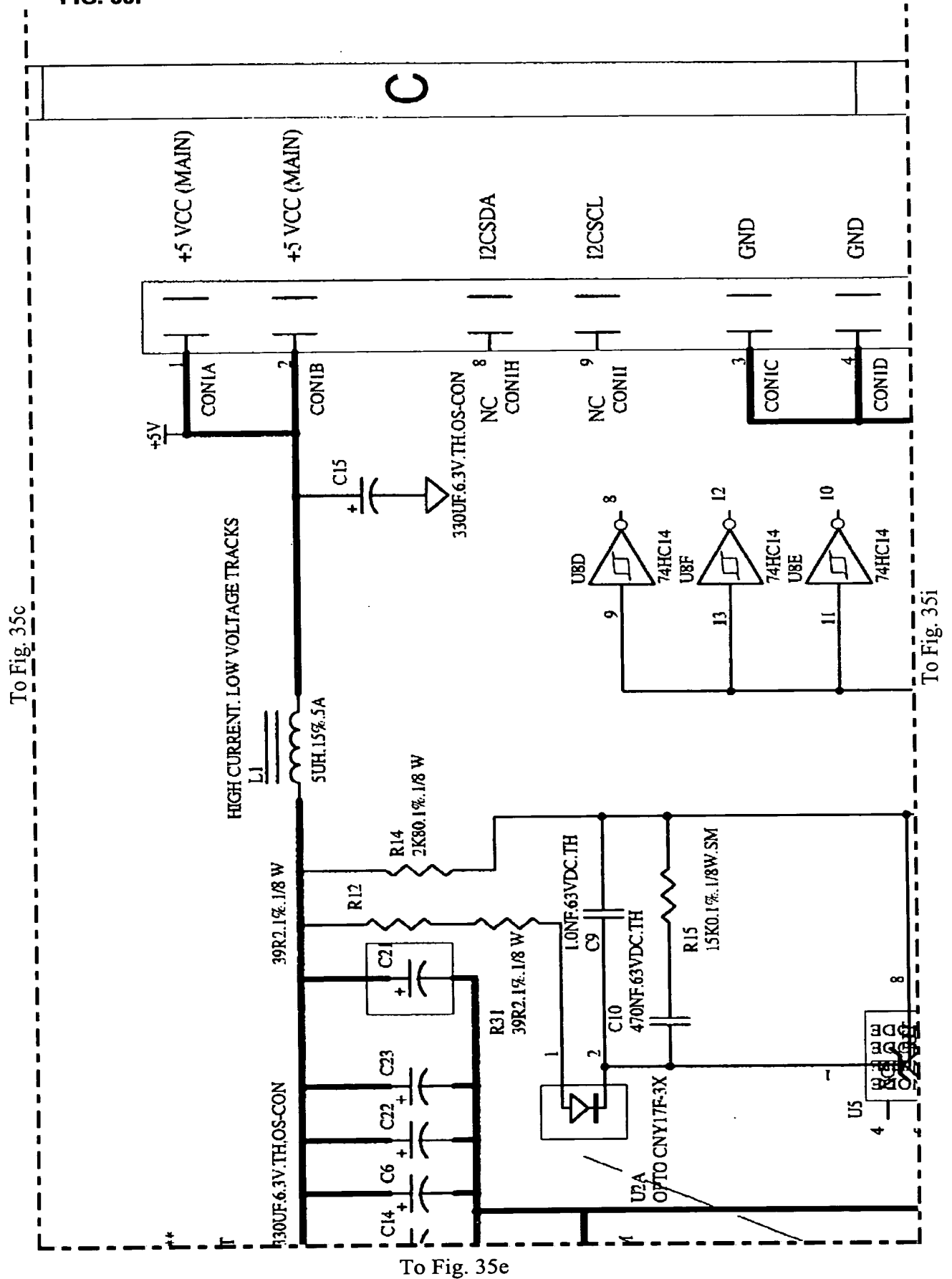
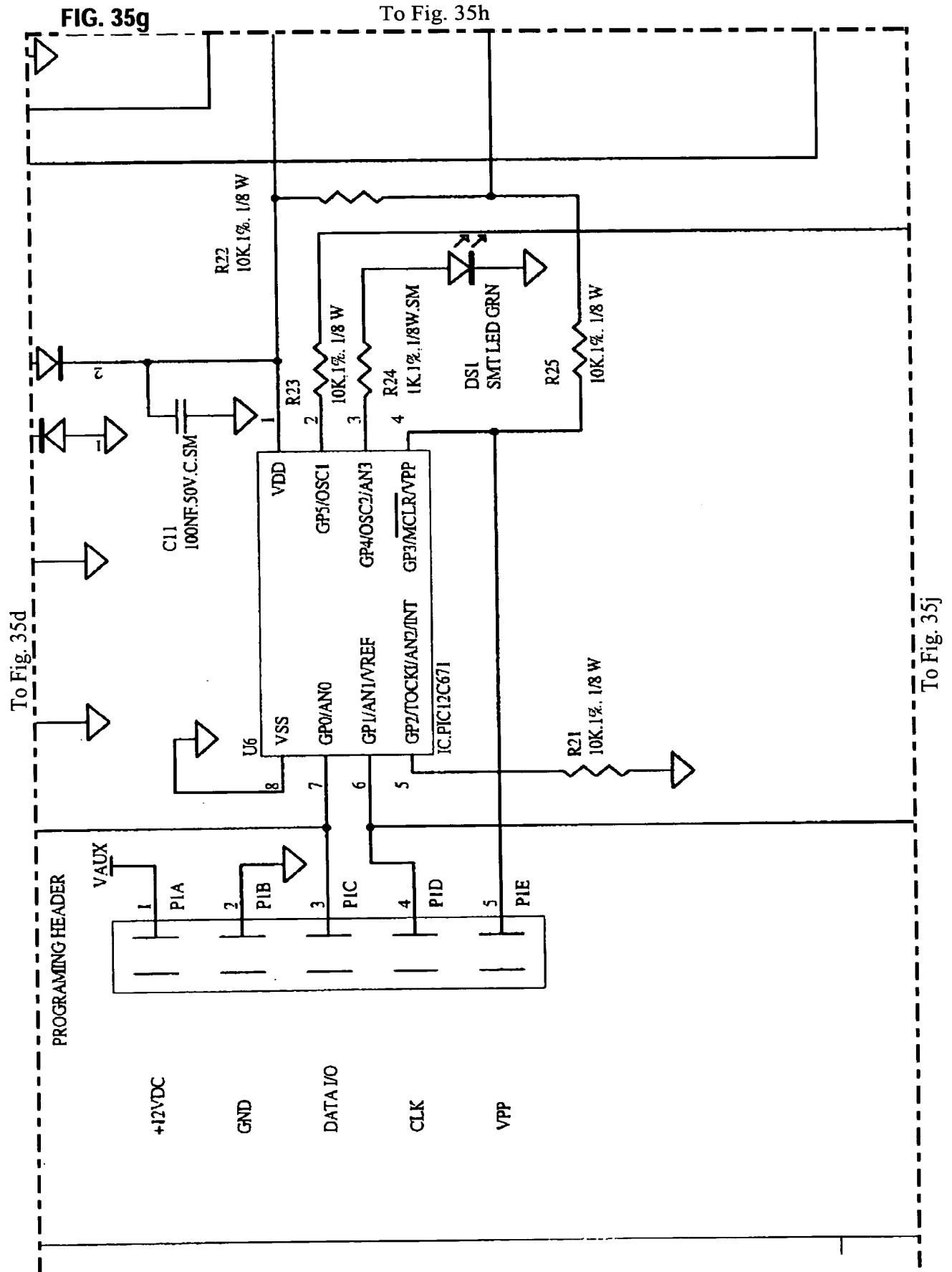


FIG. 35f





Inventor(s): David W. Giles et al
Attorney Docket No.: OUR CASE NO. 6270/125

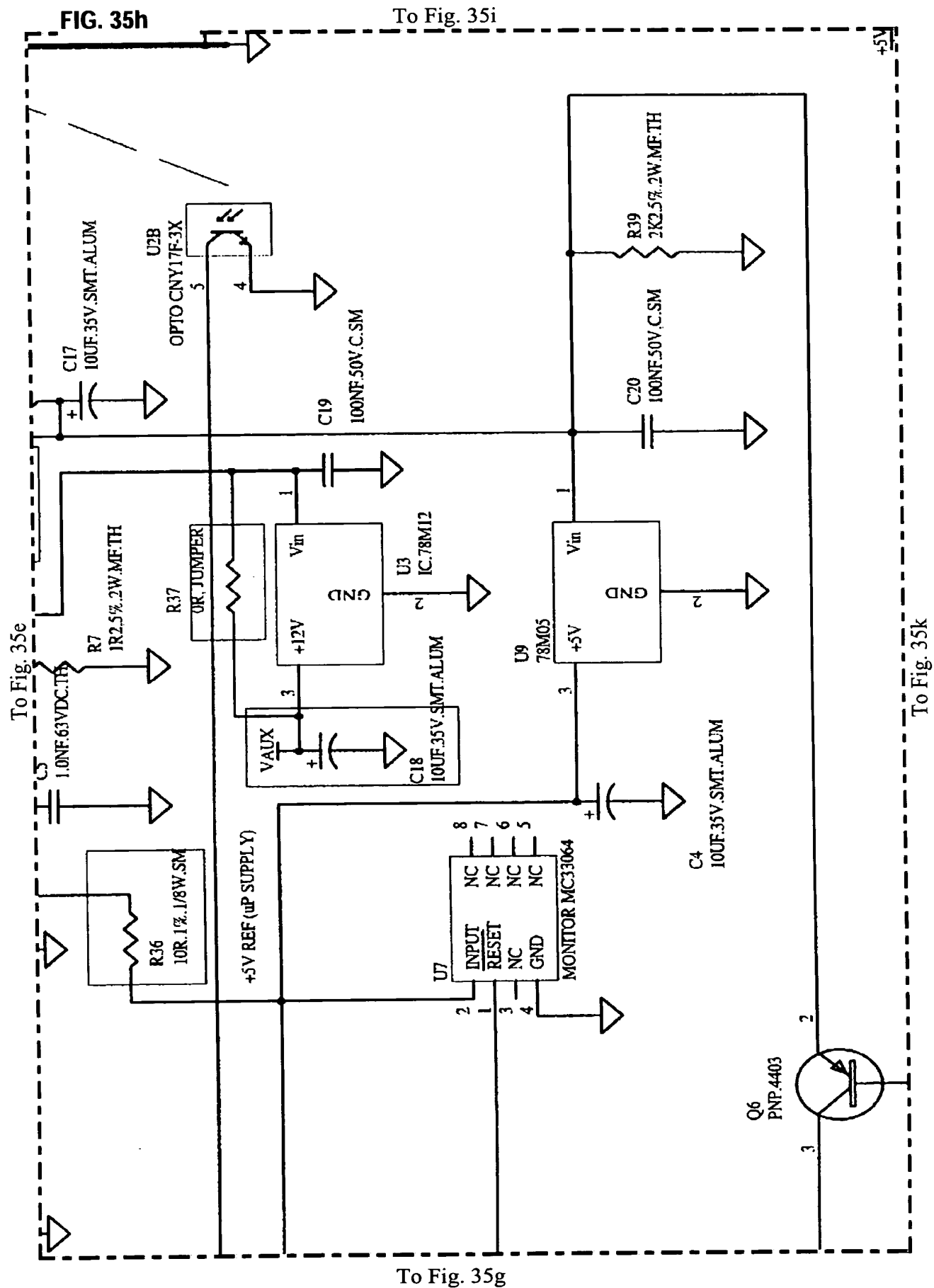
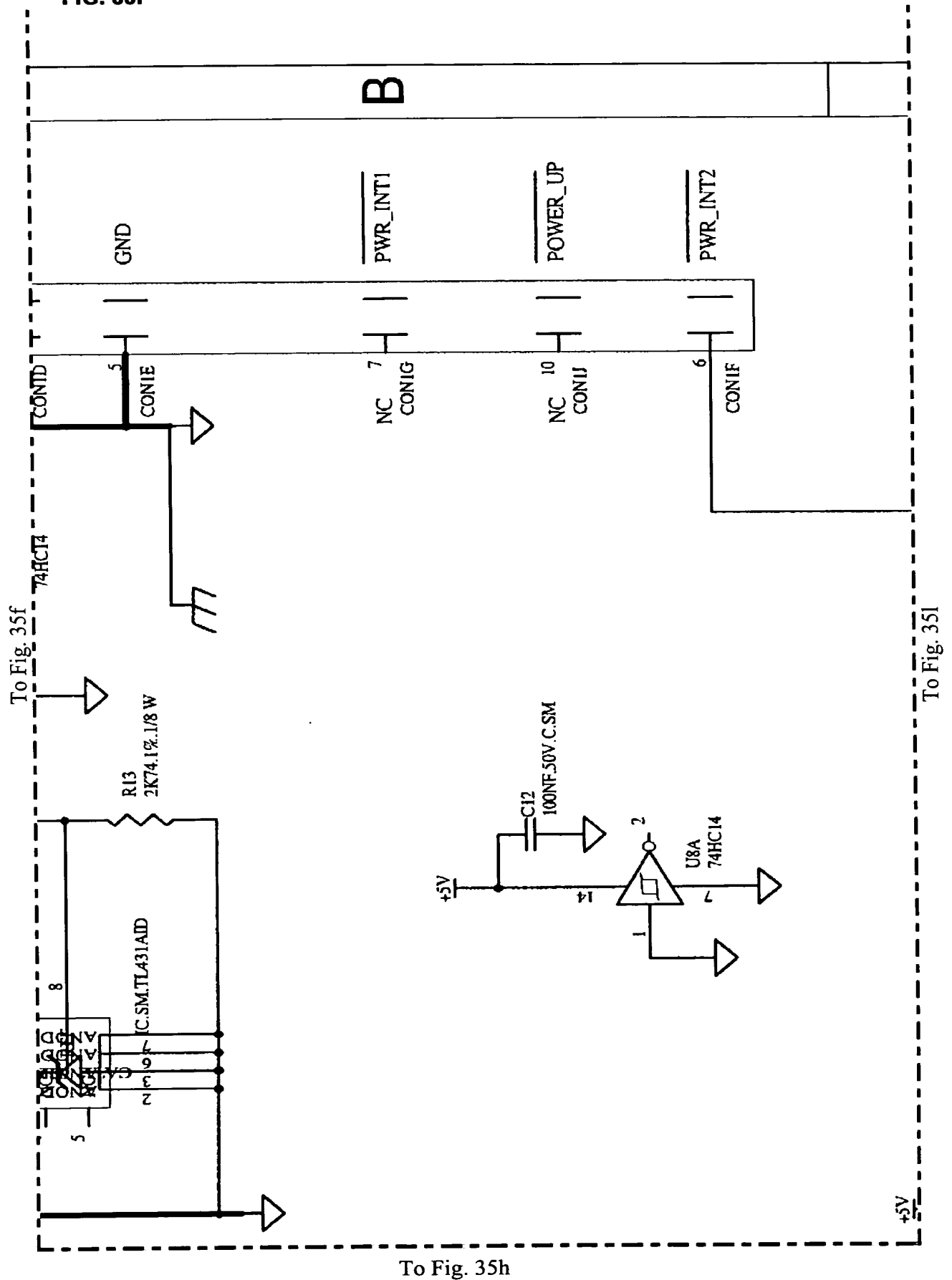
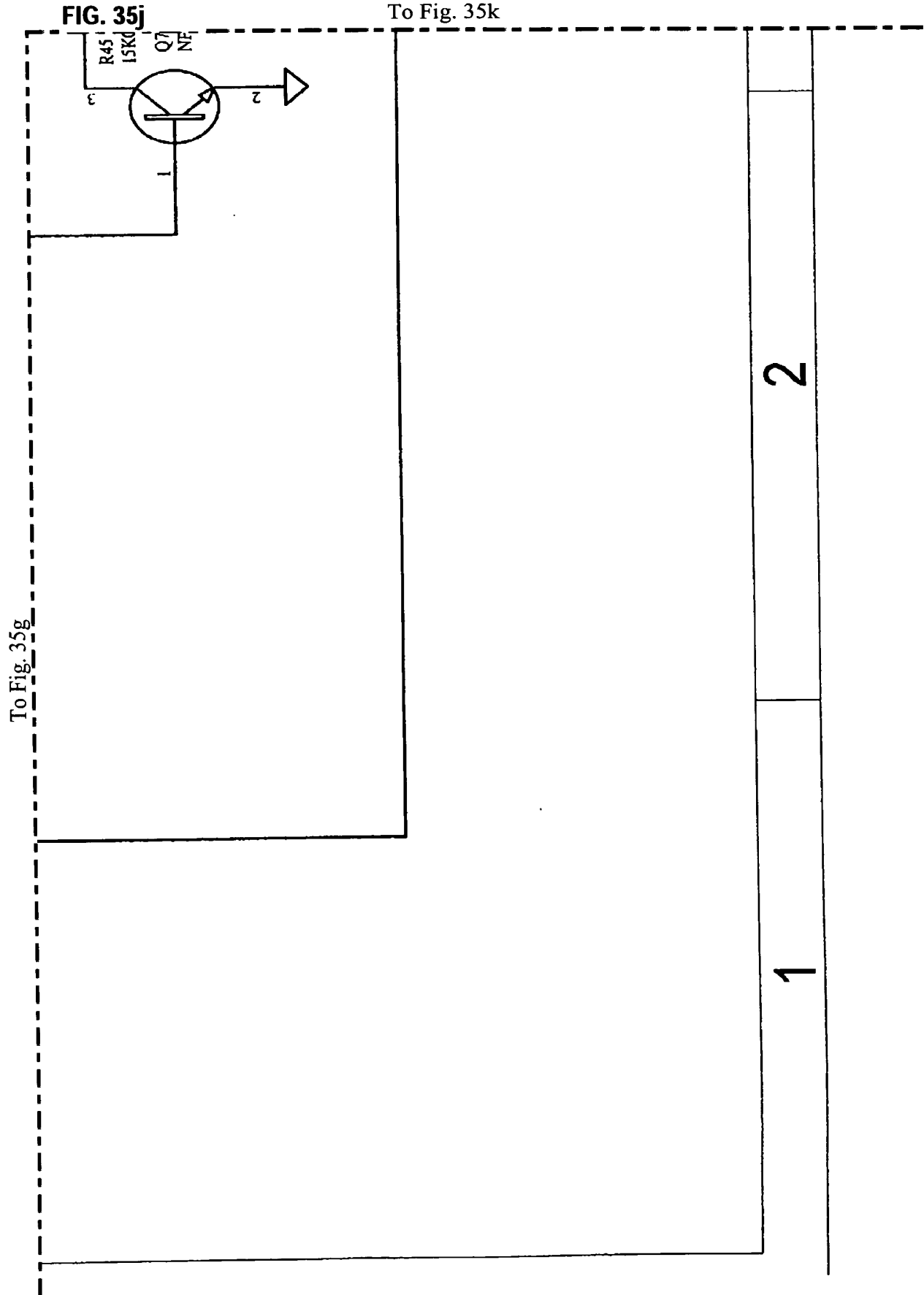


FIG. 35i





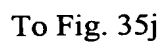
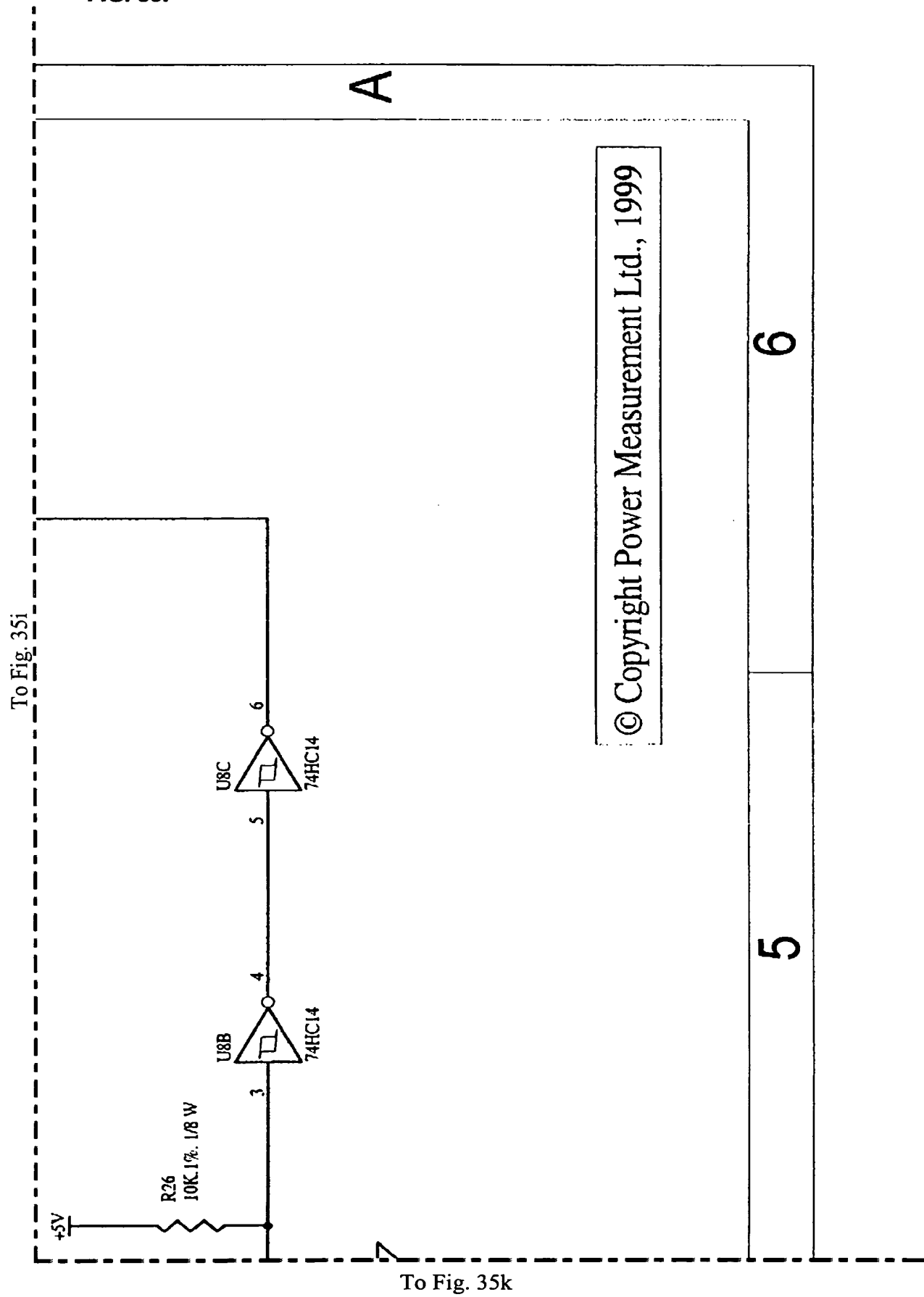
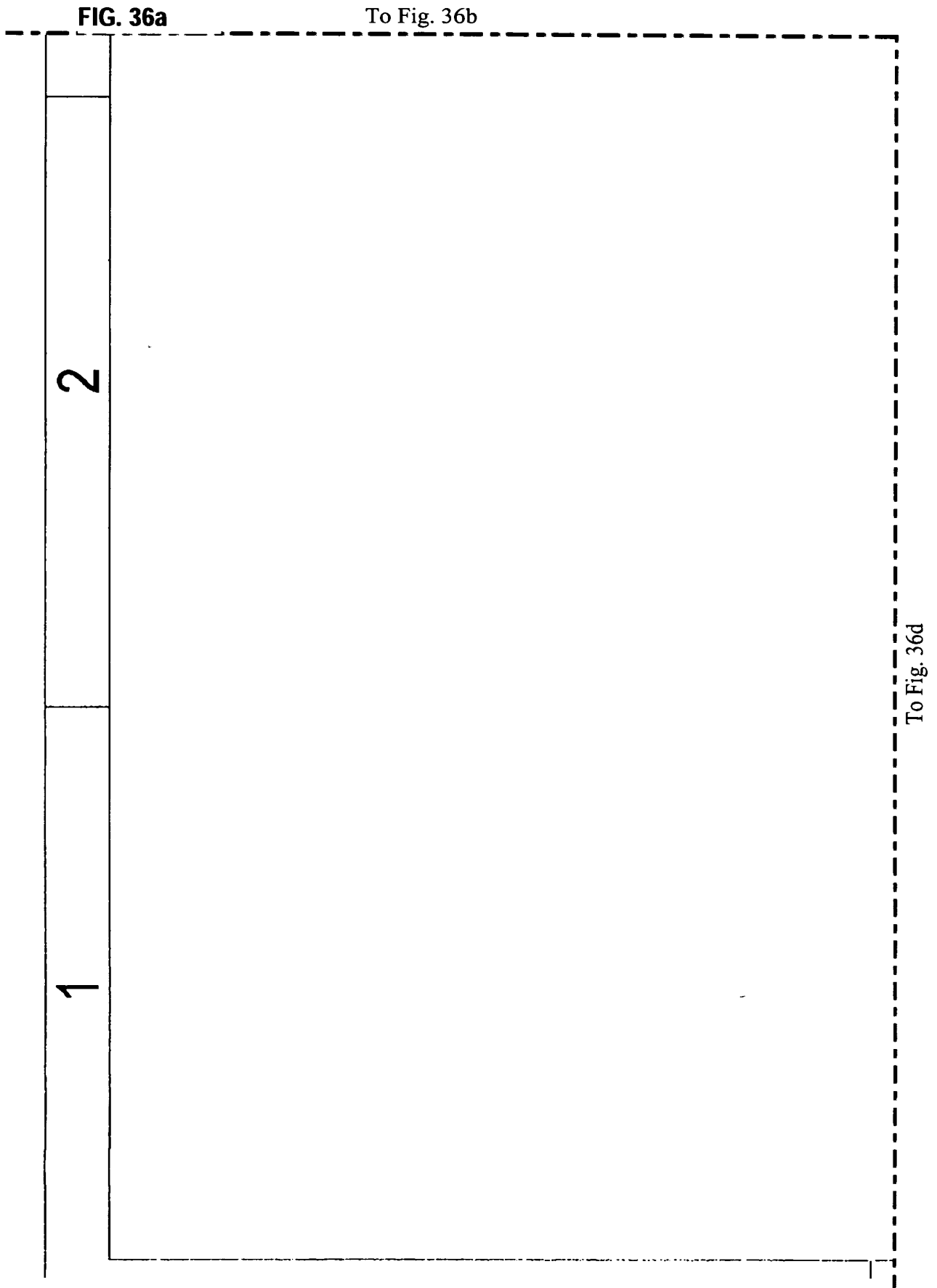


FIG. 35I





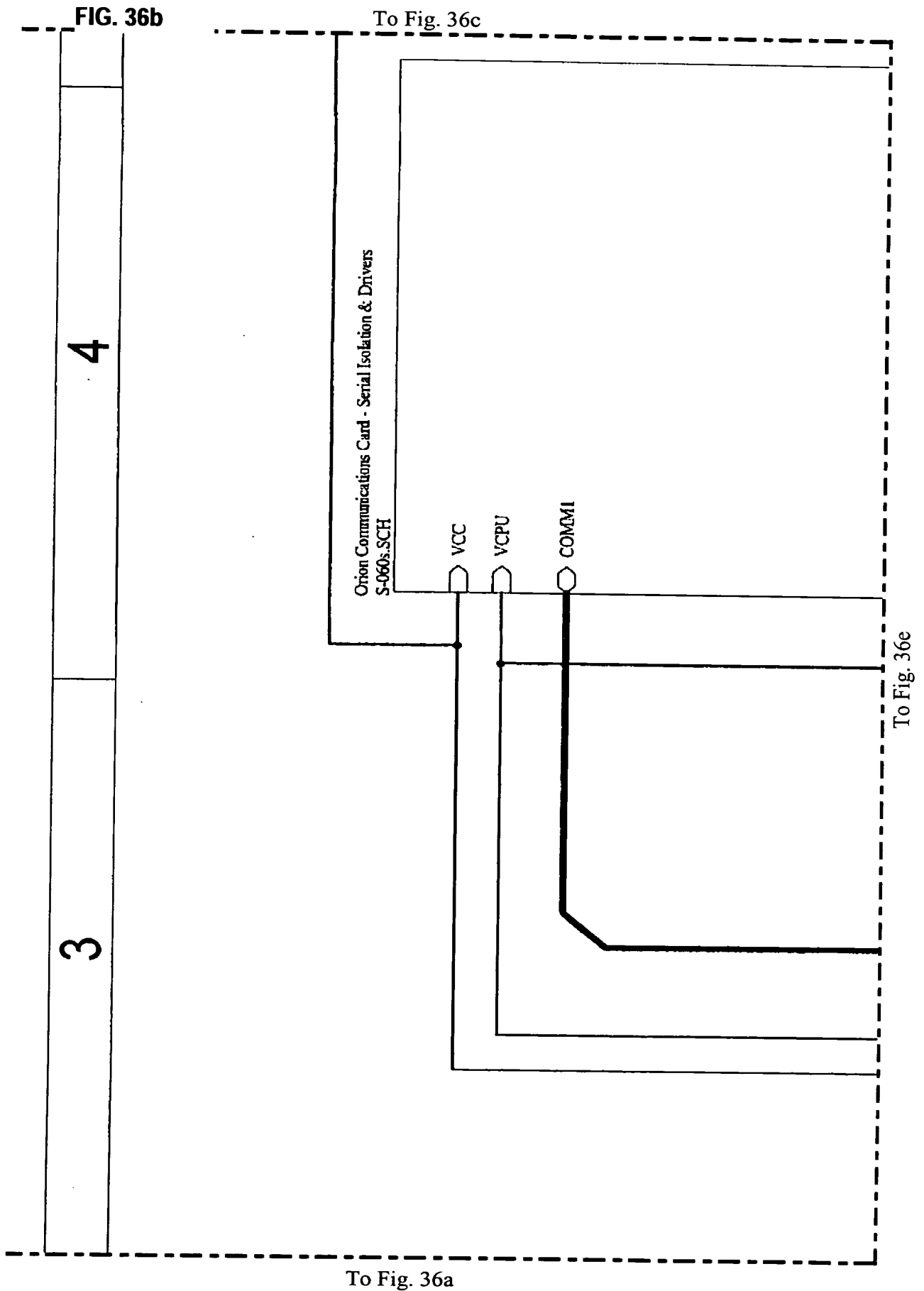
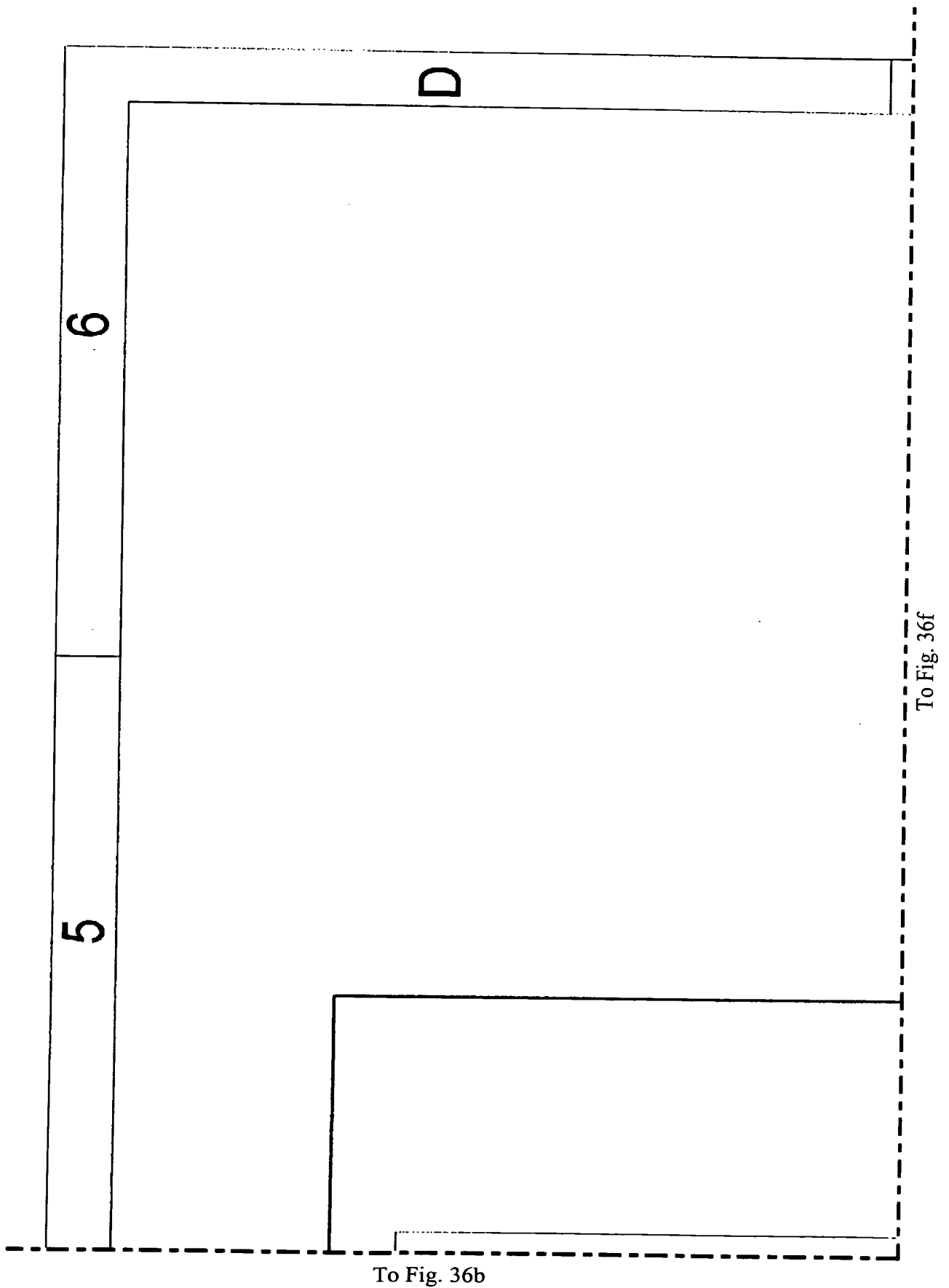
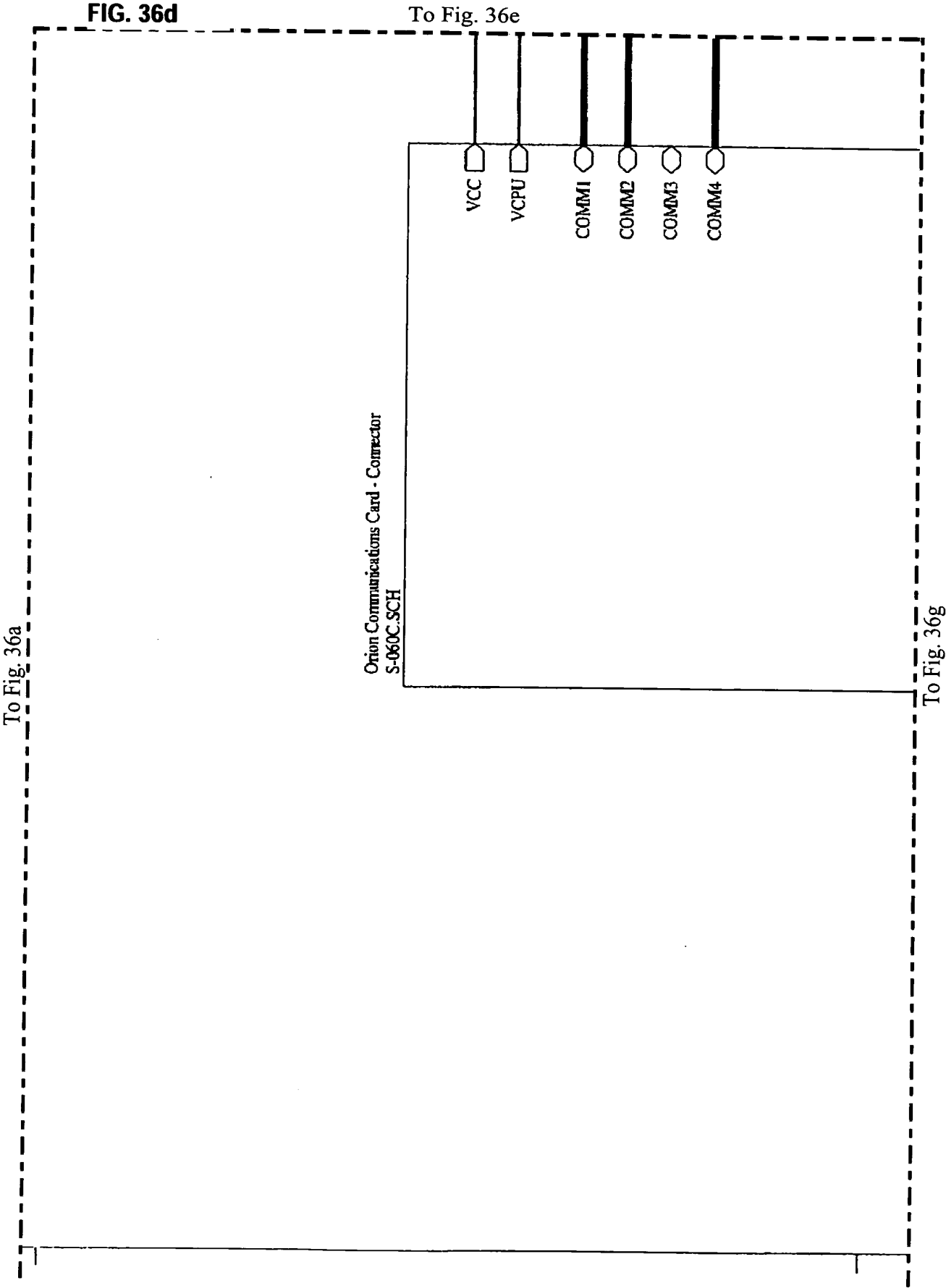


FIG. 36c





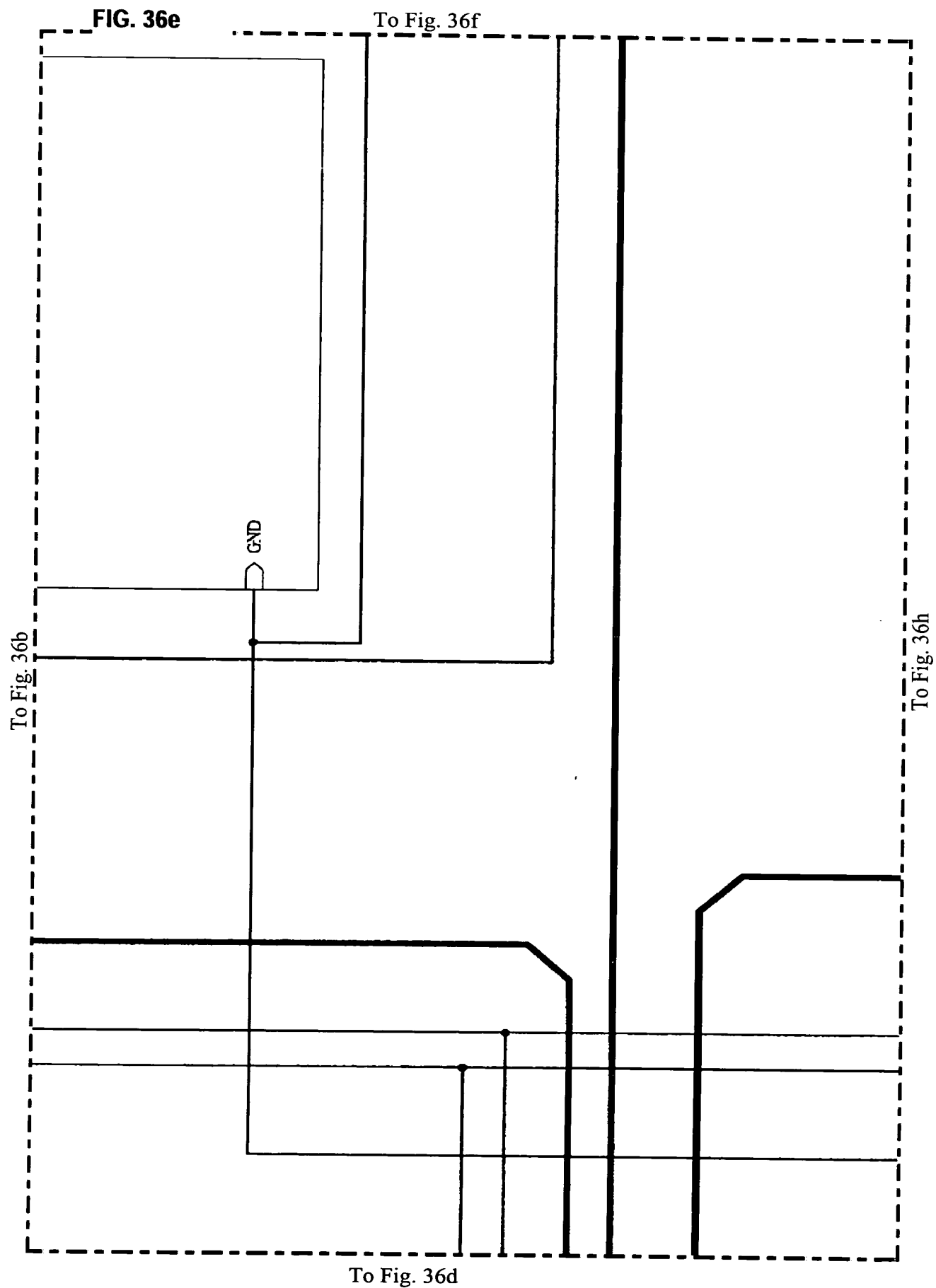


FIG. 36f

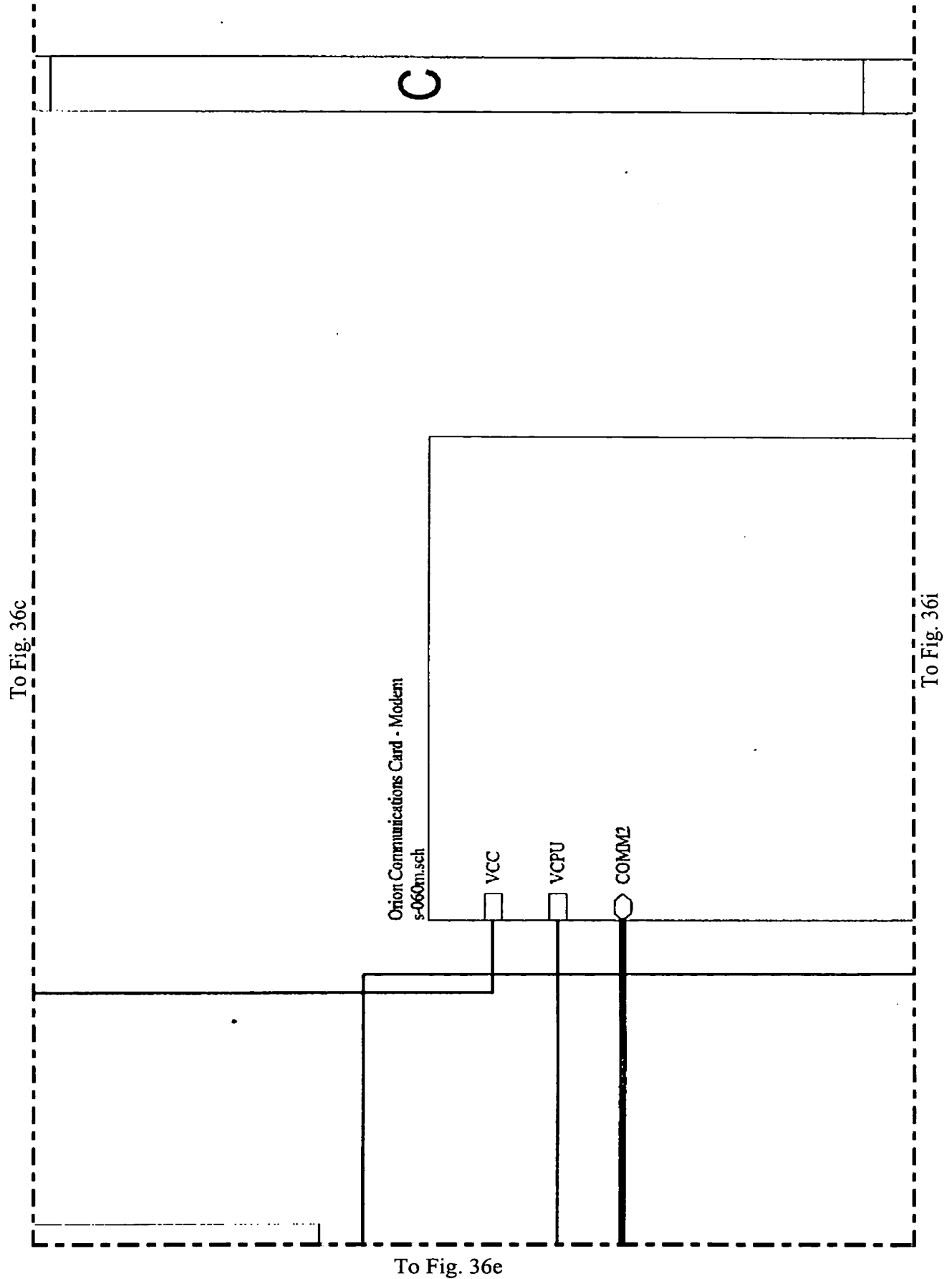
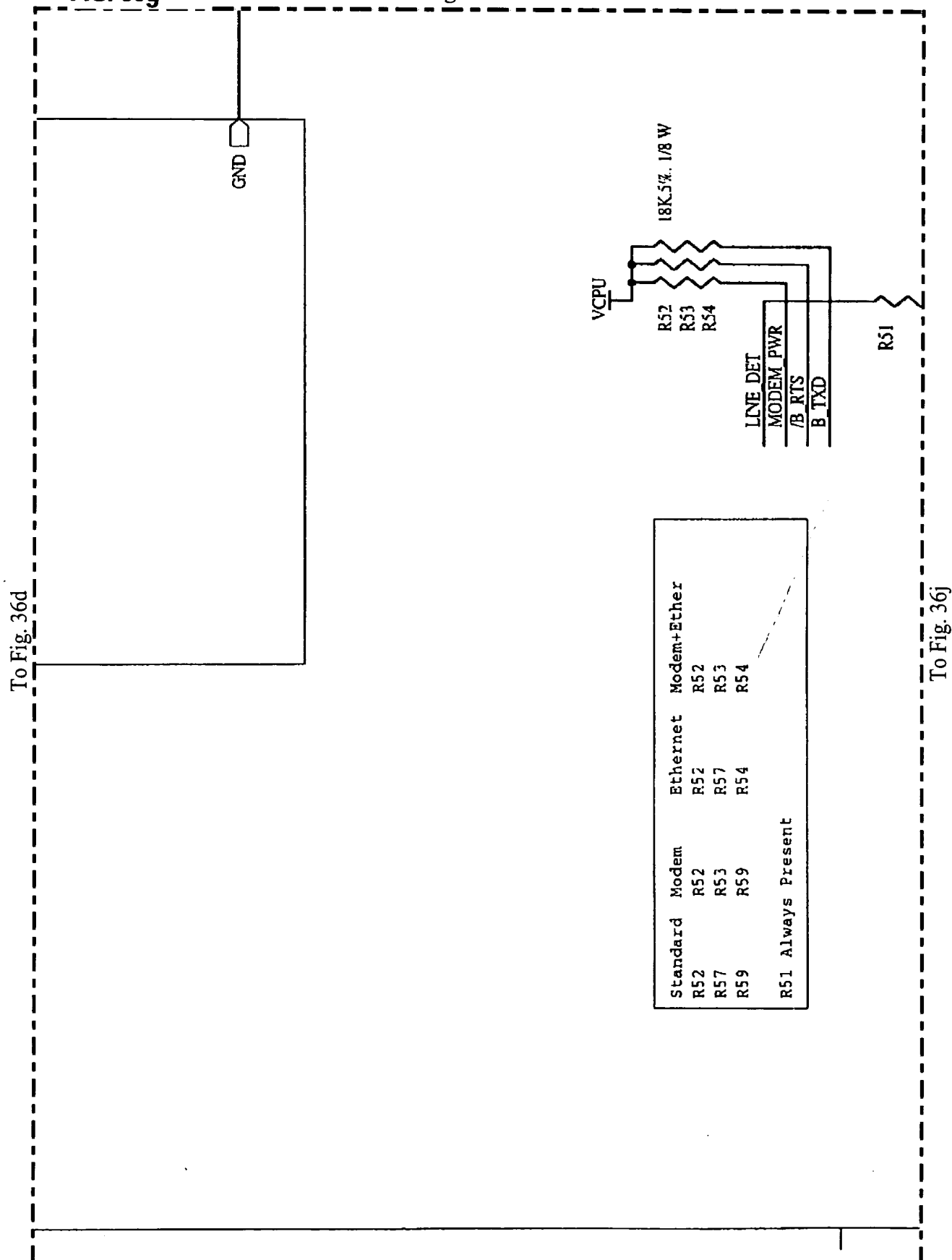


FIG. 36g

To Fig. 36h



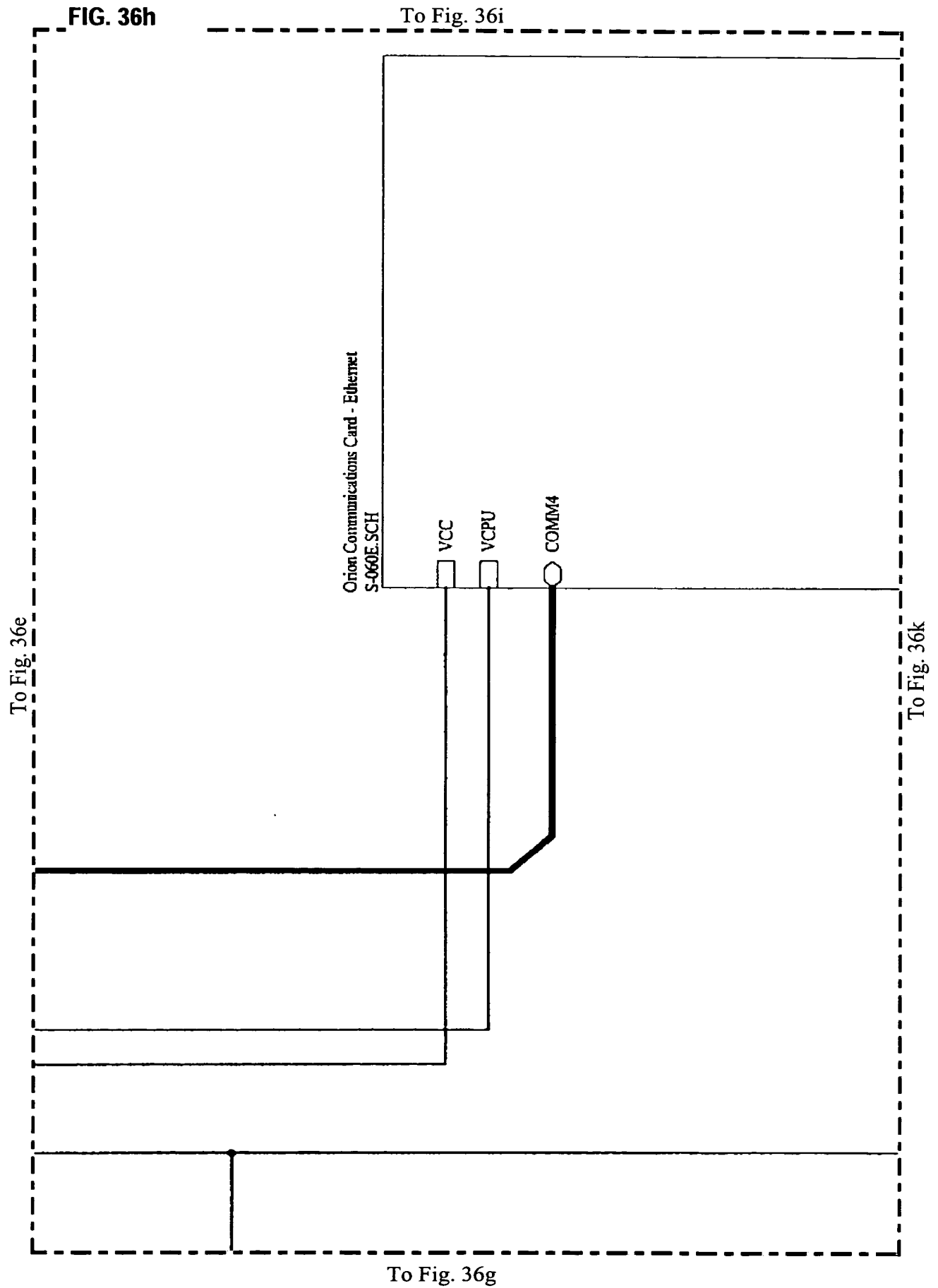


FIG. 36i

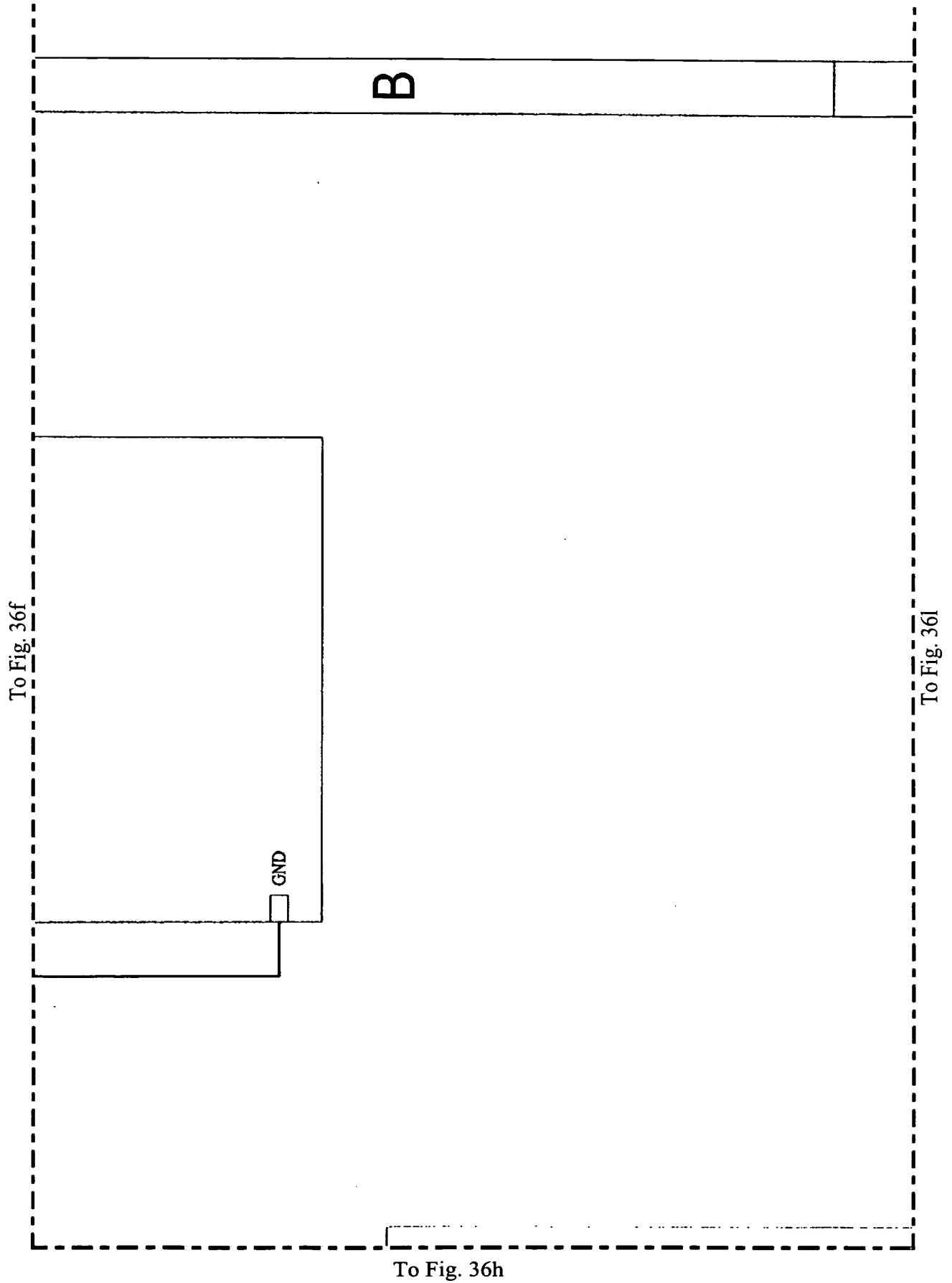
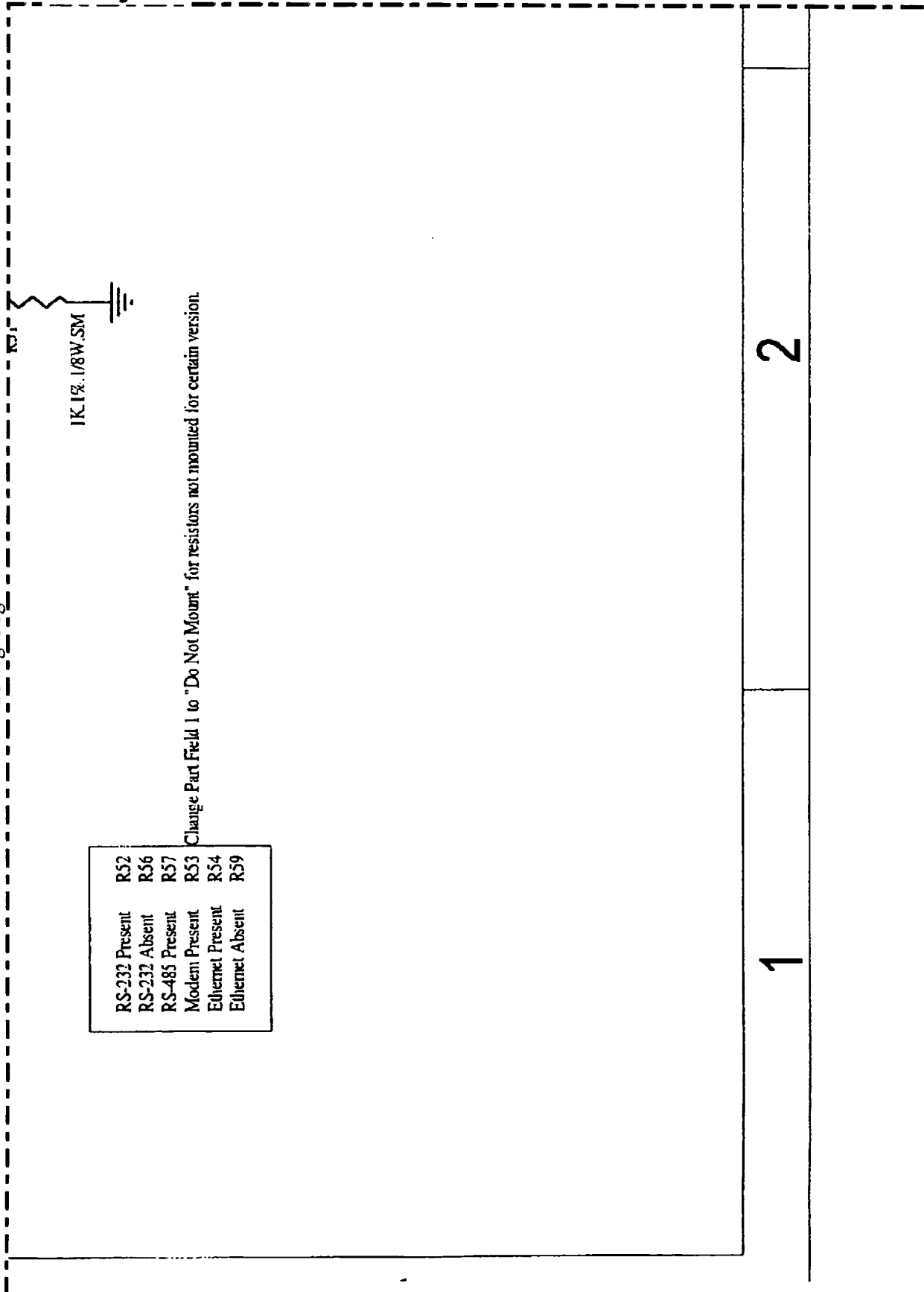


FIG. 36j

To Fig. 36k

To Fig. 36g



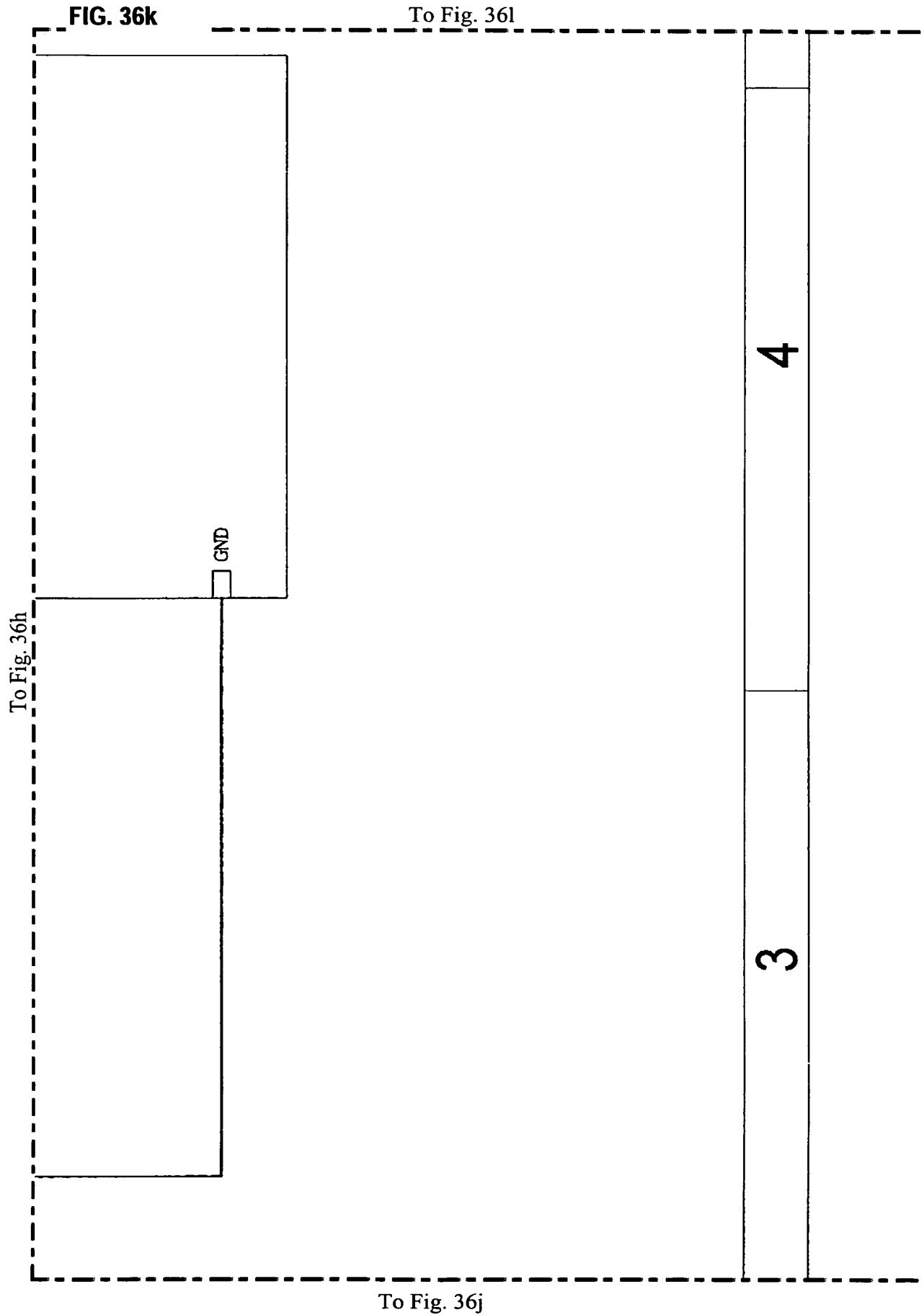


FIG. 36I

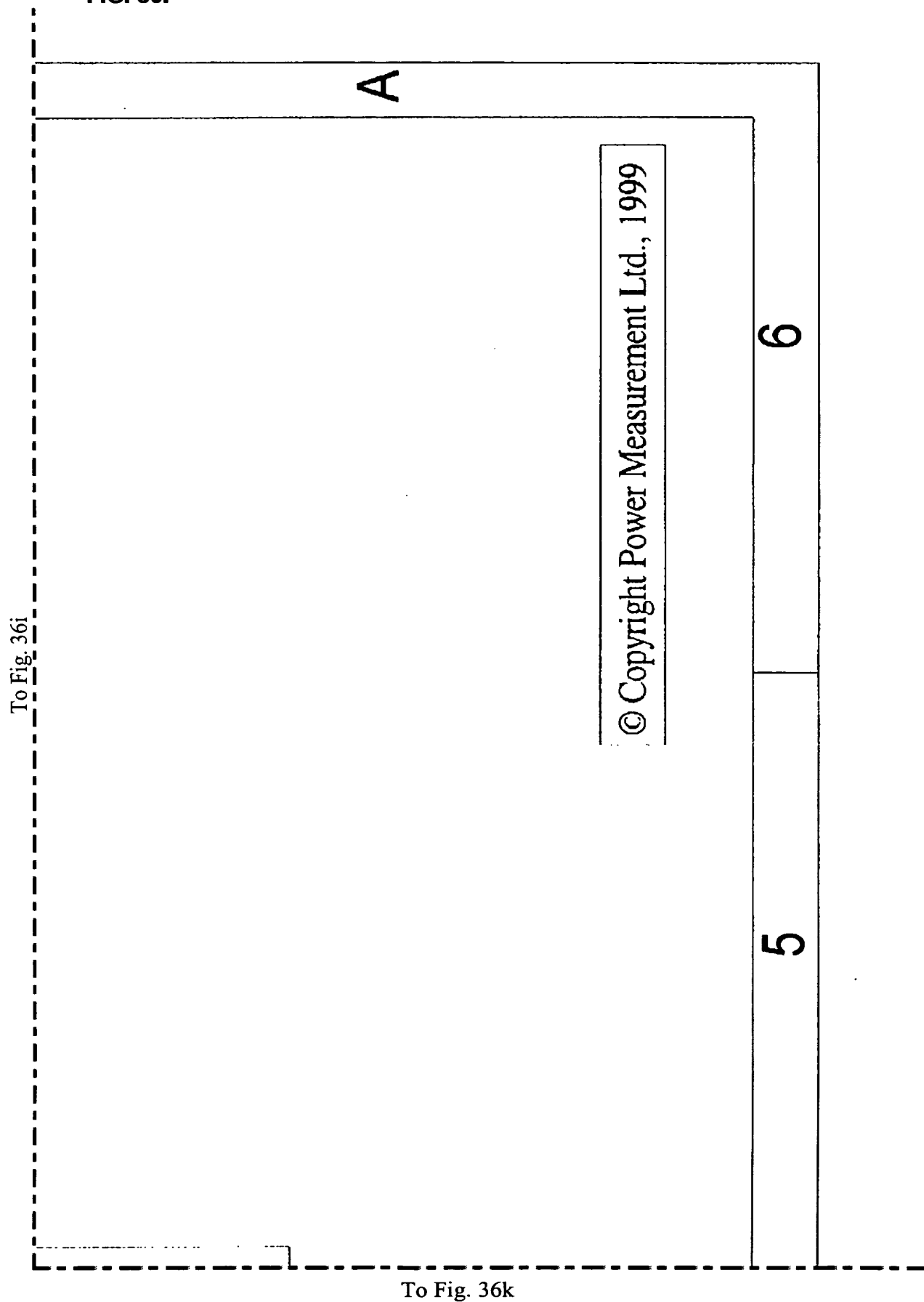


FIG. 37a

To Fig. 37b

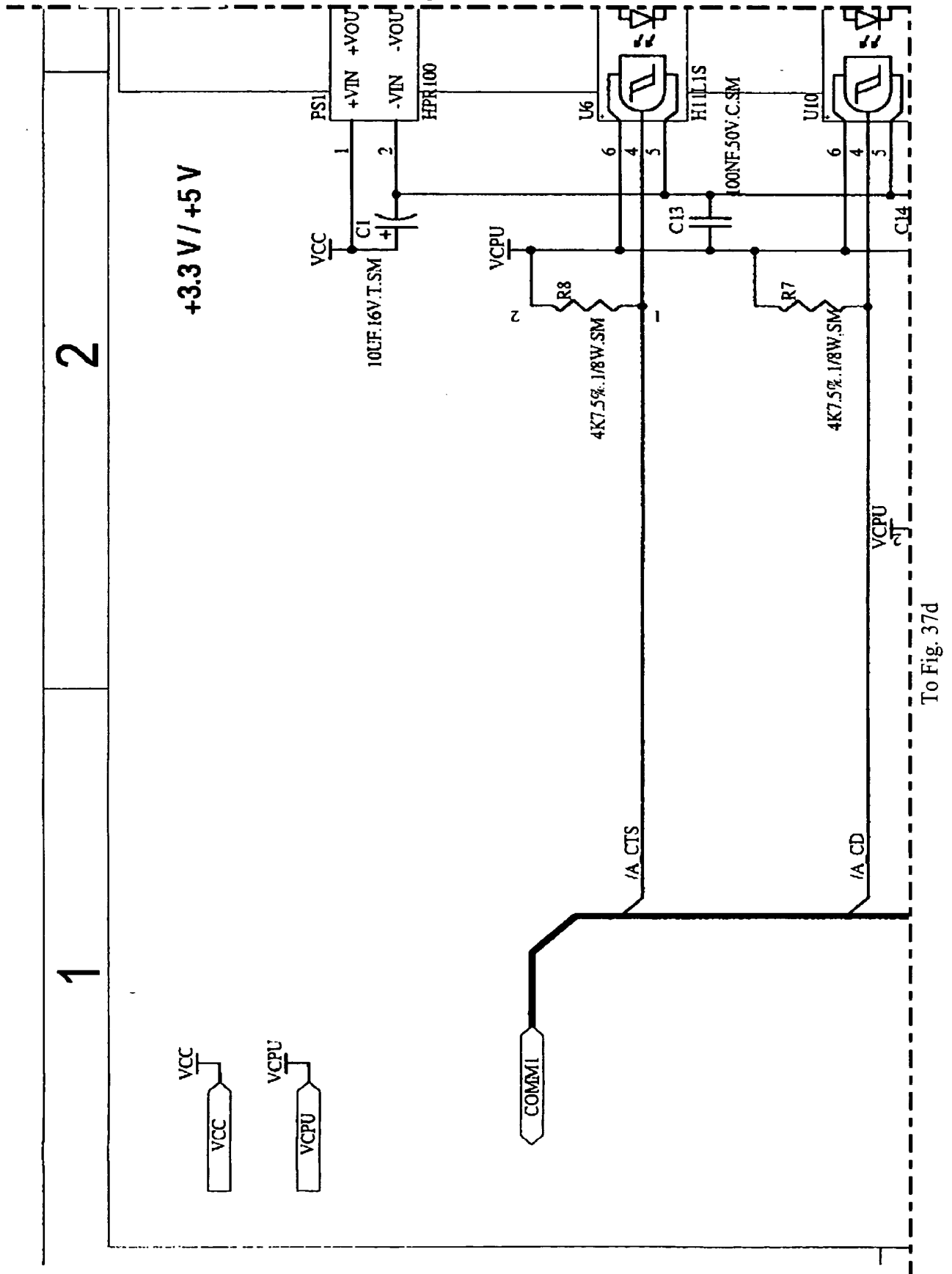


FIG. 37b

To Fig. 37c

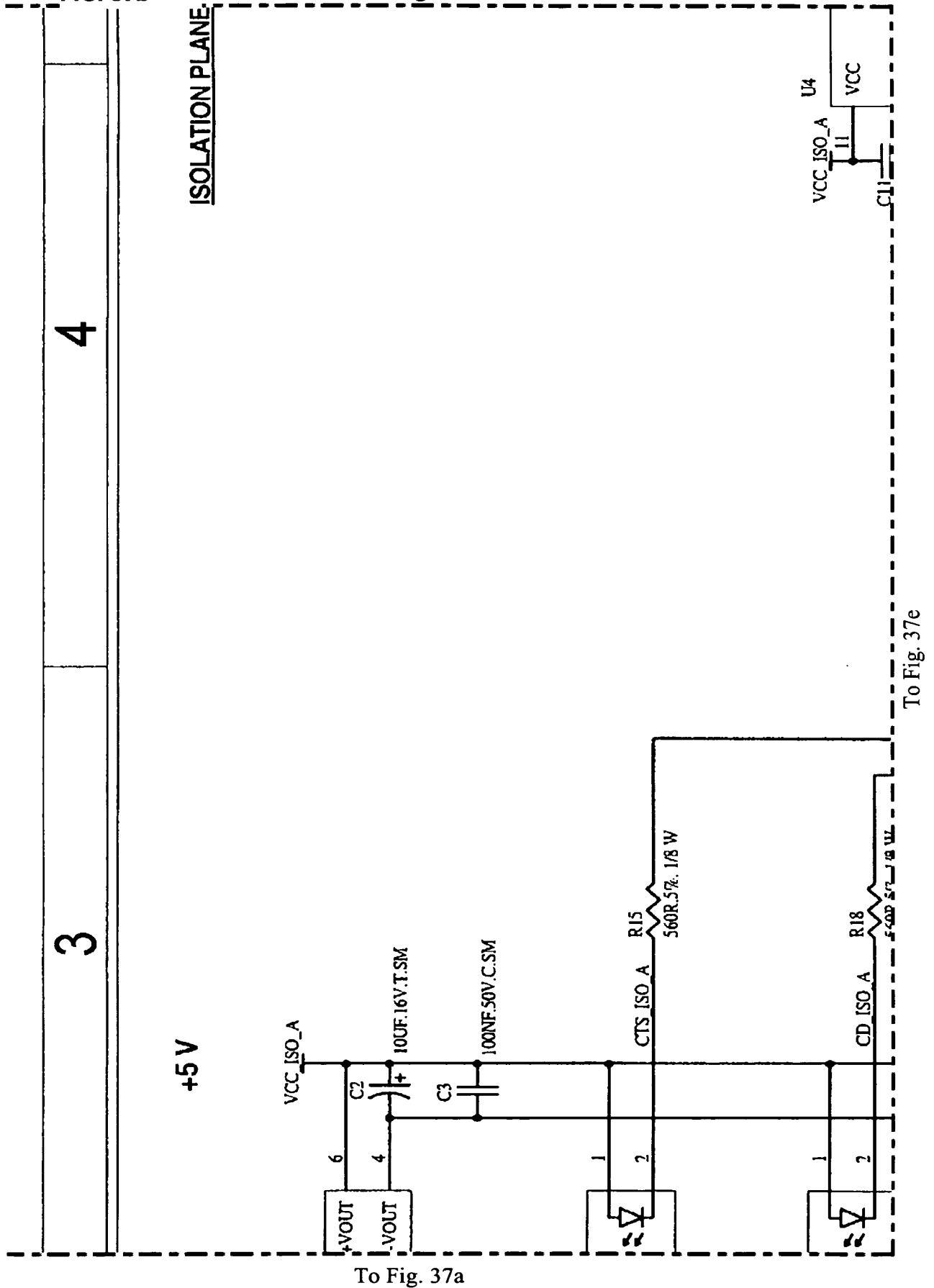
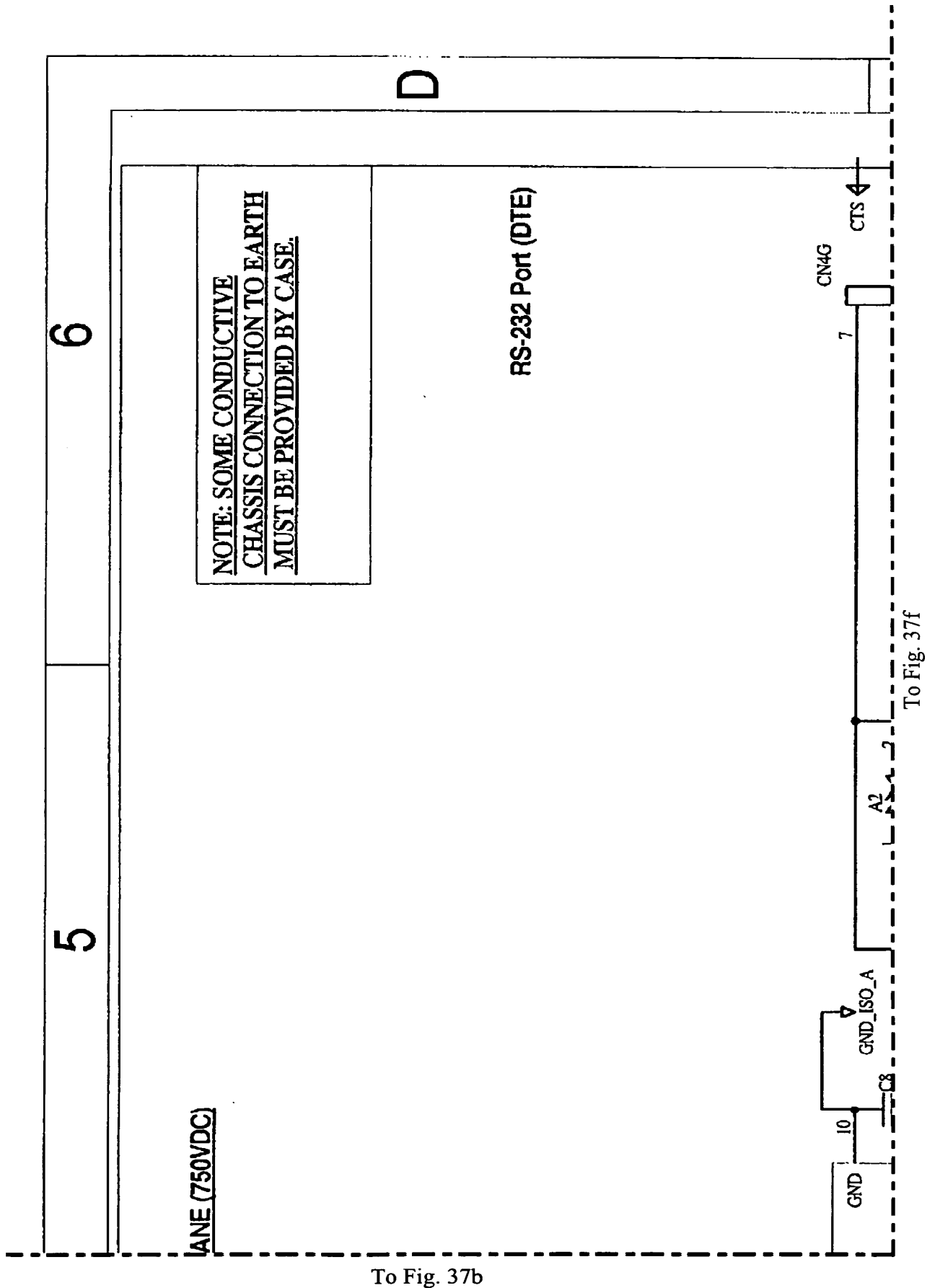
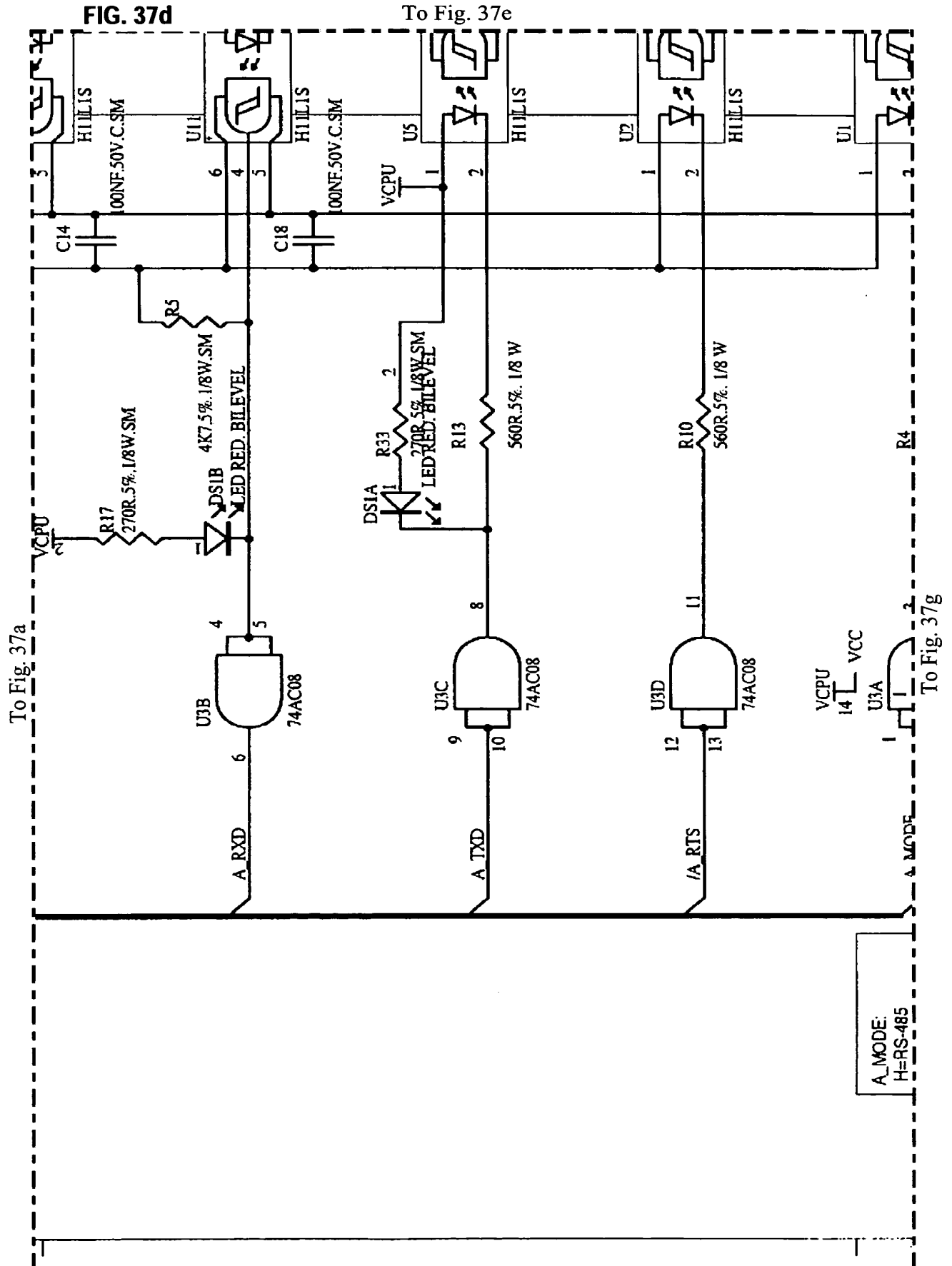


FIG. 37c





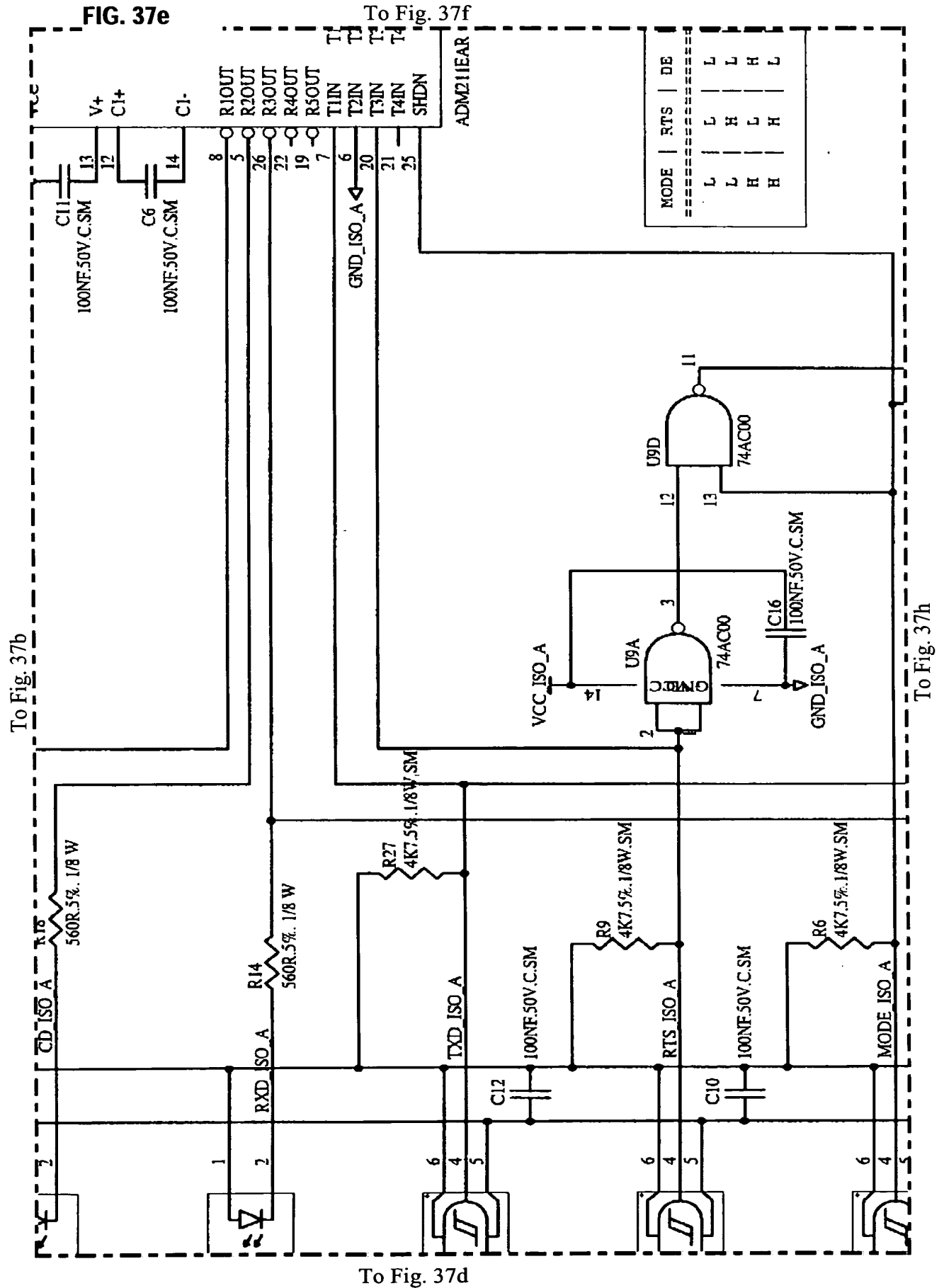
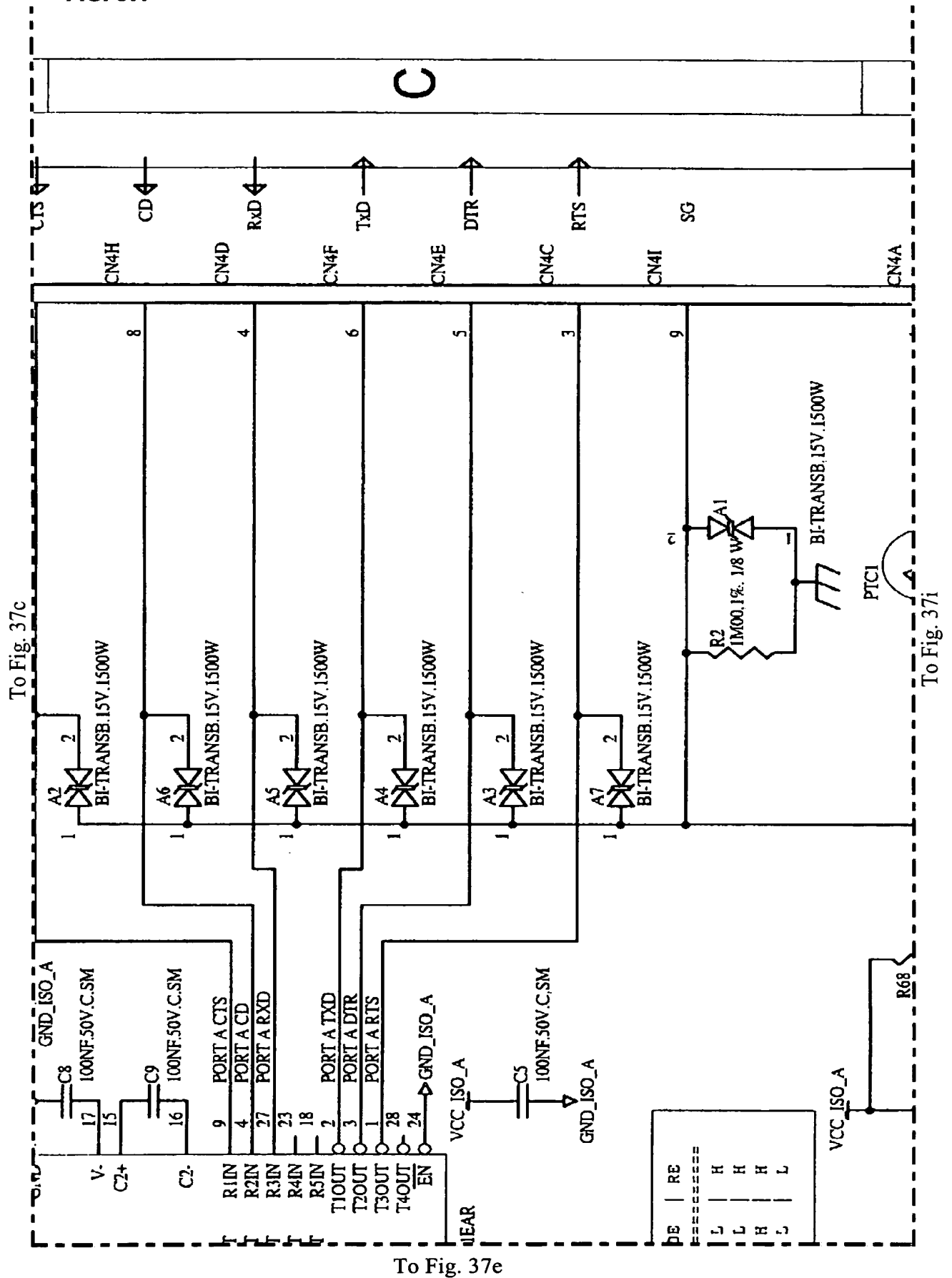
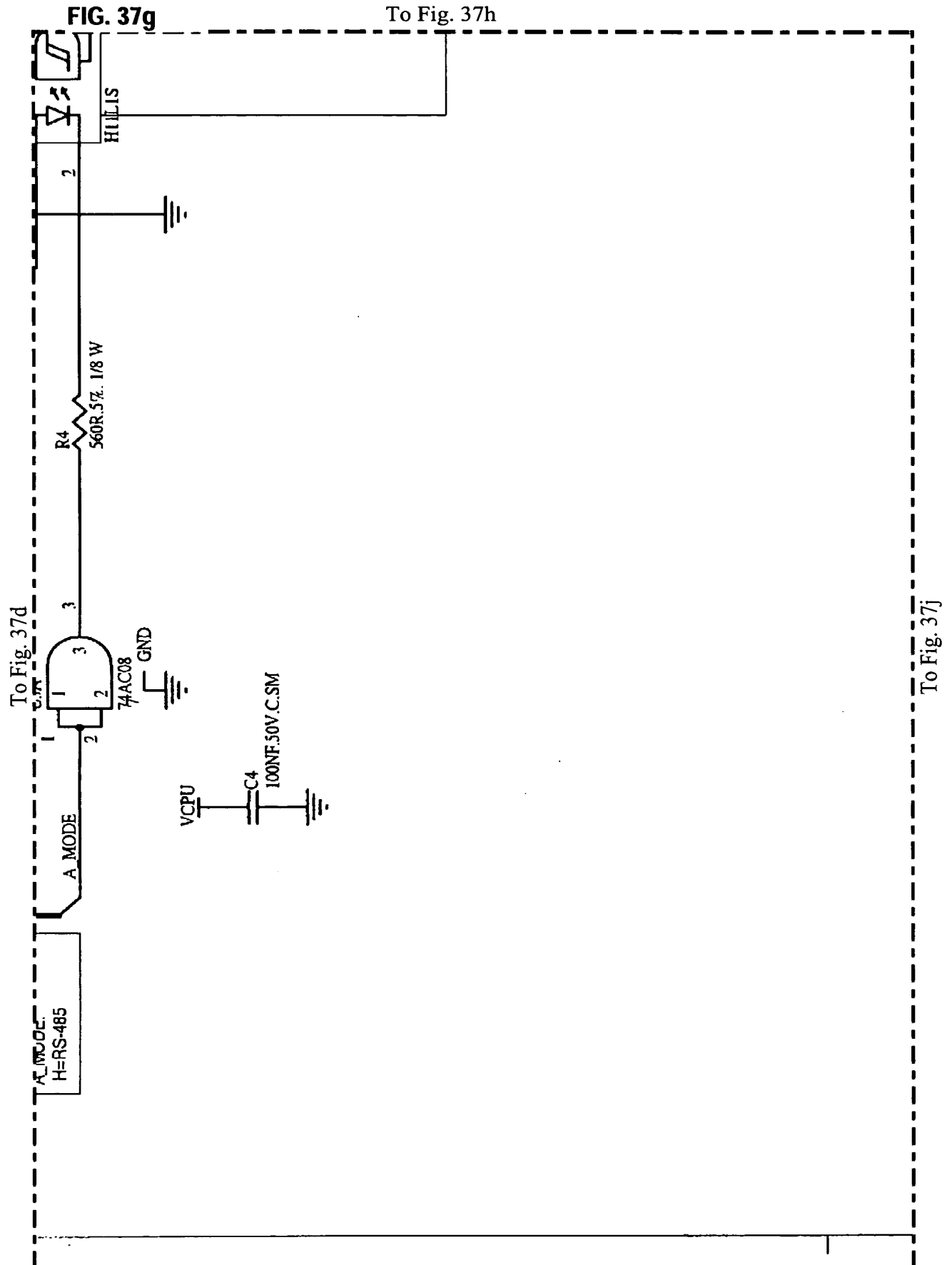


FIG. 37f





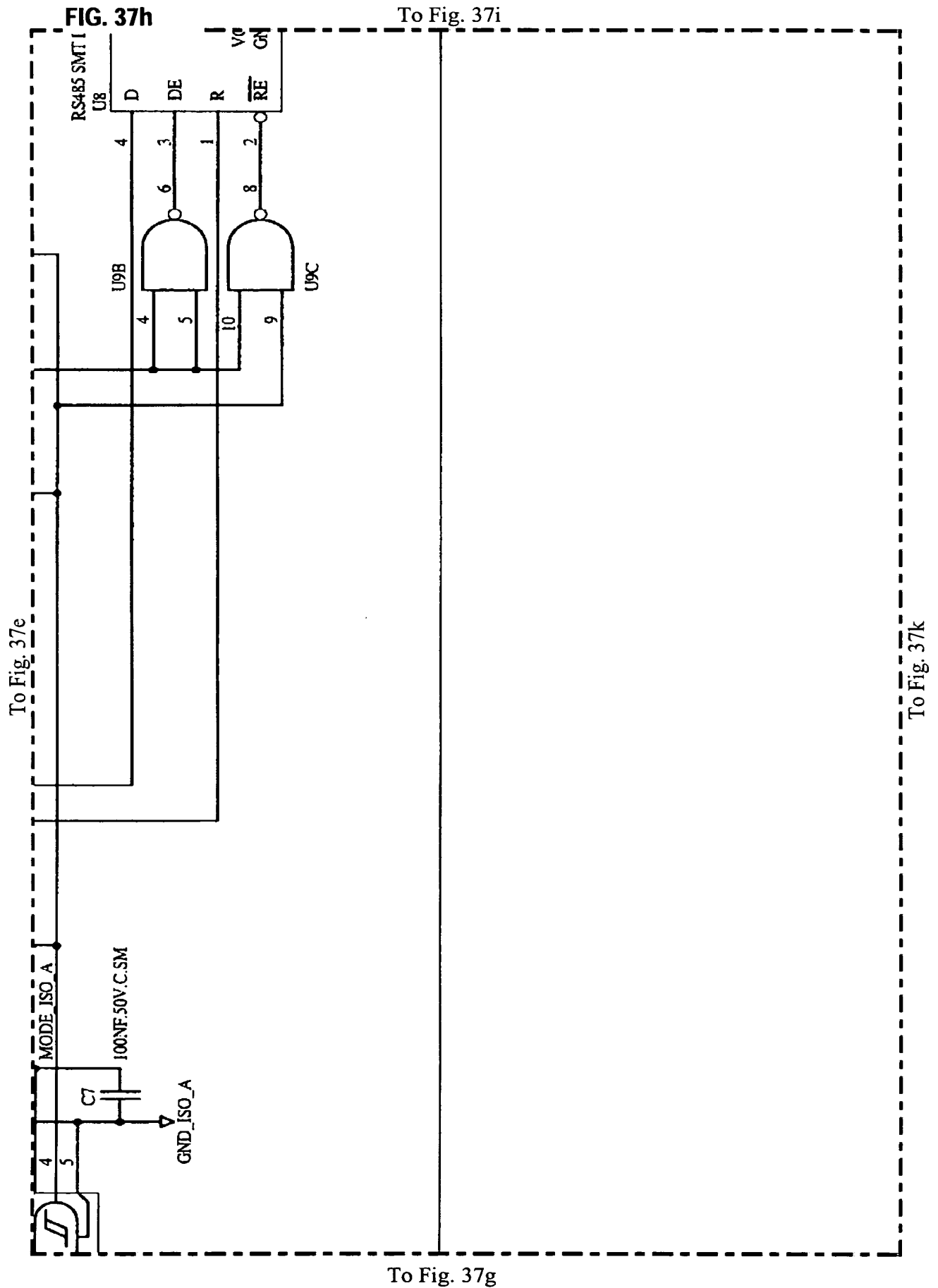
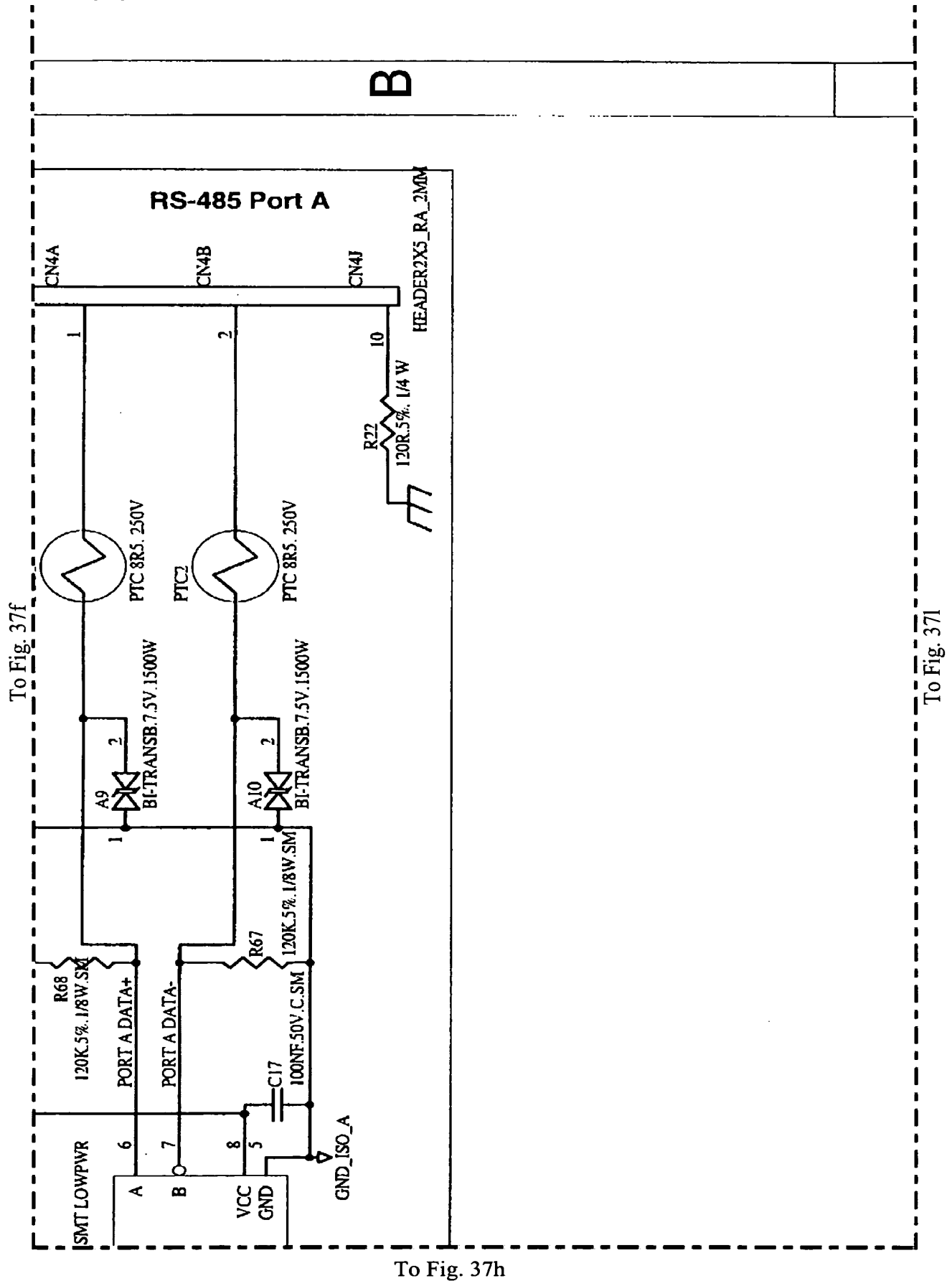
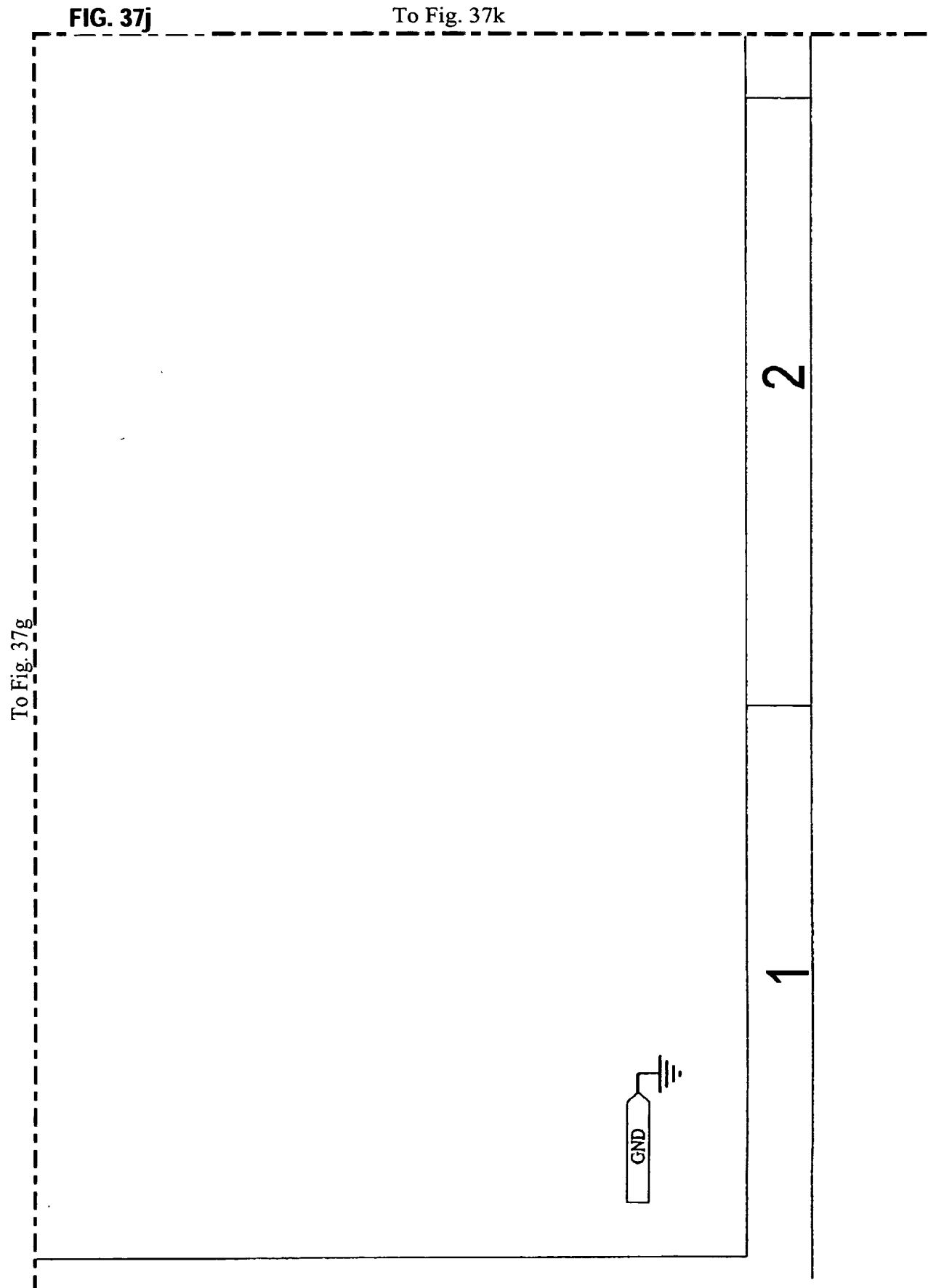


FIG. 37i



To Fig. 37h



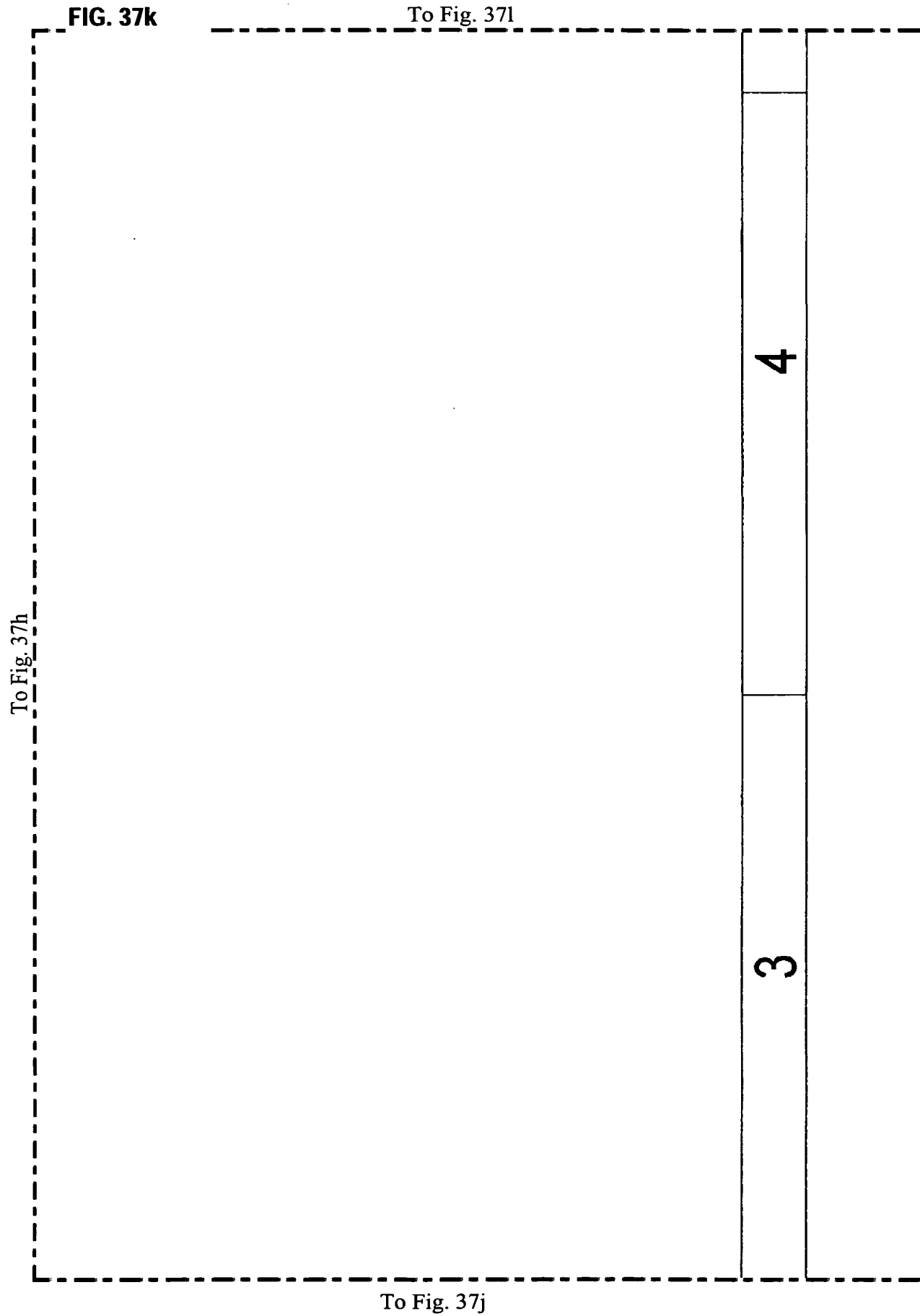


FIG. 37l

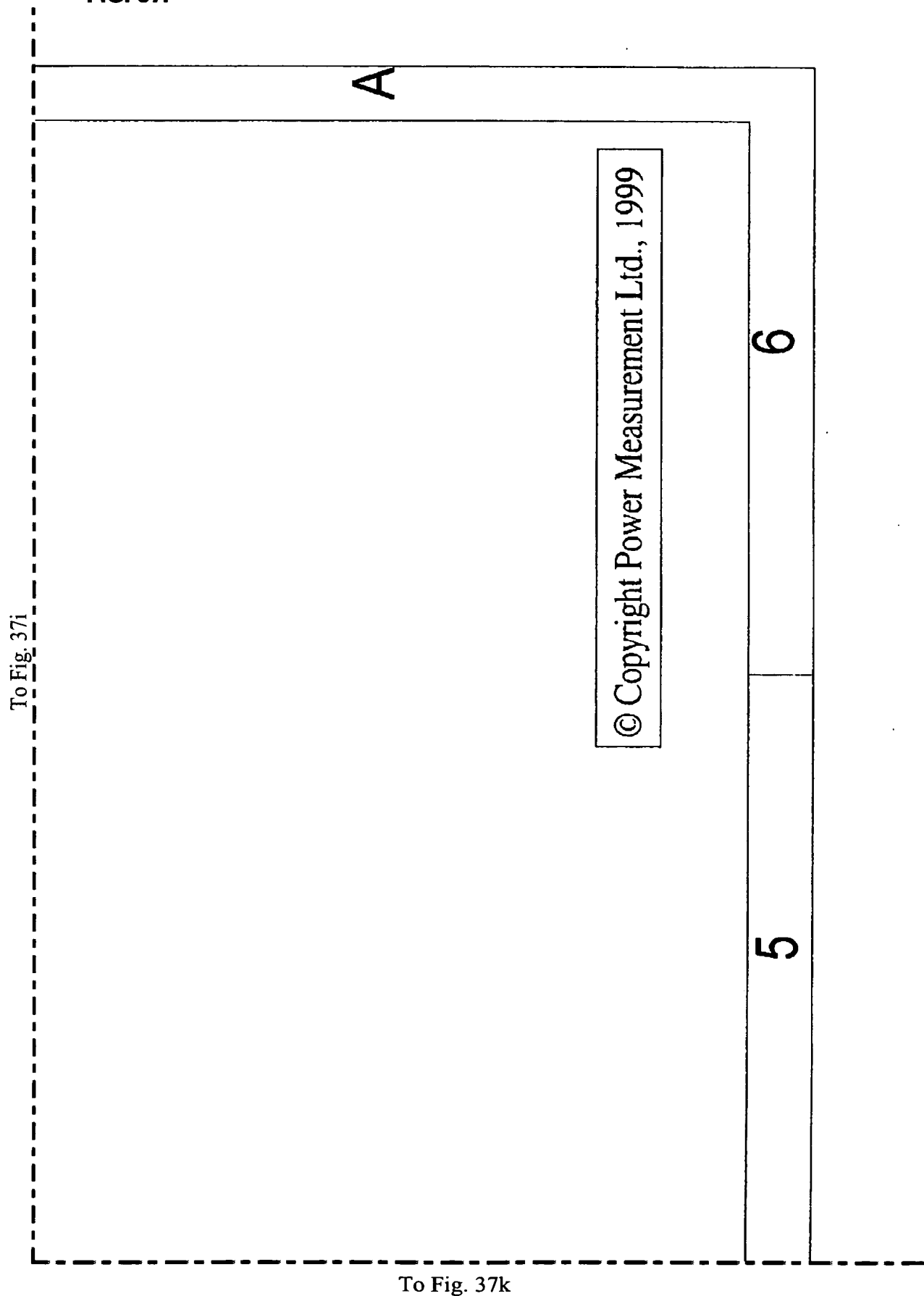
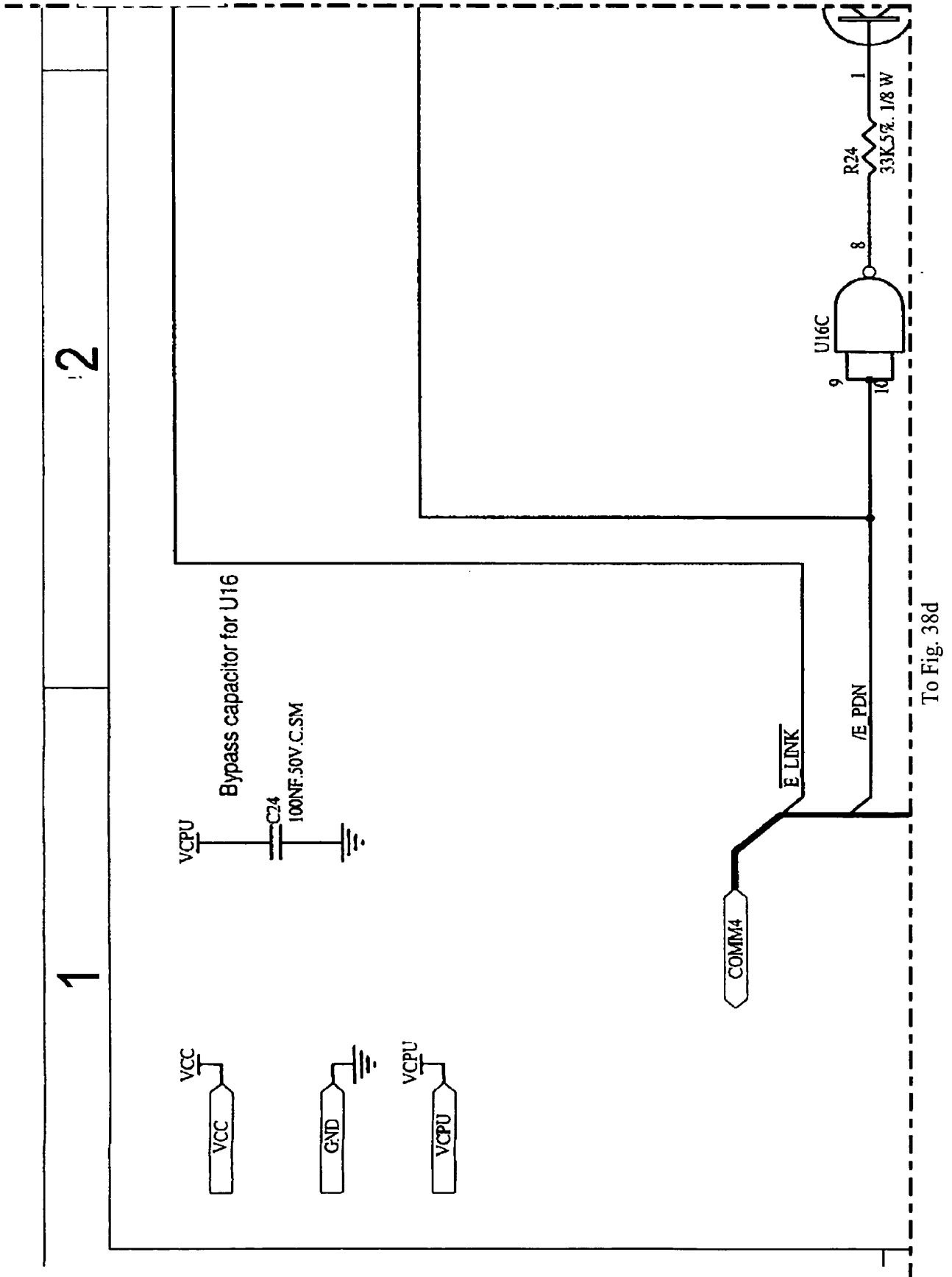


FIG. 38a

To Fig. 38b



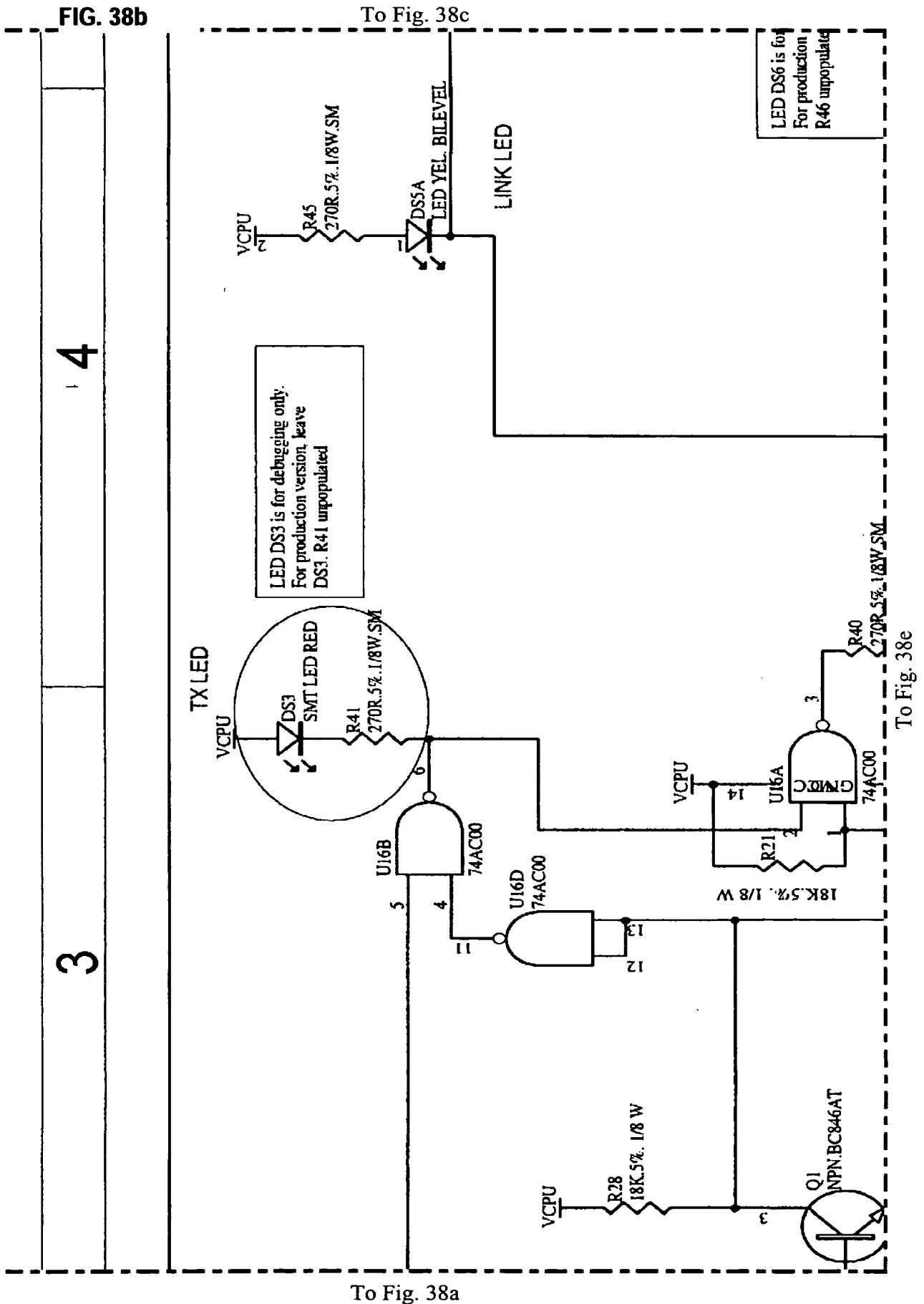
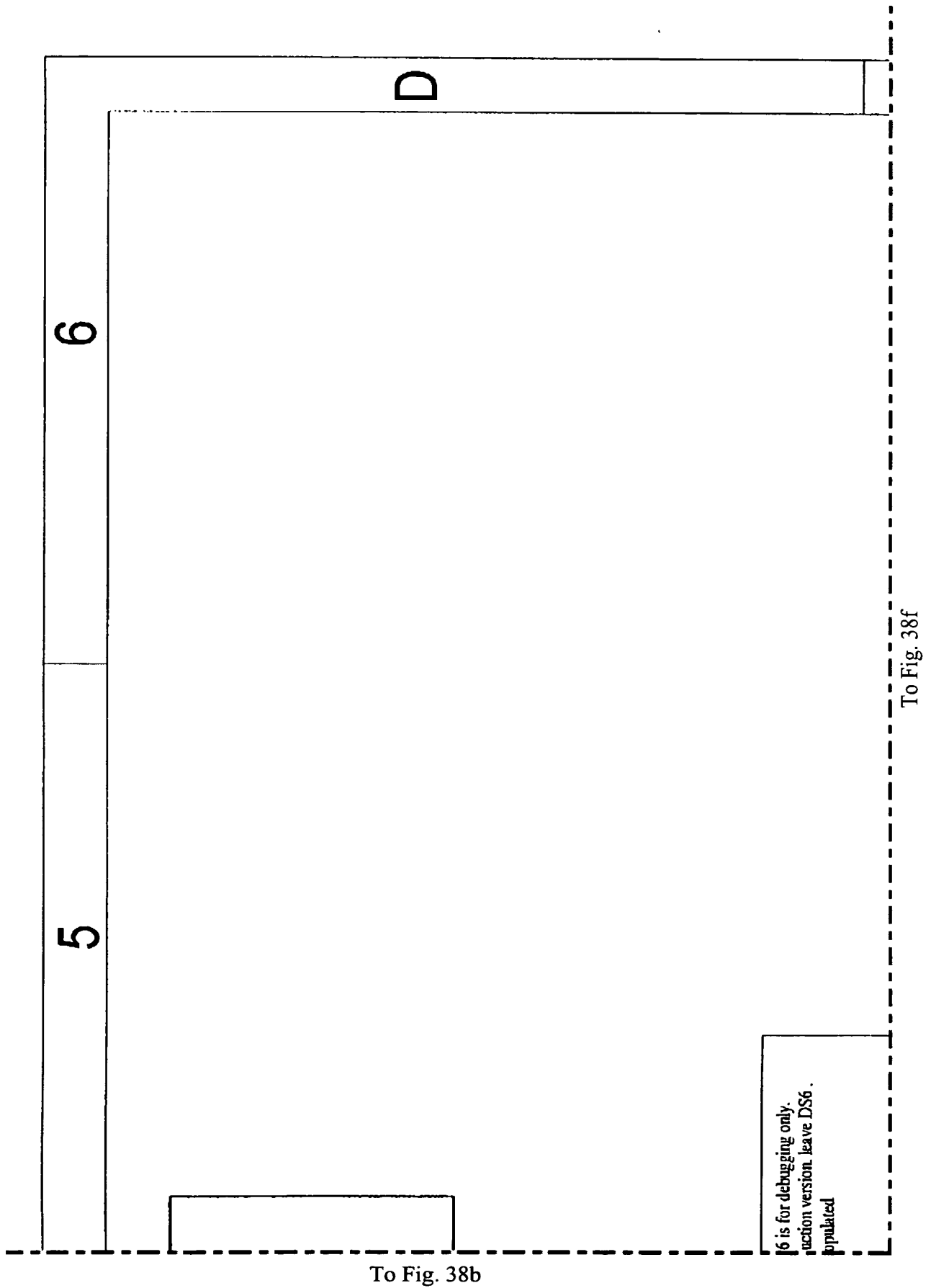
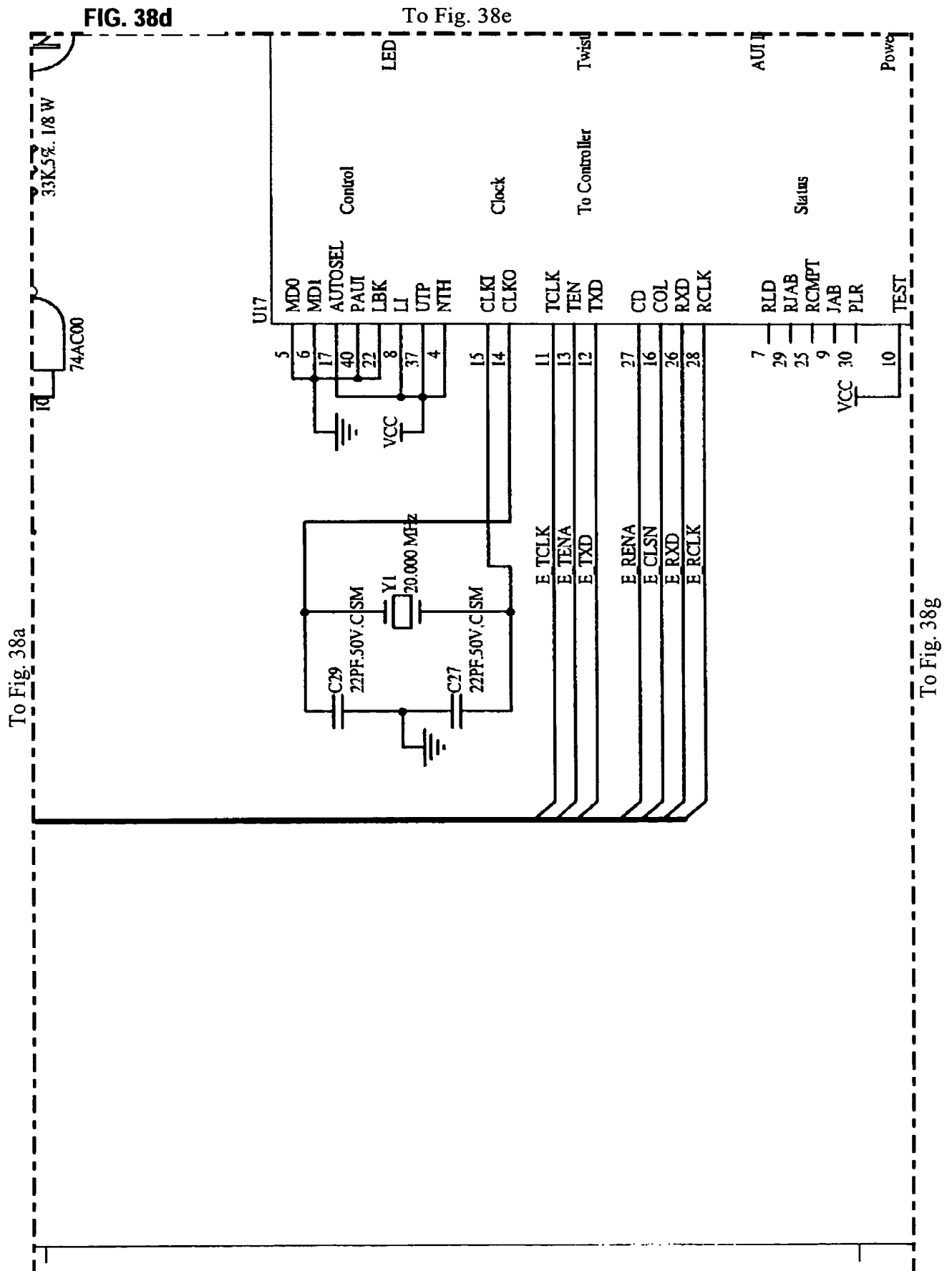


FIG. 38c





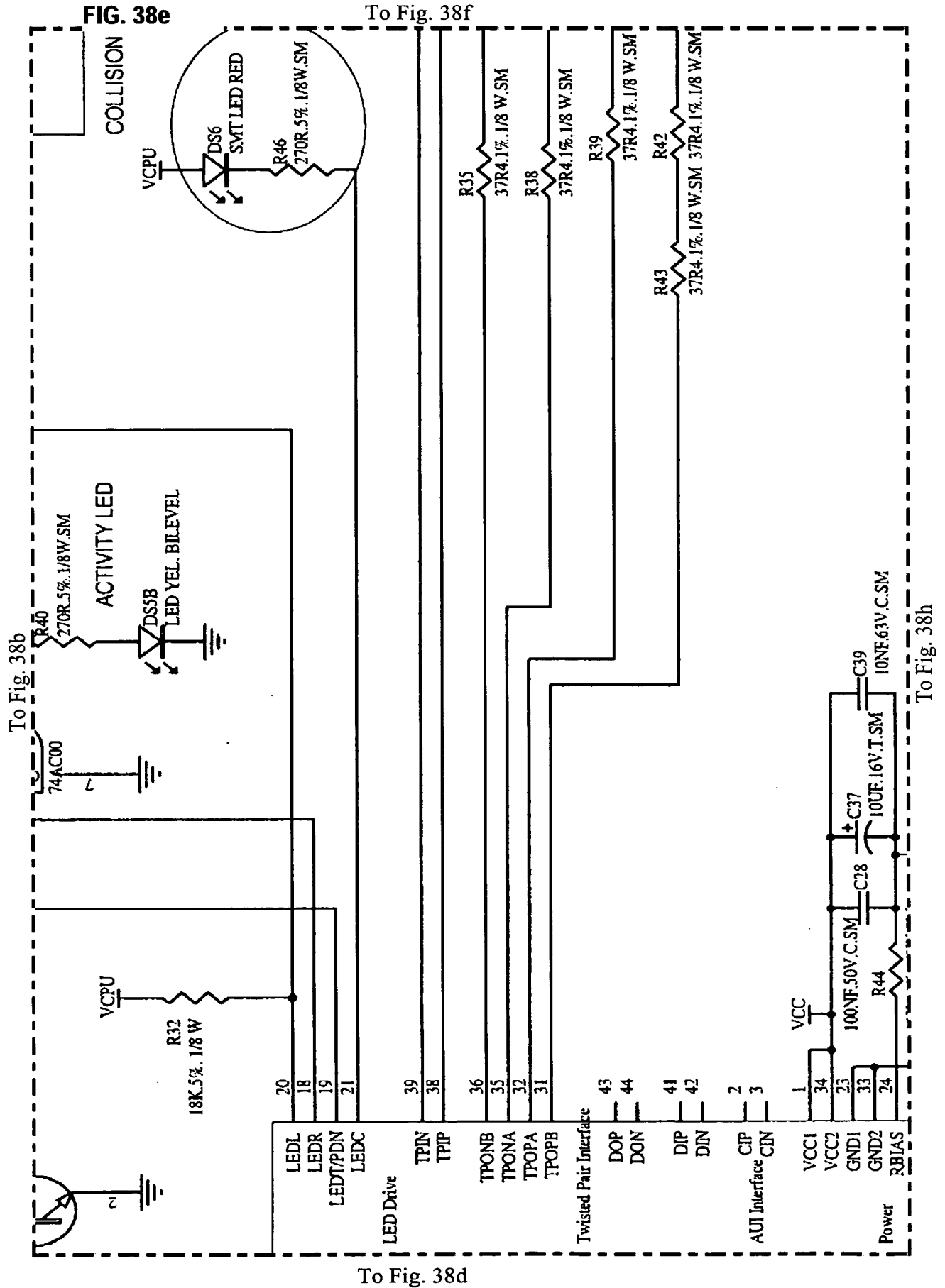
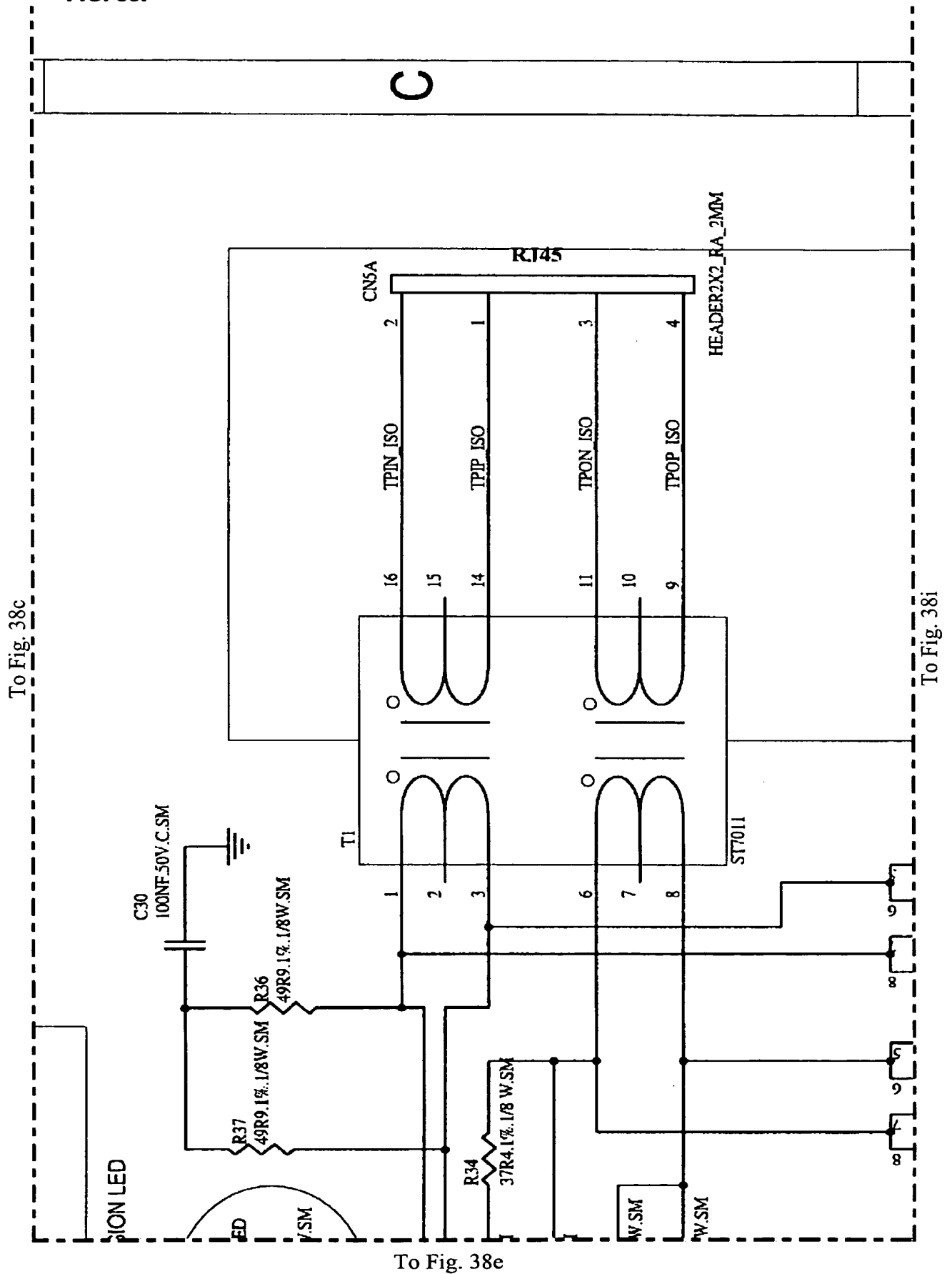
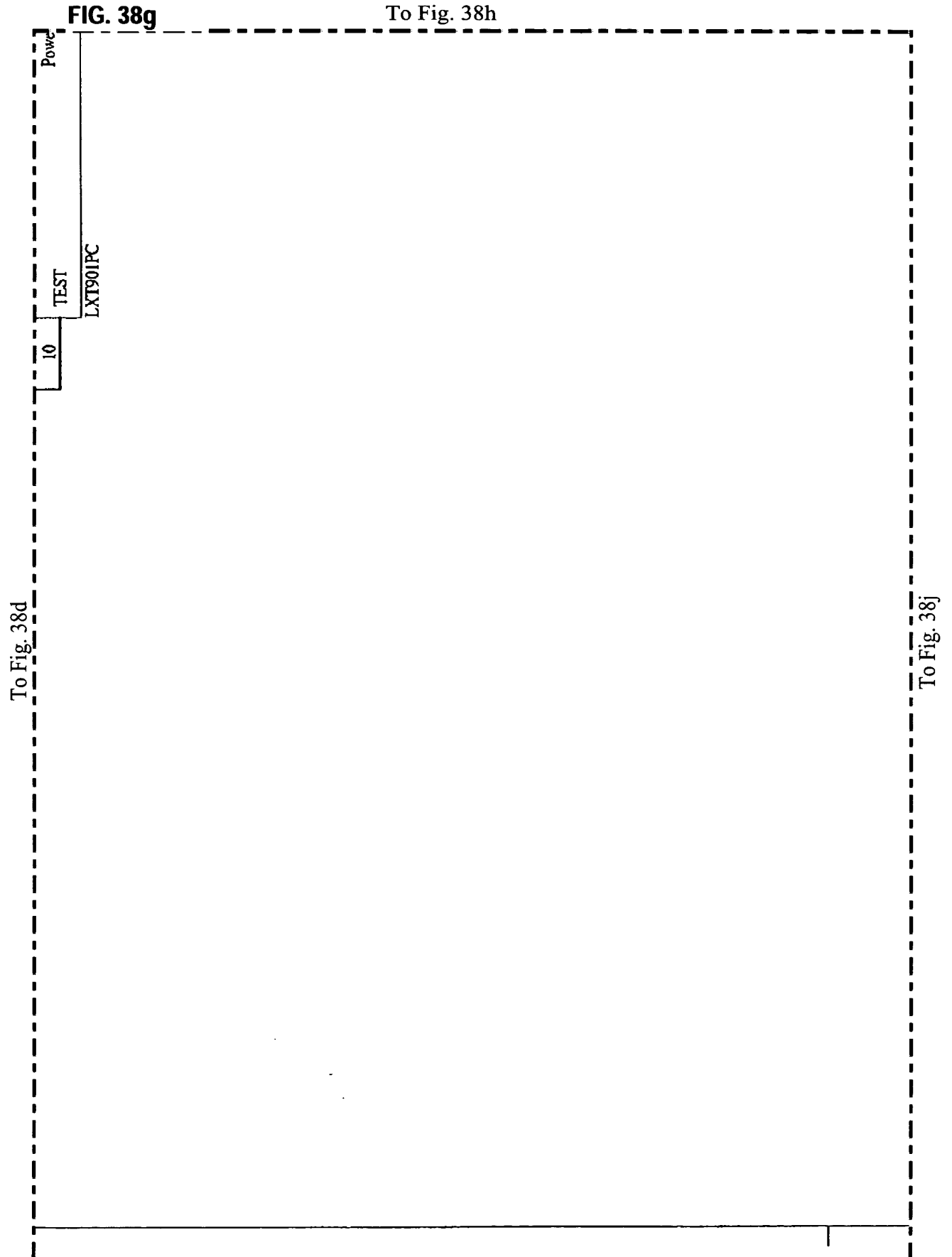


FIG. 38f





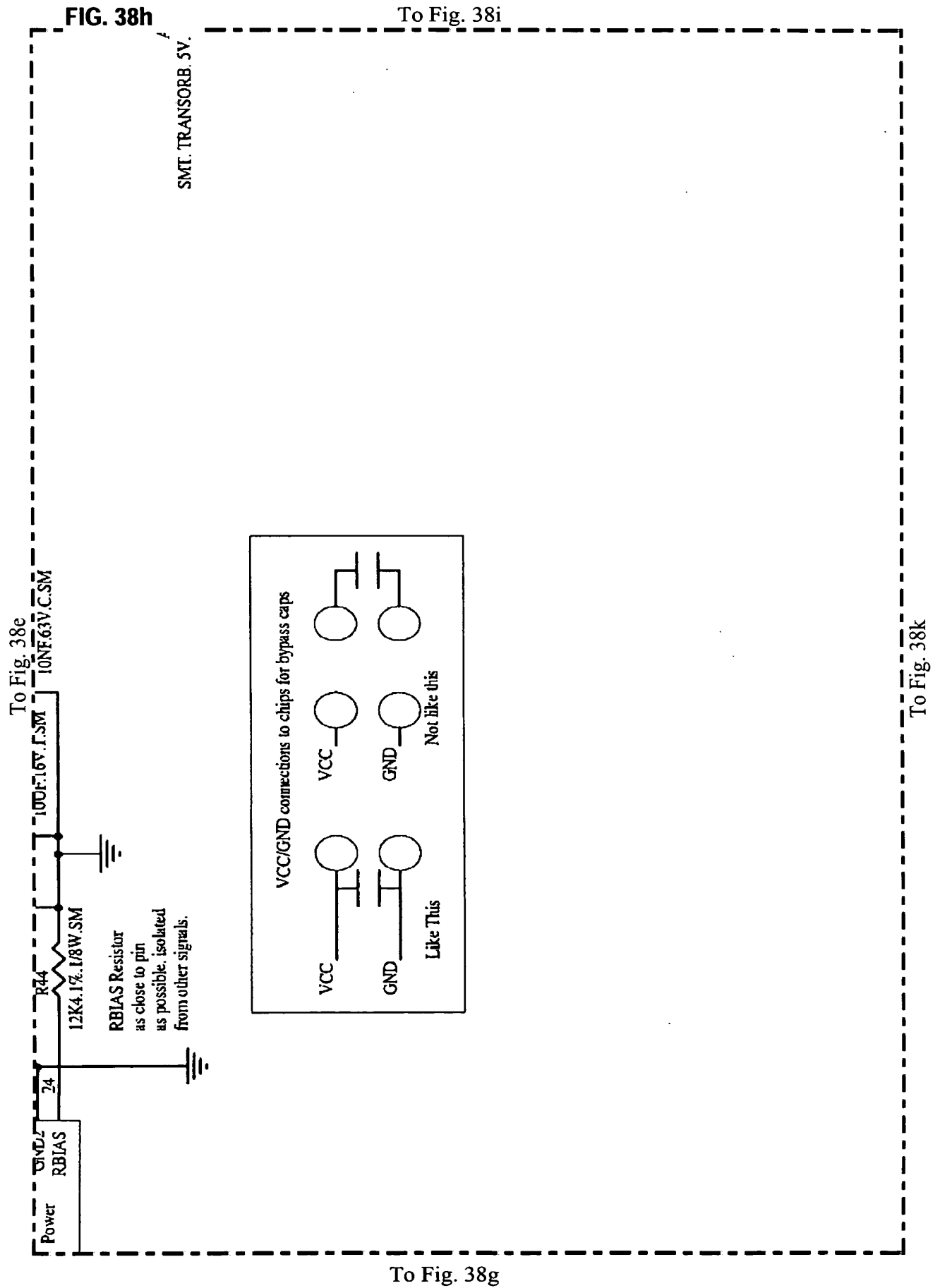


FIG. 38i

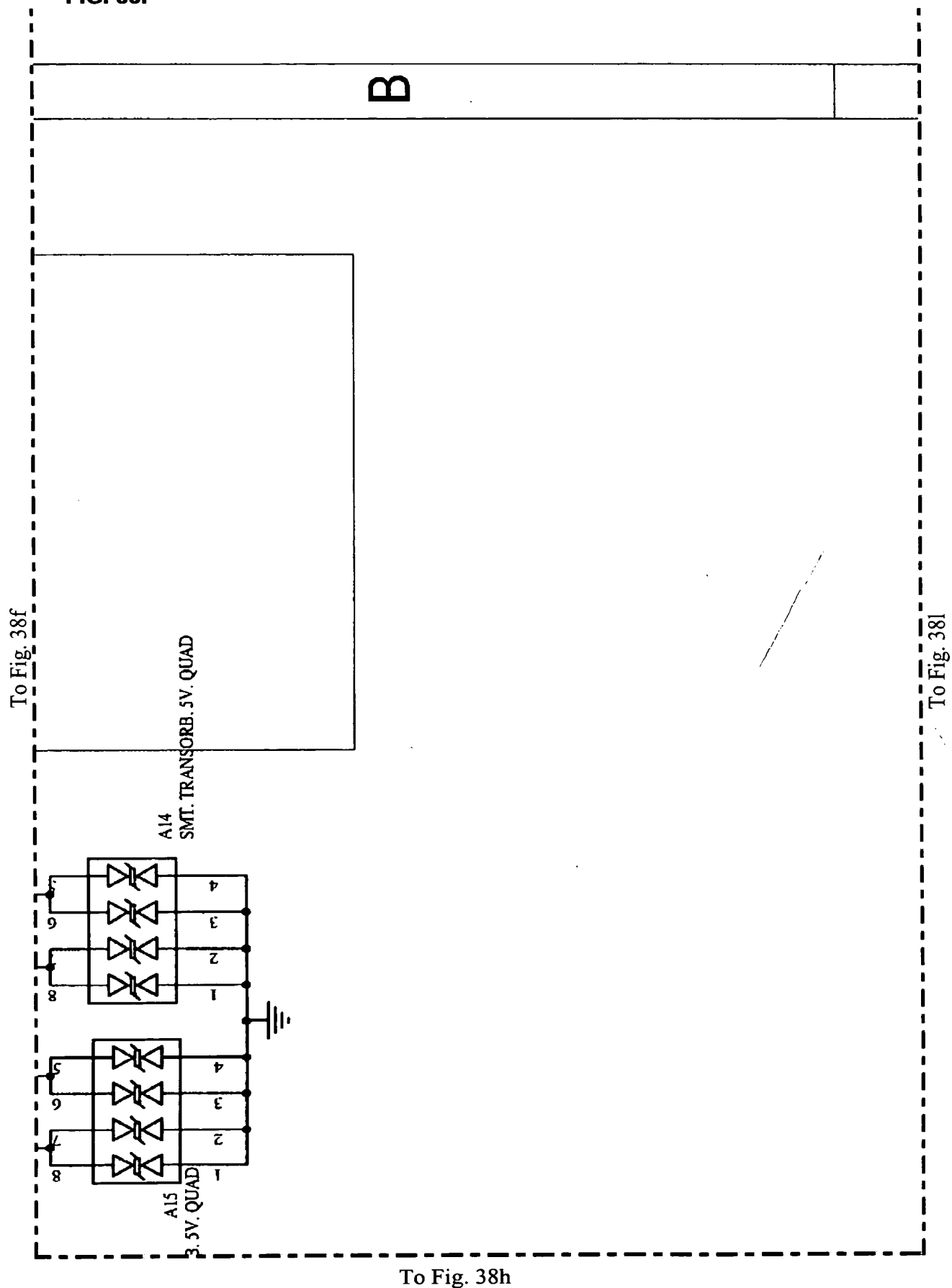


FIG. 38j

To Fig. 38k

To Fig. 38g

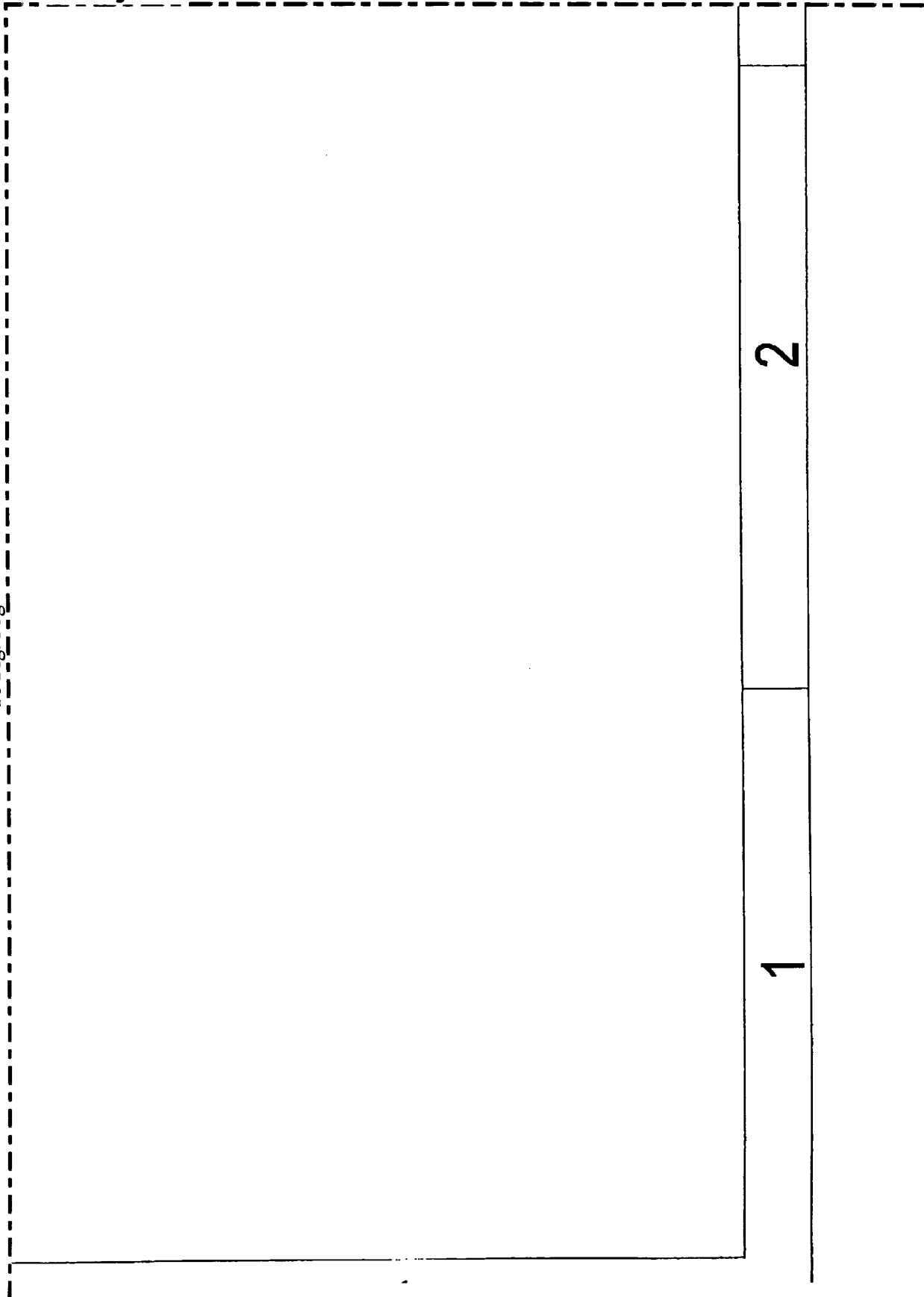


FIG. 38k

To Fig. 38l

To Fig. 38h

4

3

To Fig. 38j

FIG. 38l

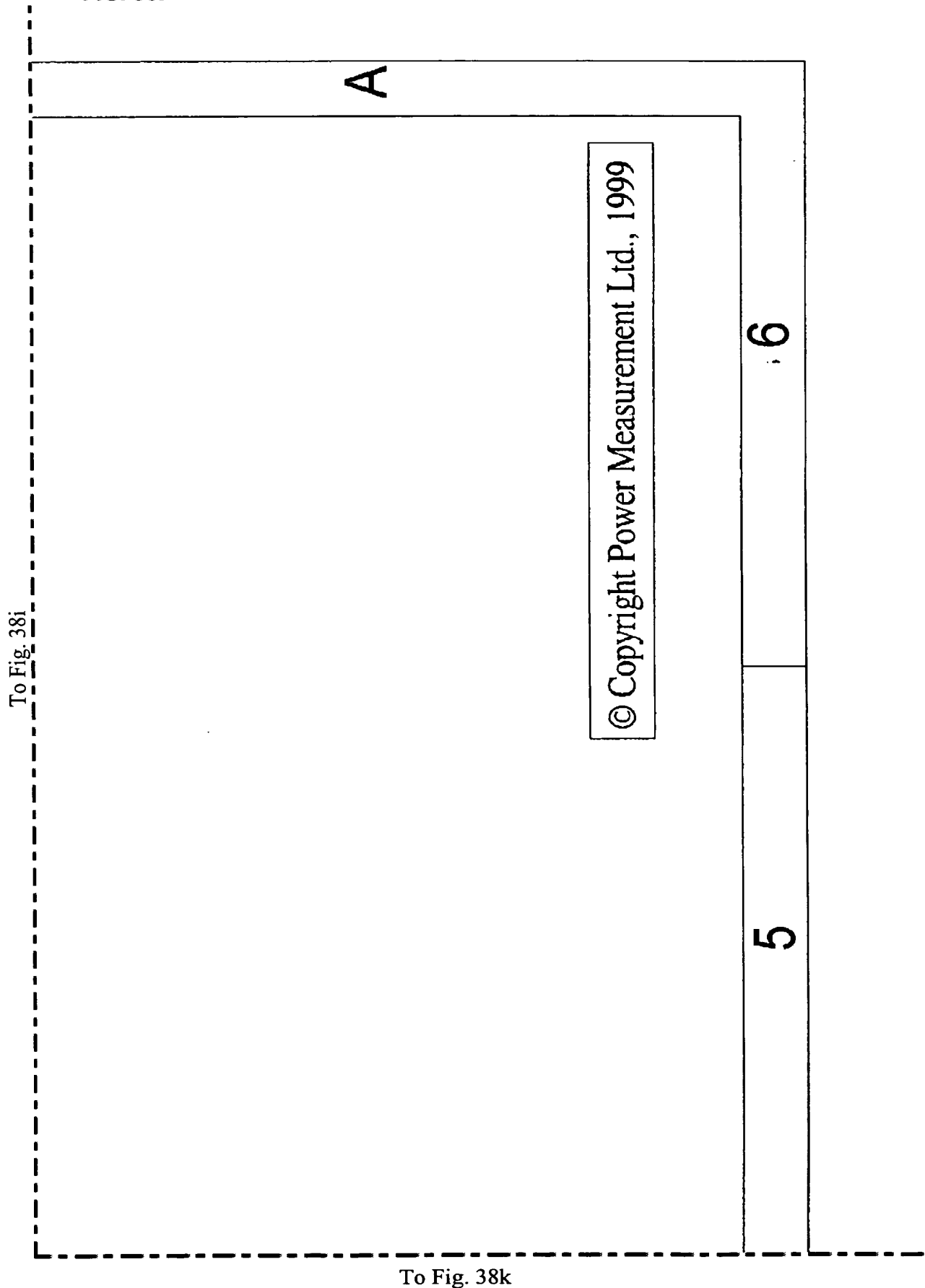
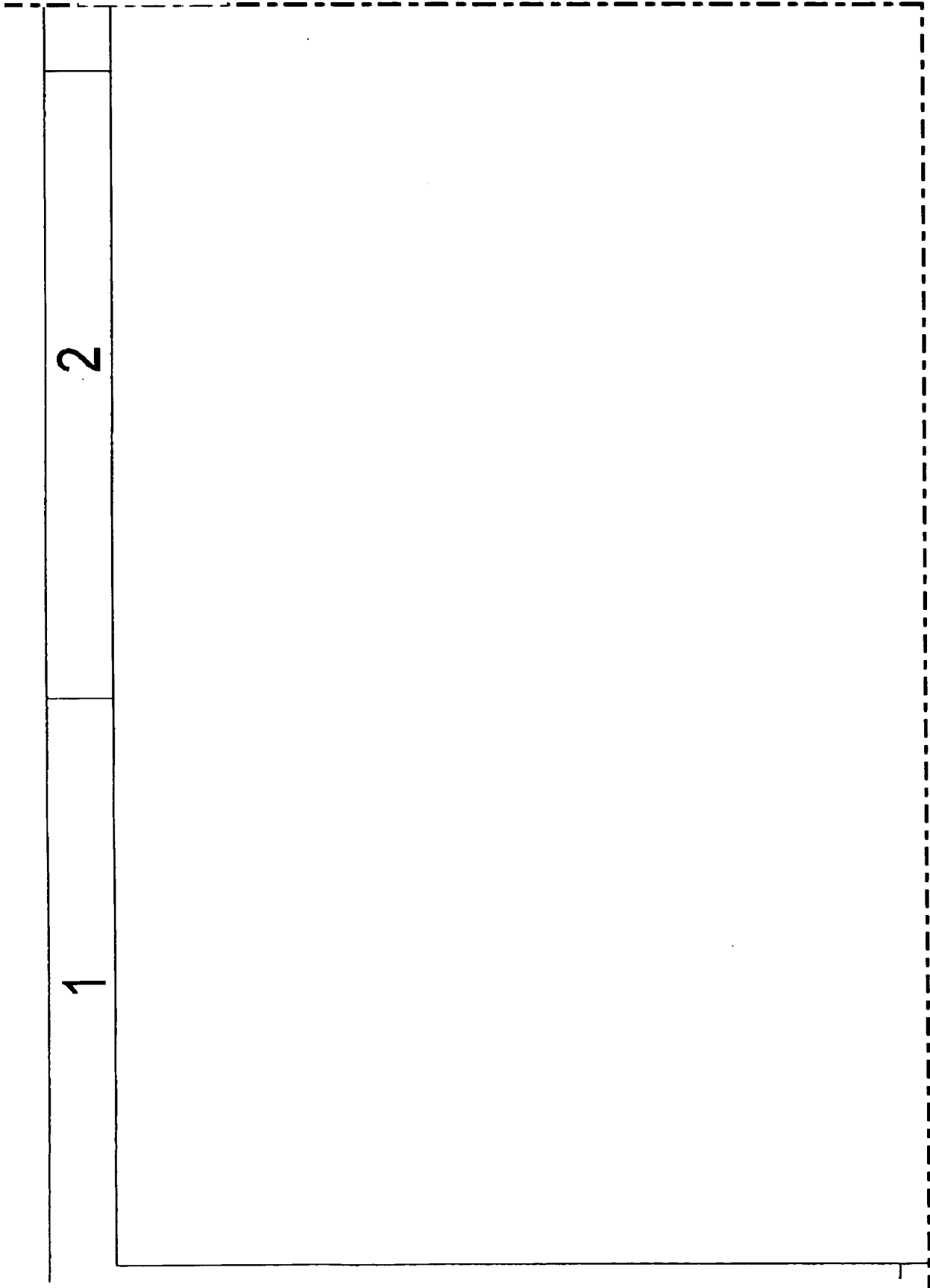


FIG. 39a

To Fig. 39b



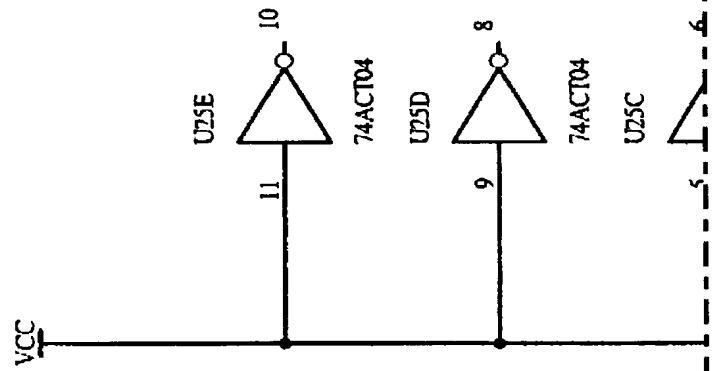
To Fig. 39d

FIG. 39b

4

3

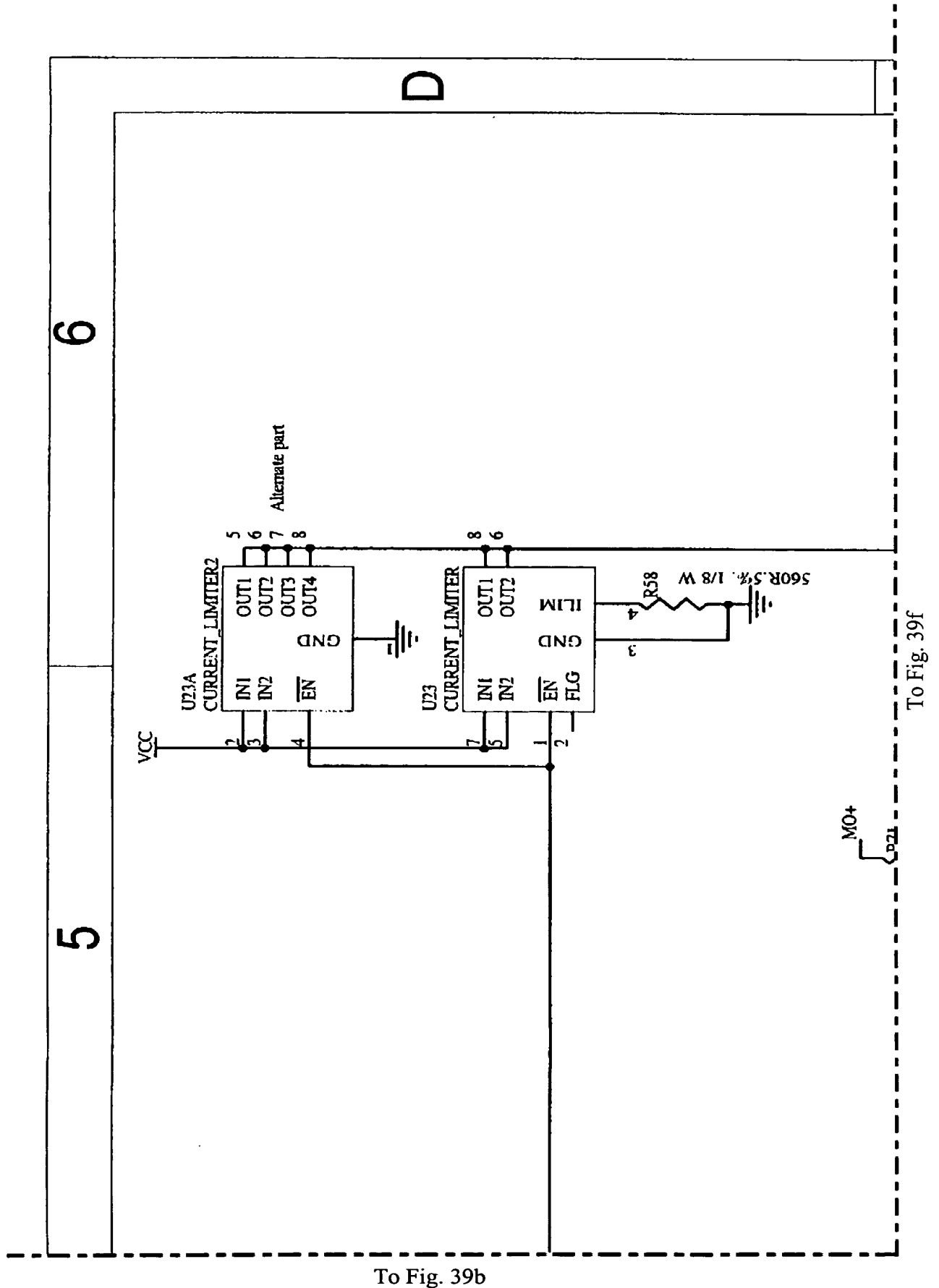
To Fig. 39c



To Fig. 39e

To Fig. 39a

FIG. 39c

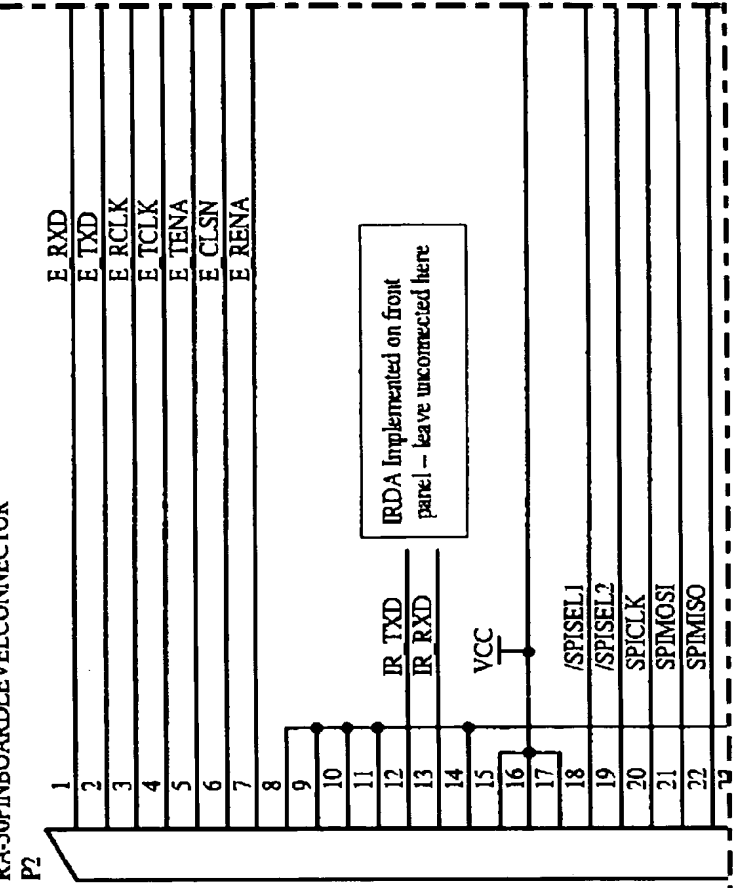


To Fig. 39a

FIG. 39d

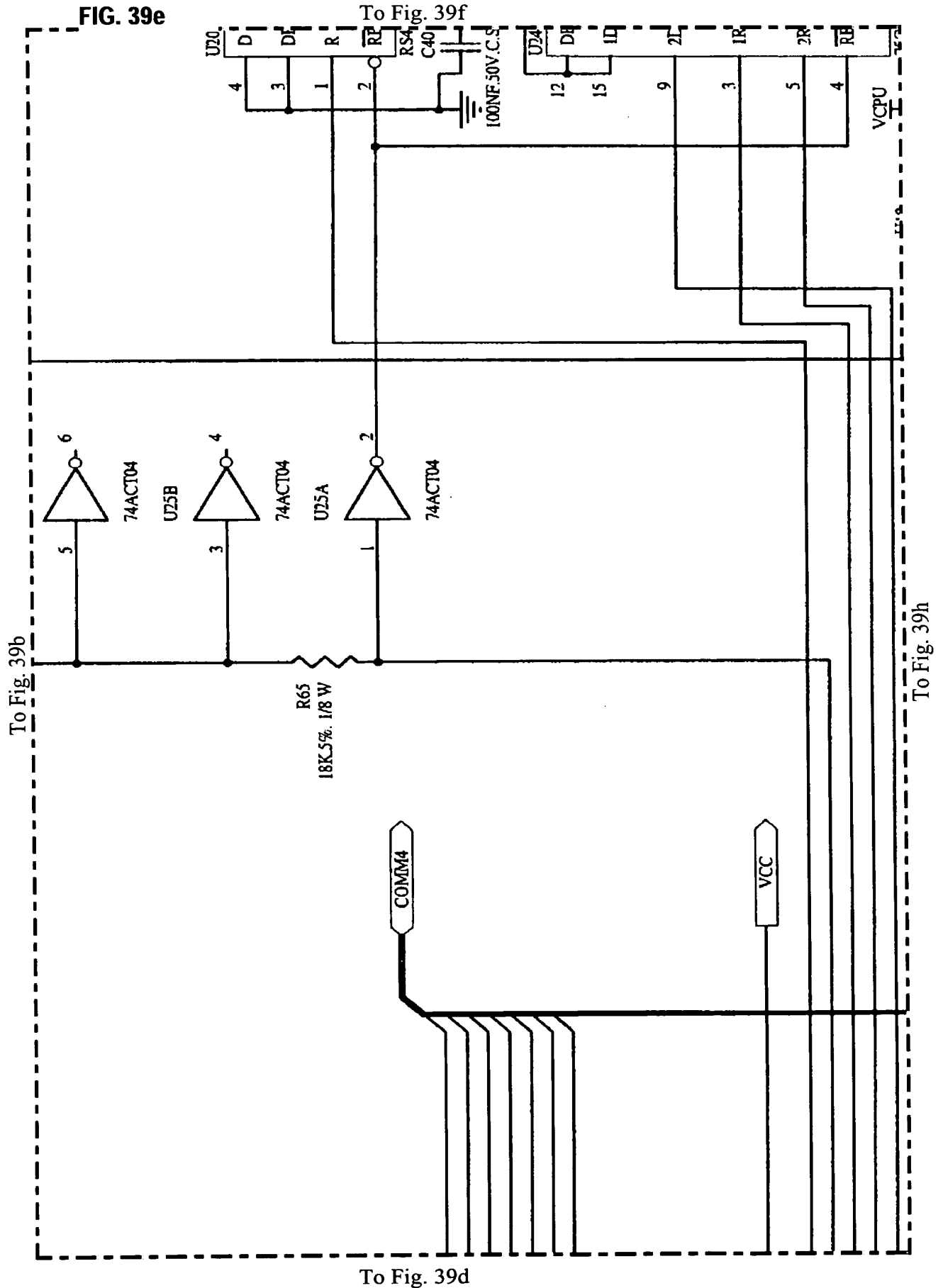
COMMUNICATIONS CARD CONNECTOR

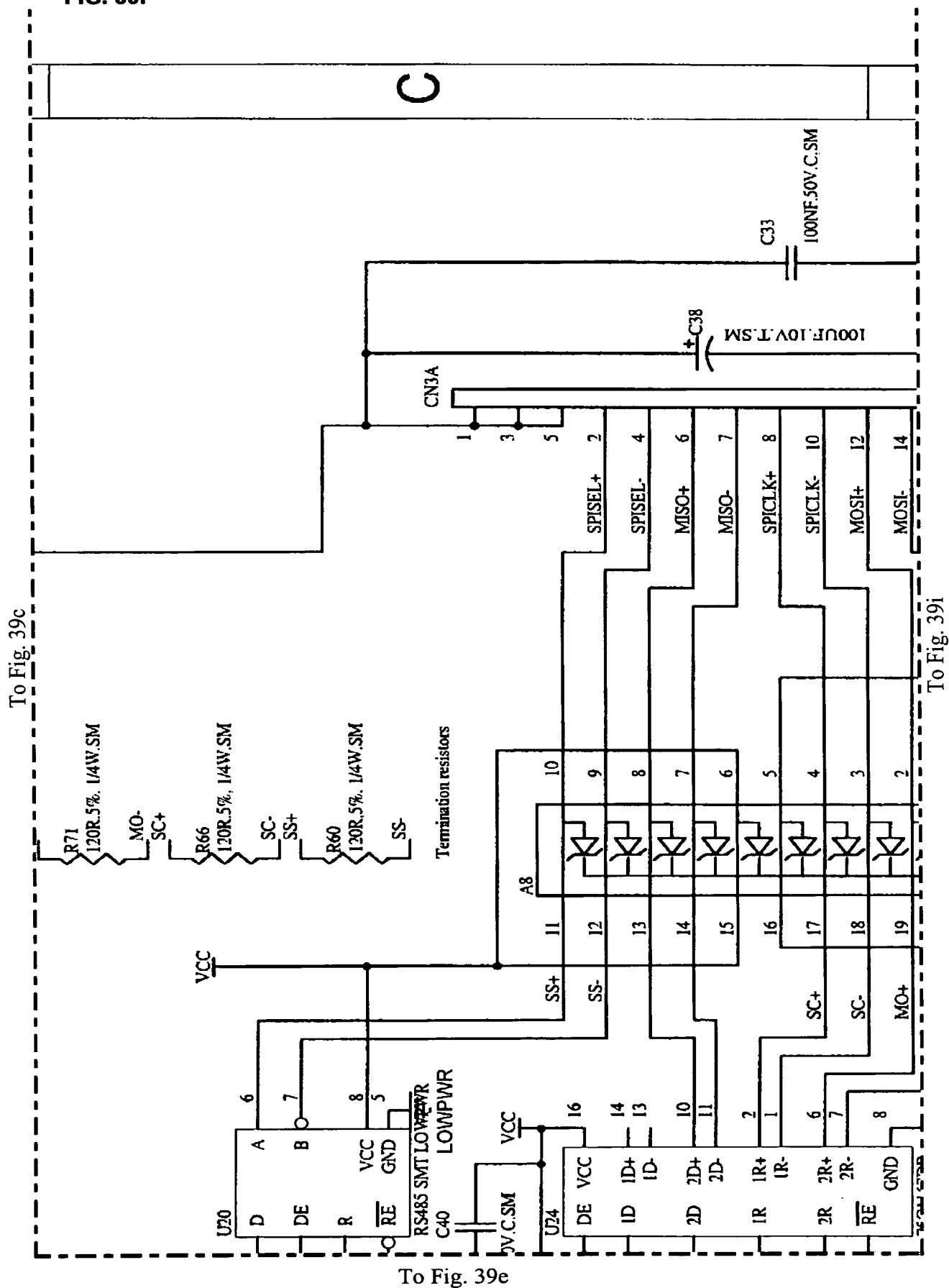
RA-50PINBOARDLEVELCONNECTOR
P2



To Fig. 39e

To Fig. 39g





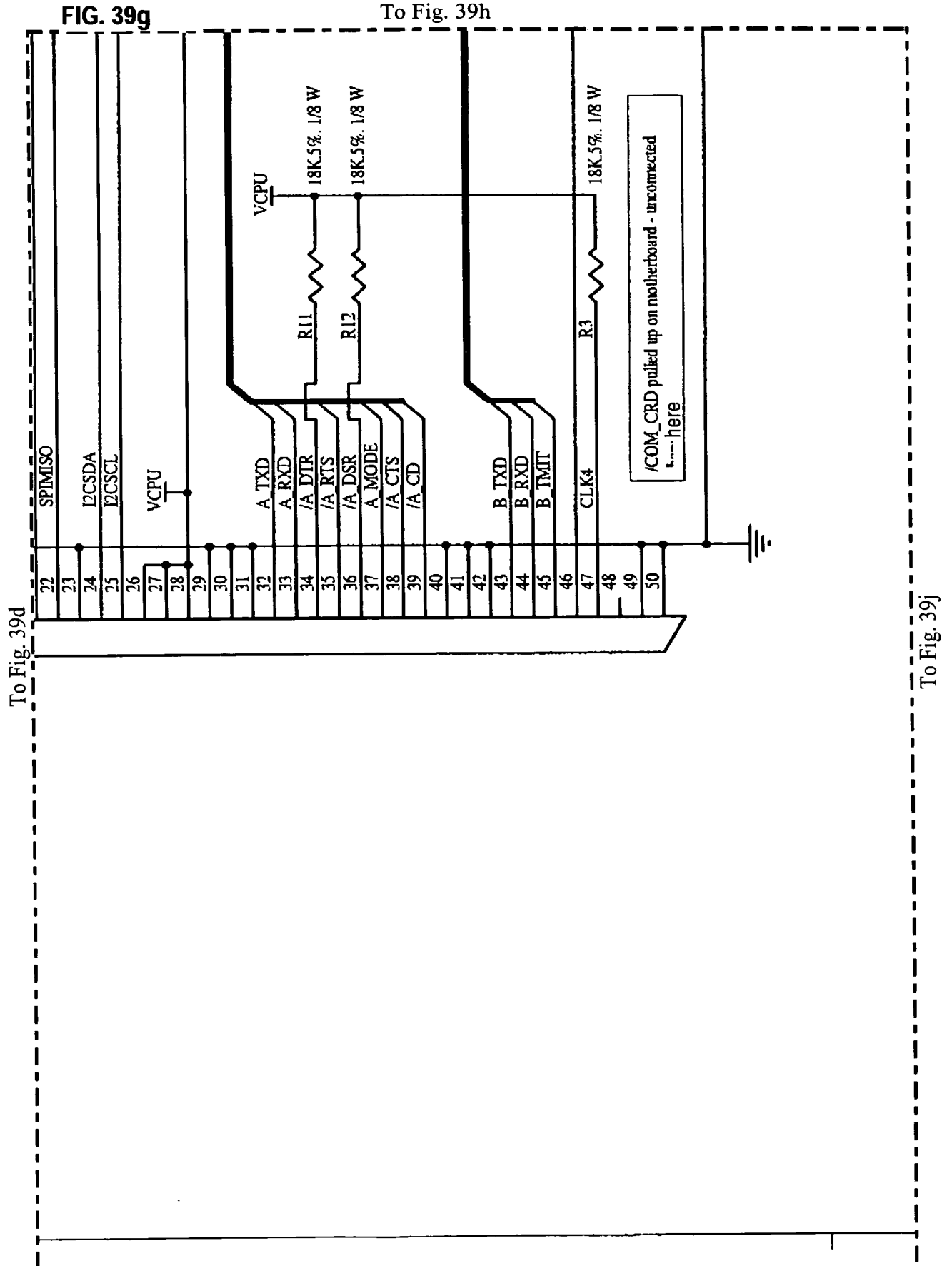
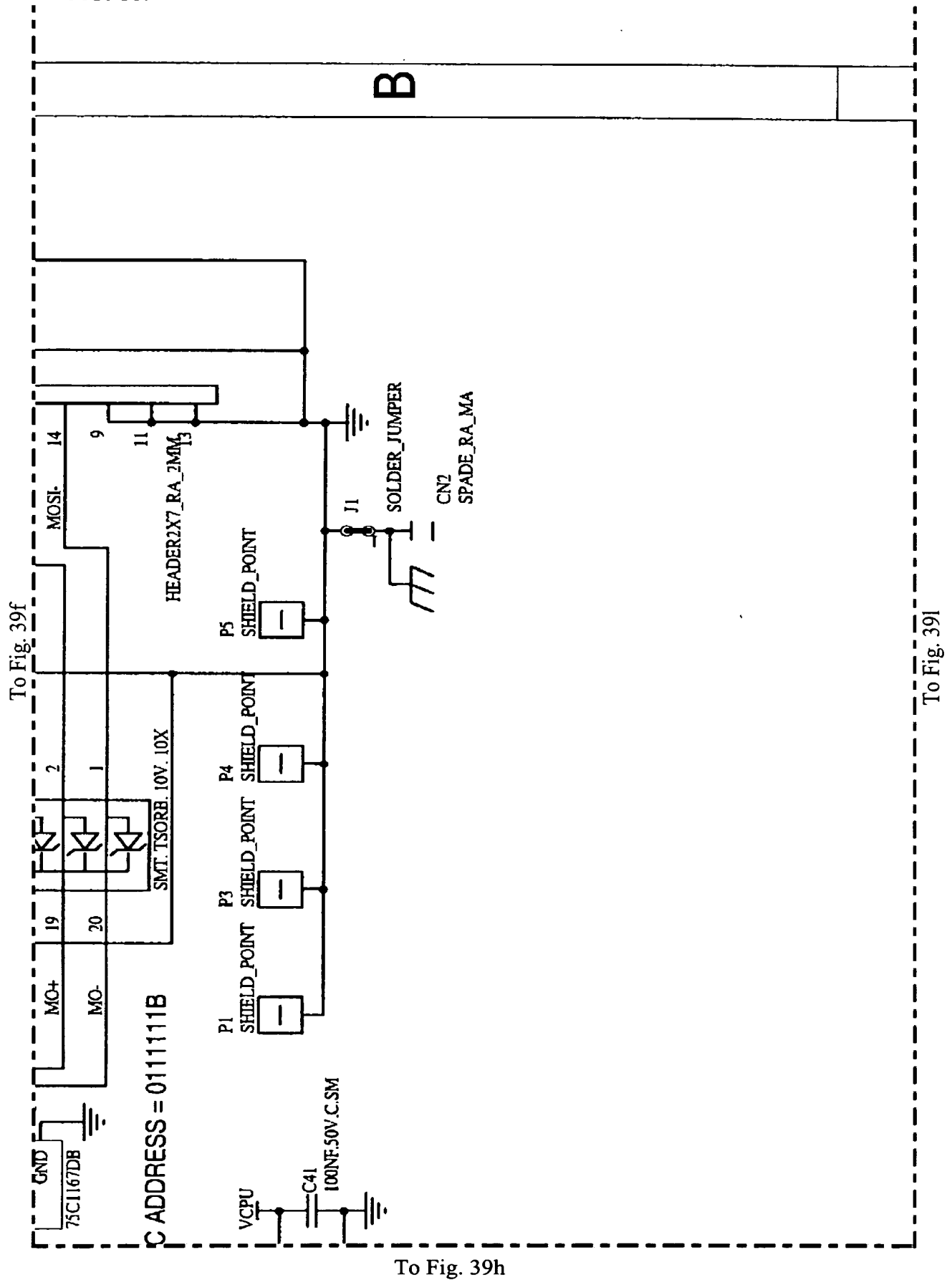
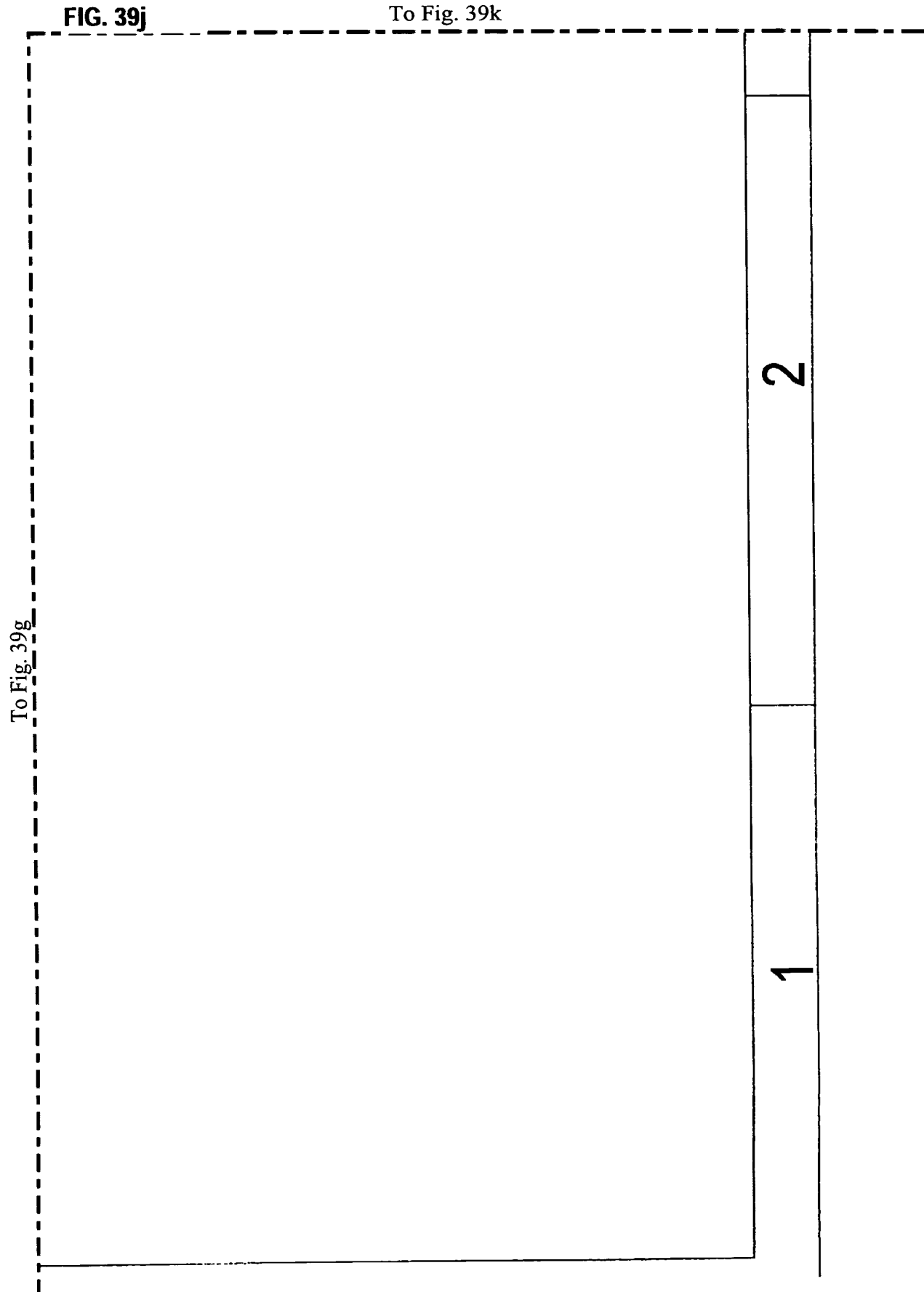


FIG. 39i





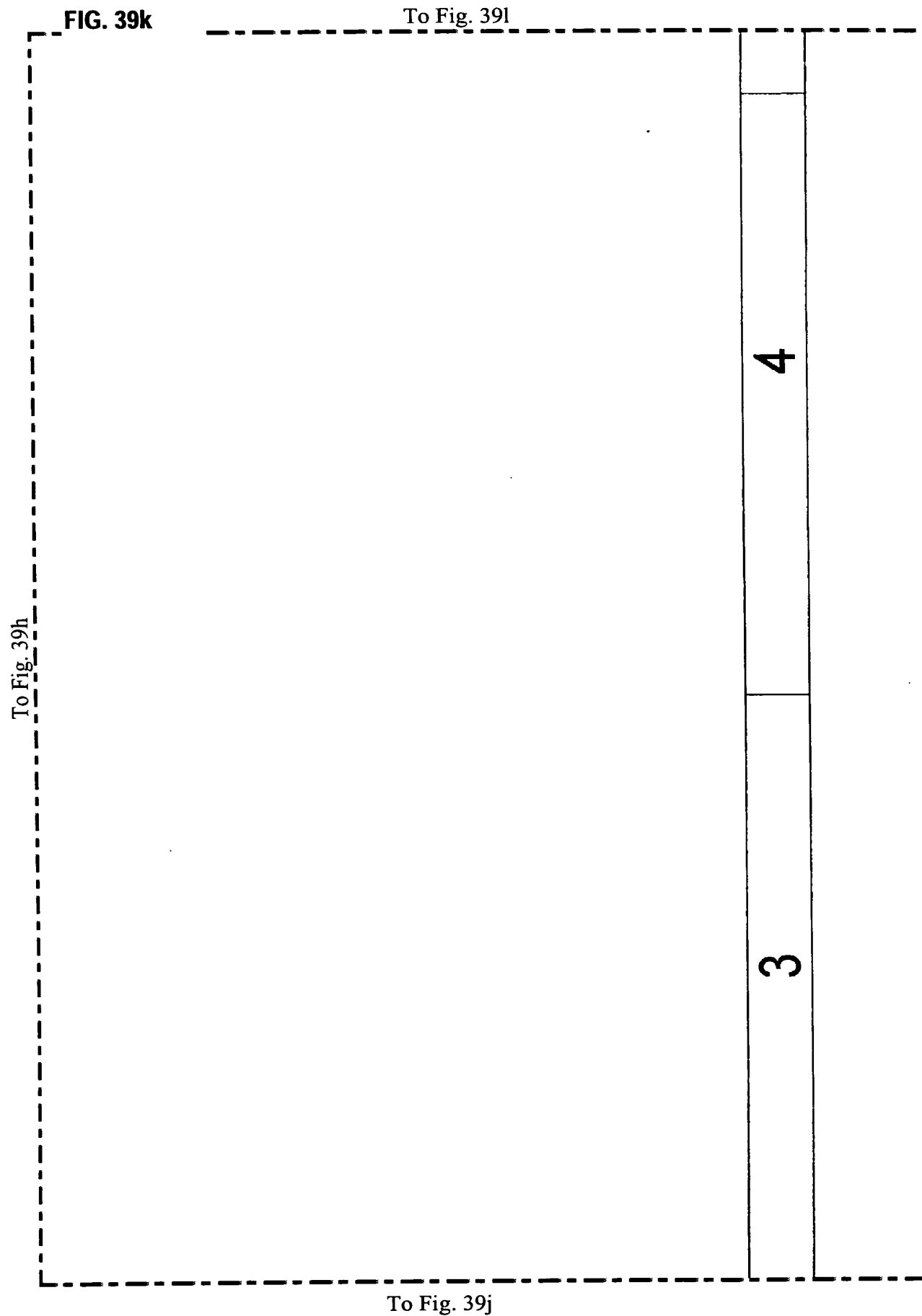


FIG. 39I

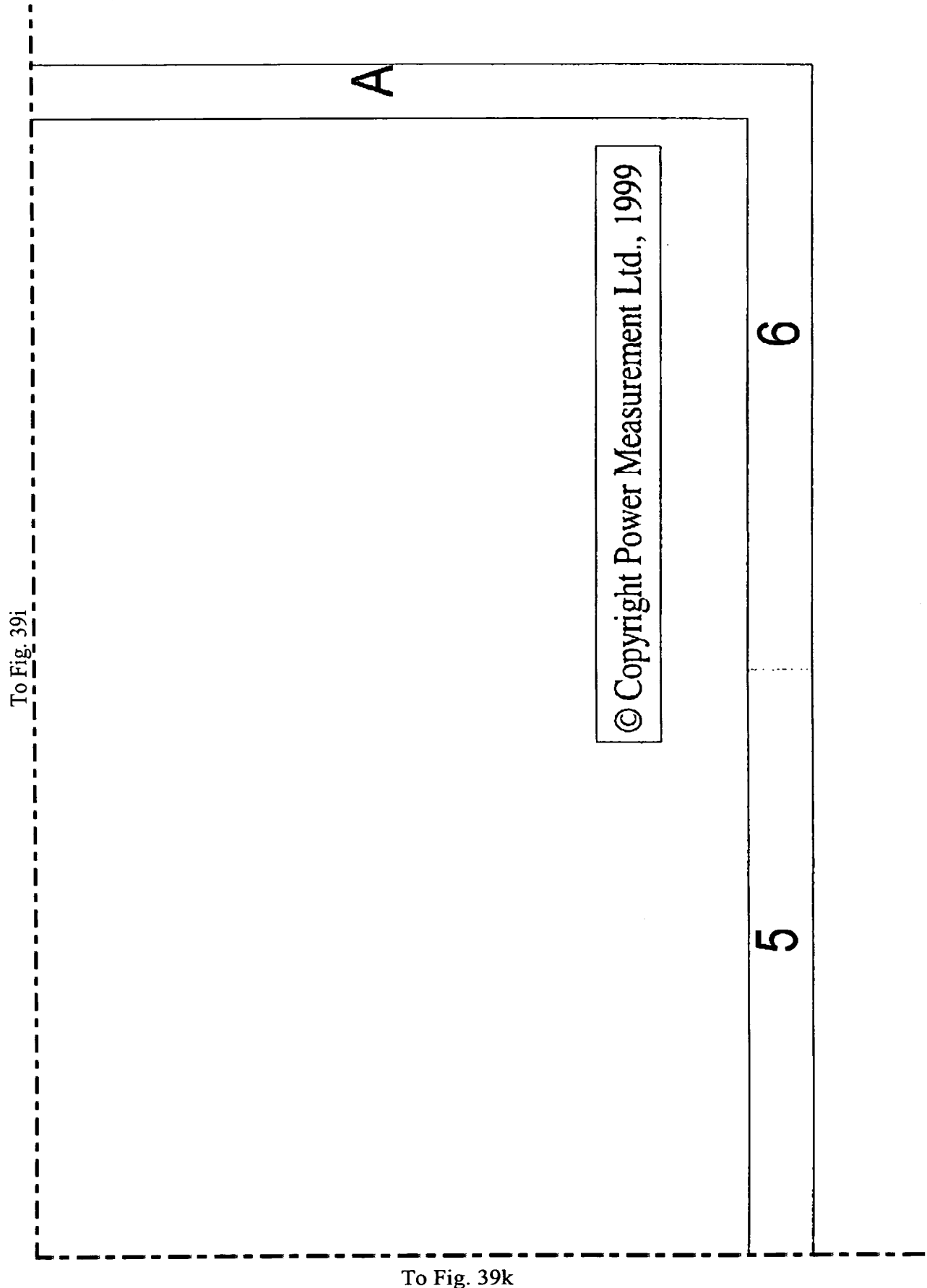


FIG. 40a

To Fig. 40b

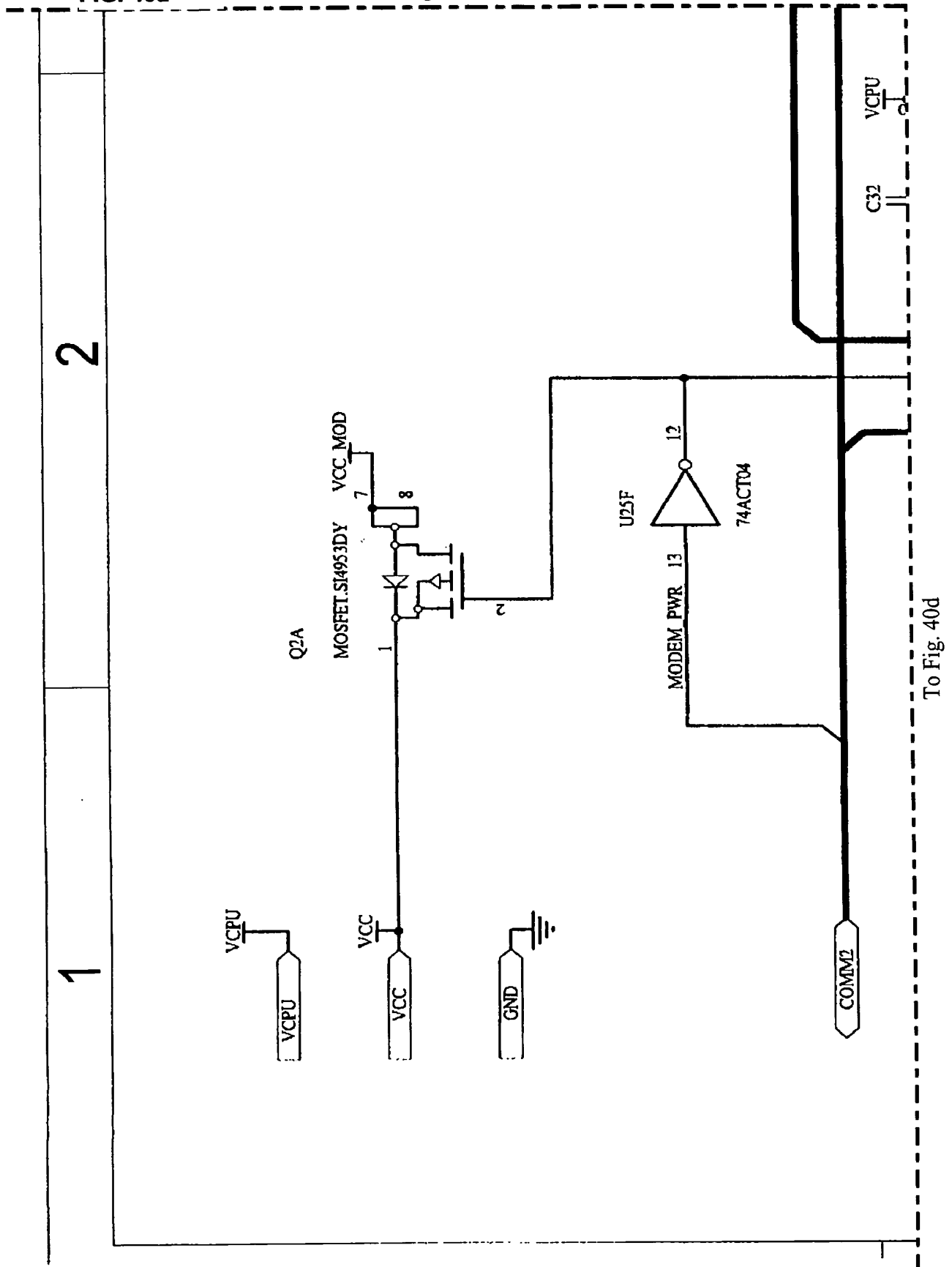
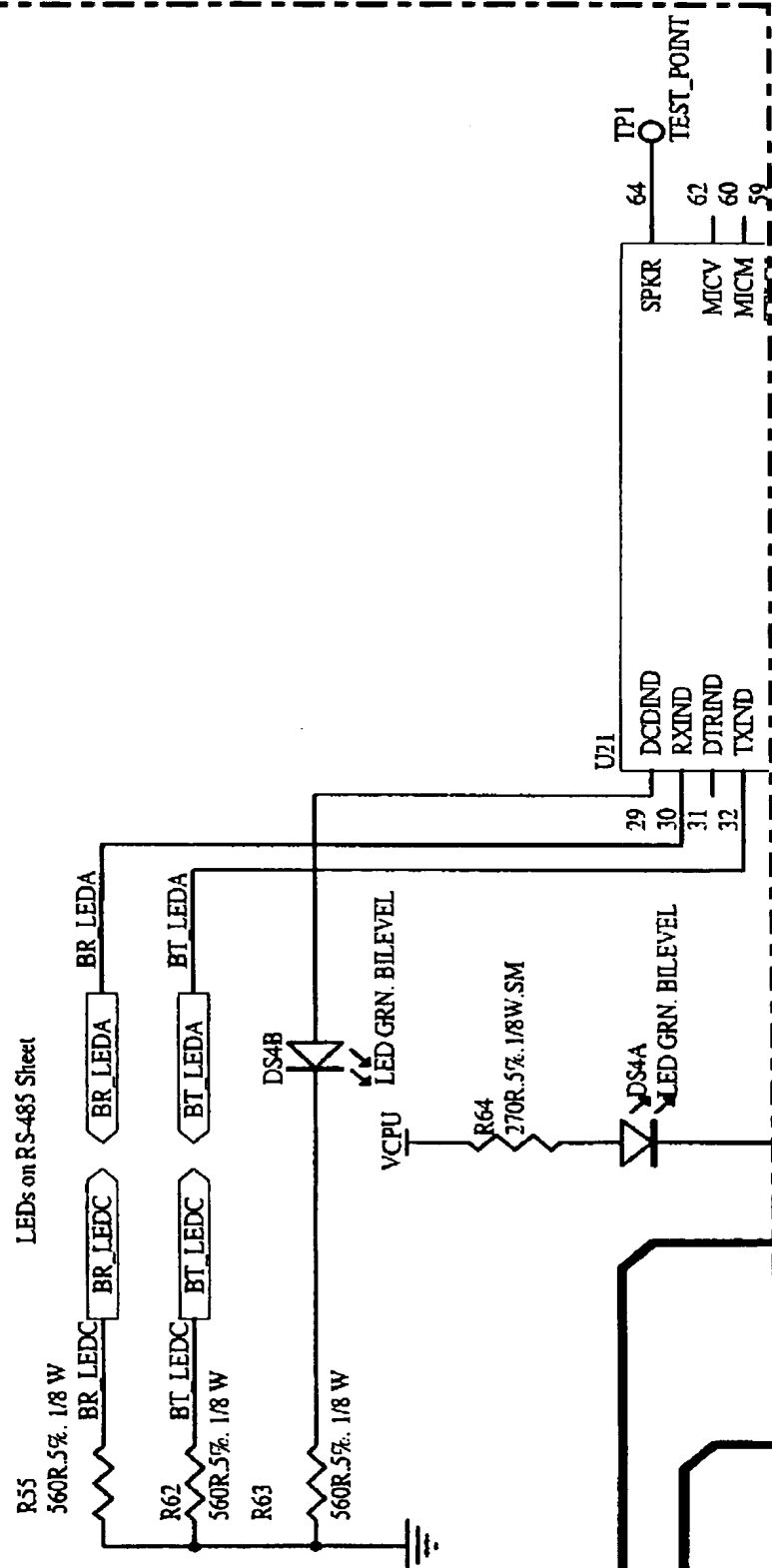


FIG. 40b

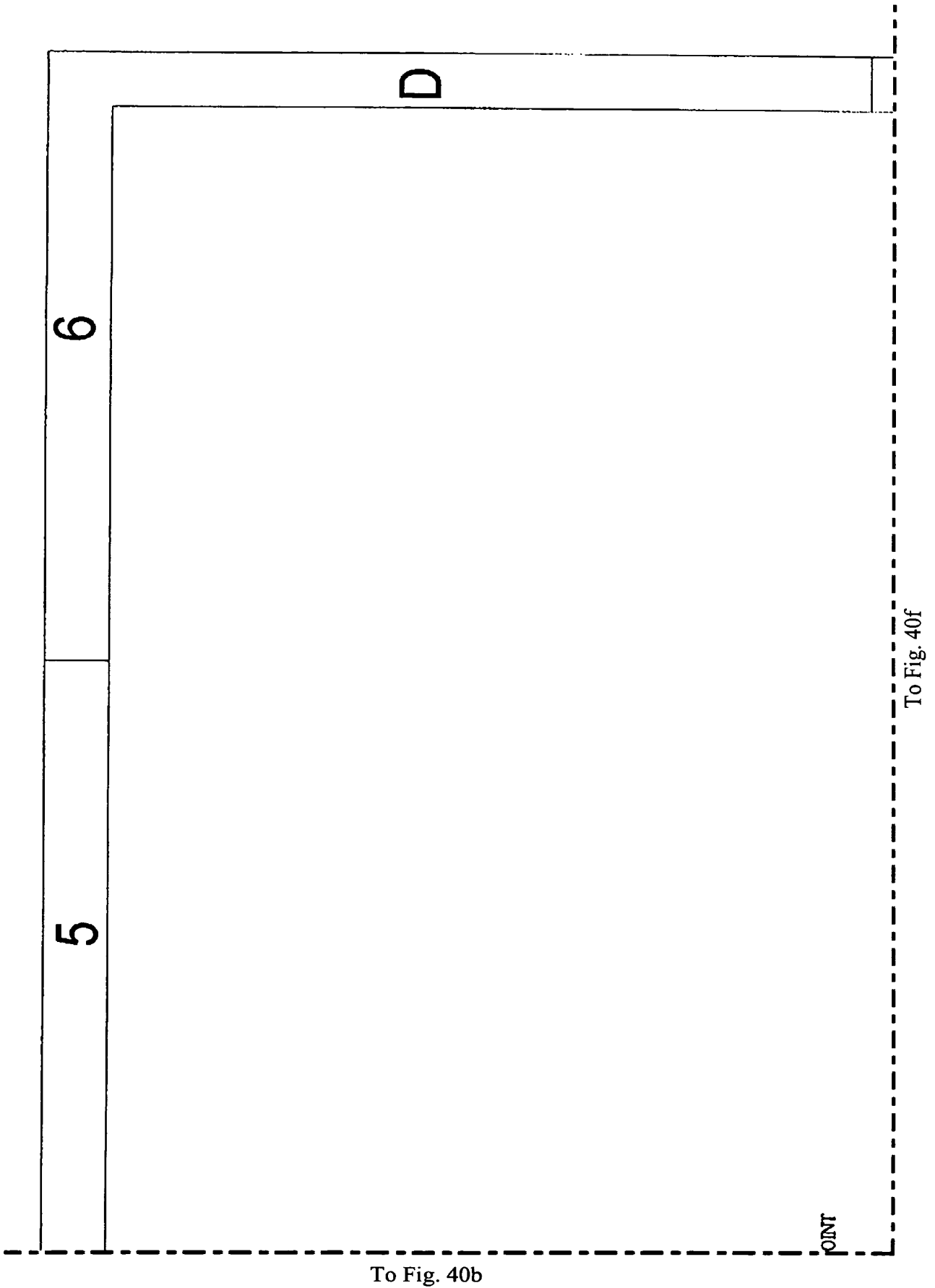
To Fig. 40c

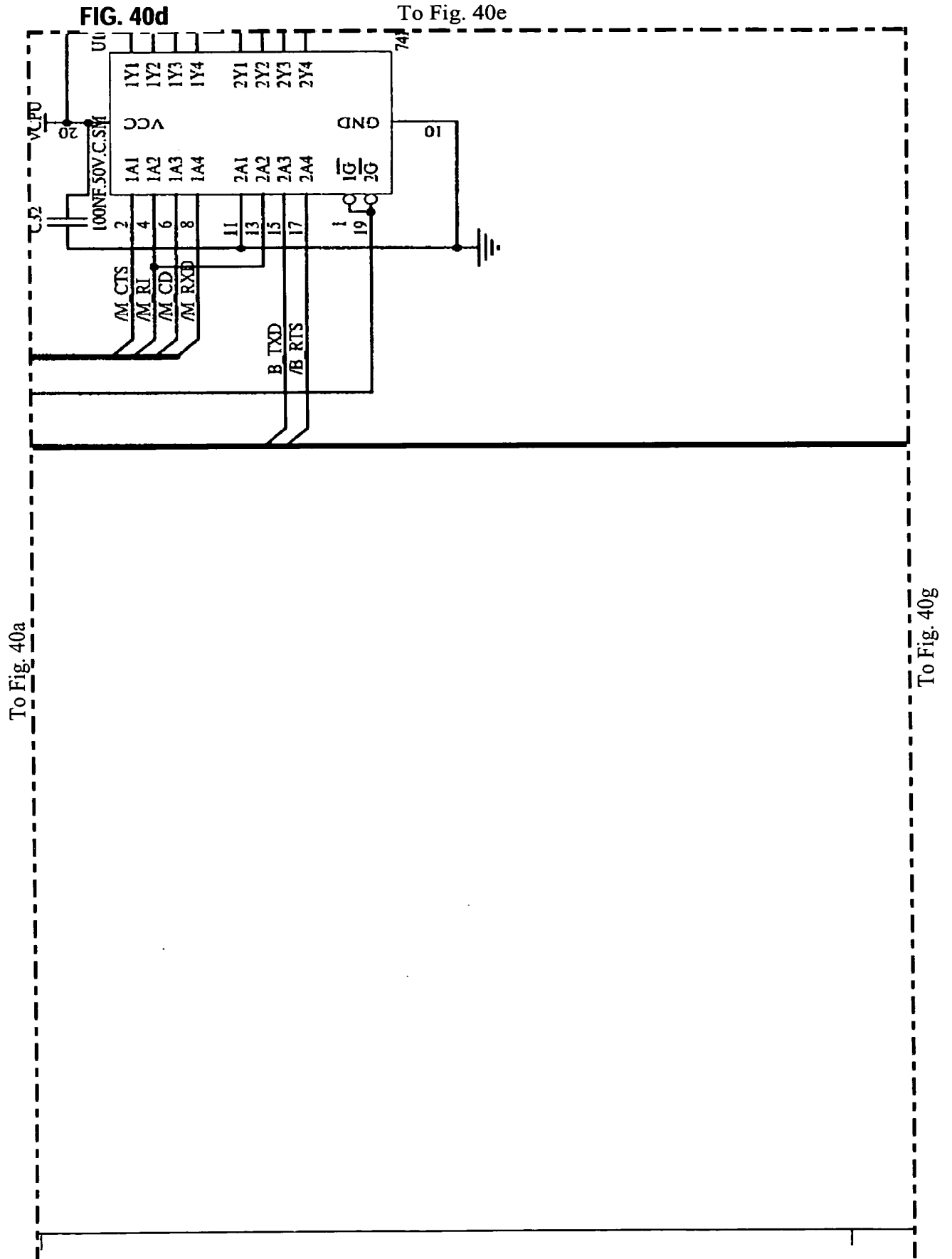


To Fig. 40e

To Fig. 40a

FIG. 40c





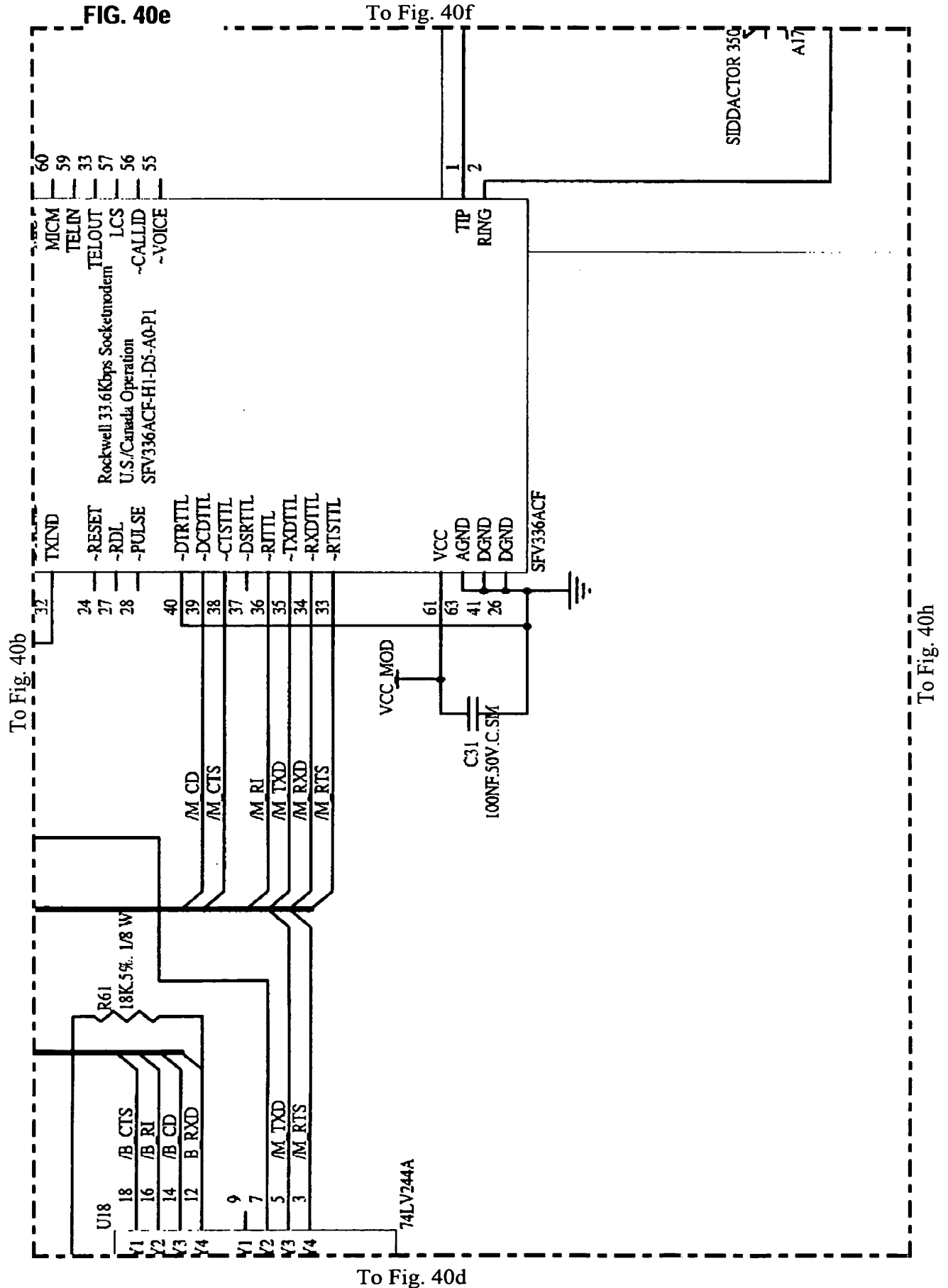
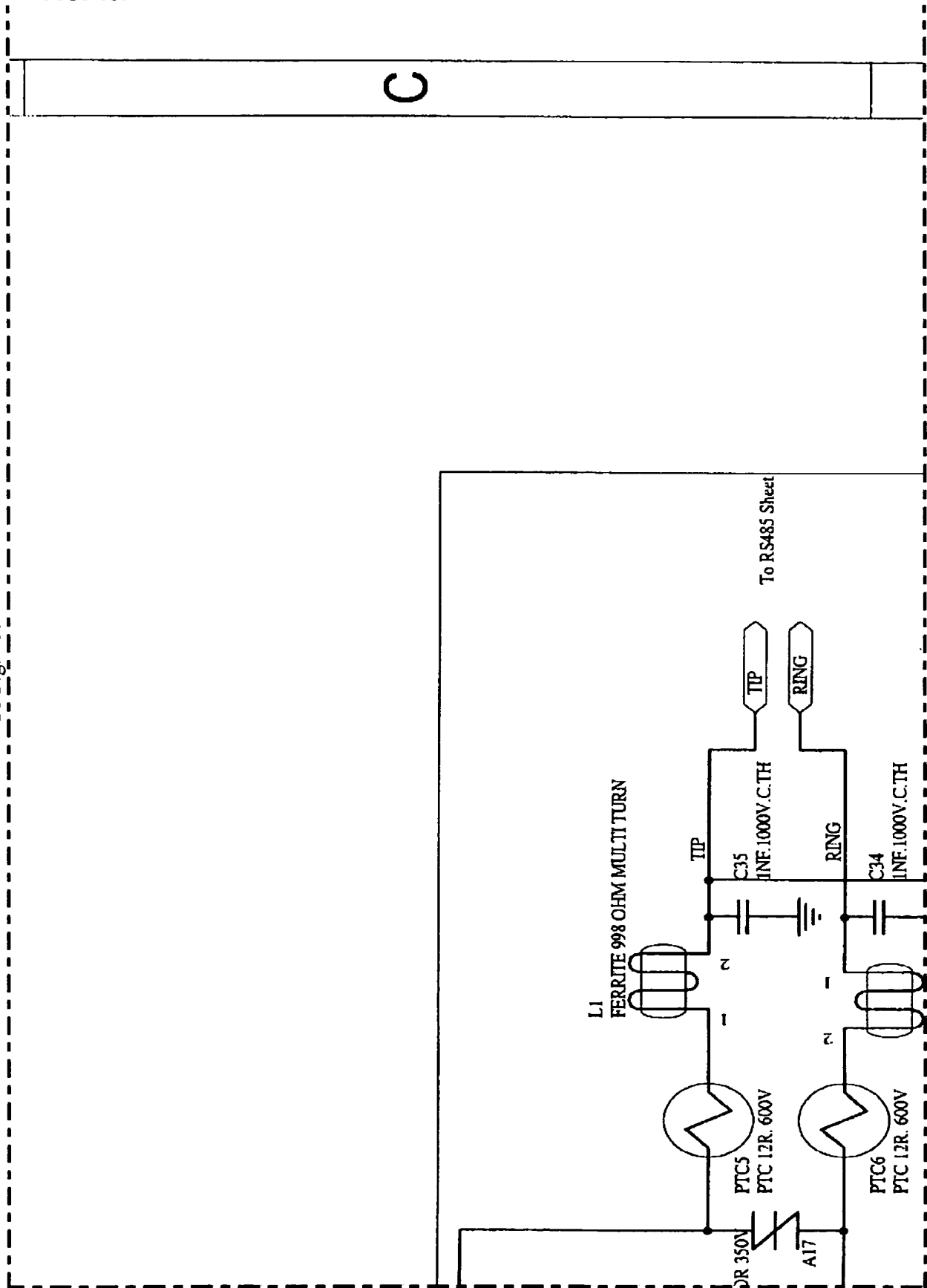


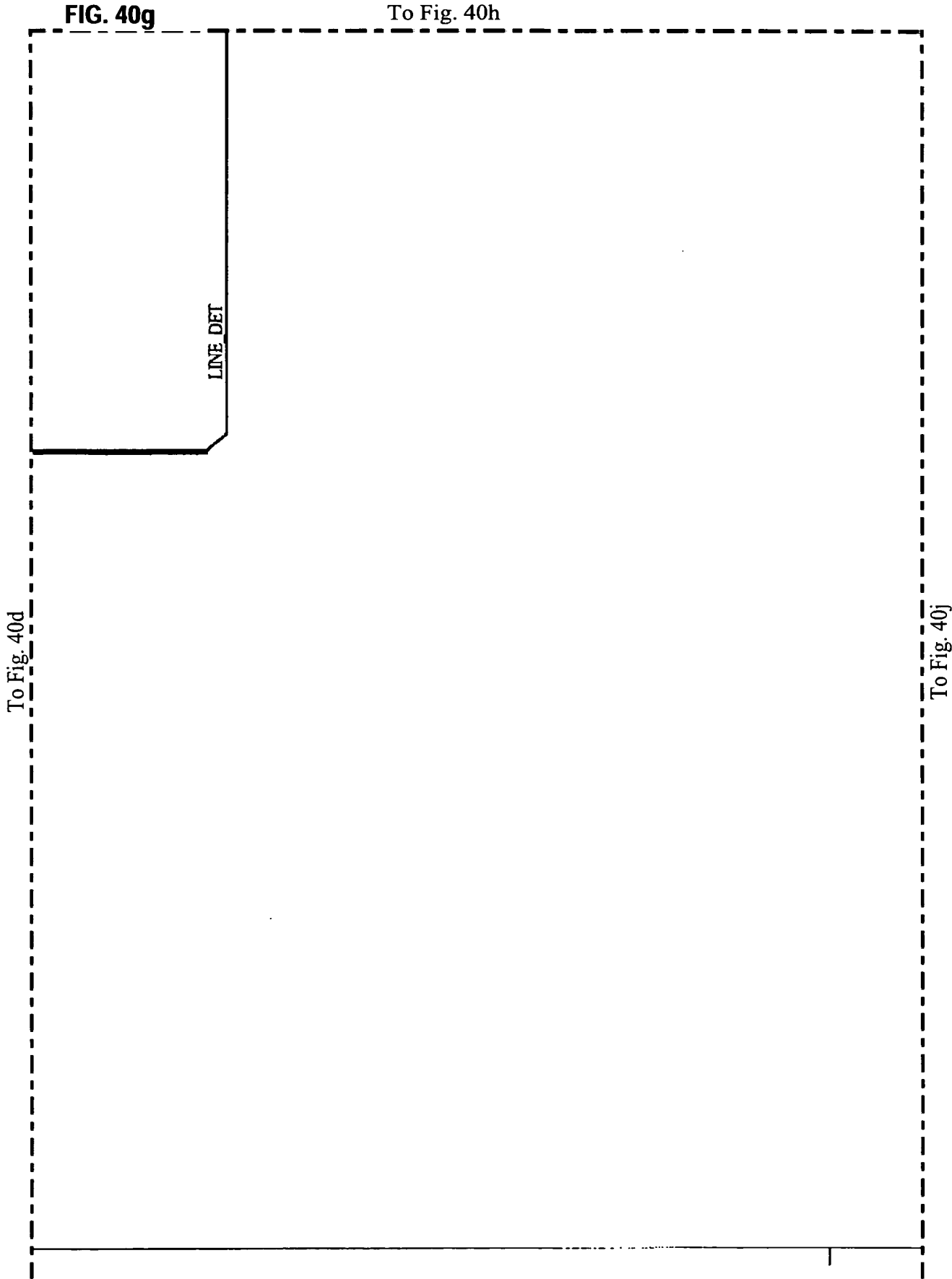
FIG. 40f

To Fig. 40c

To Fig. 40e

To Fig. 40i





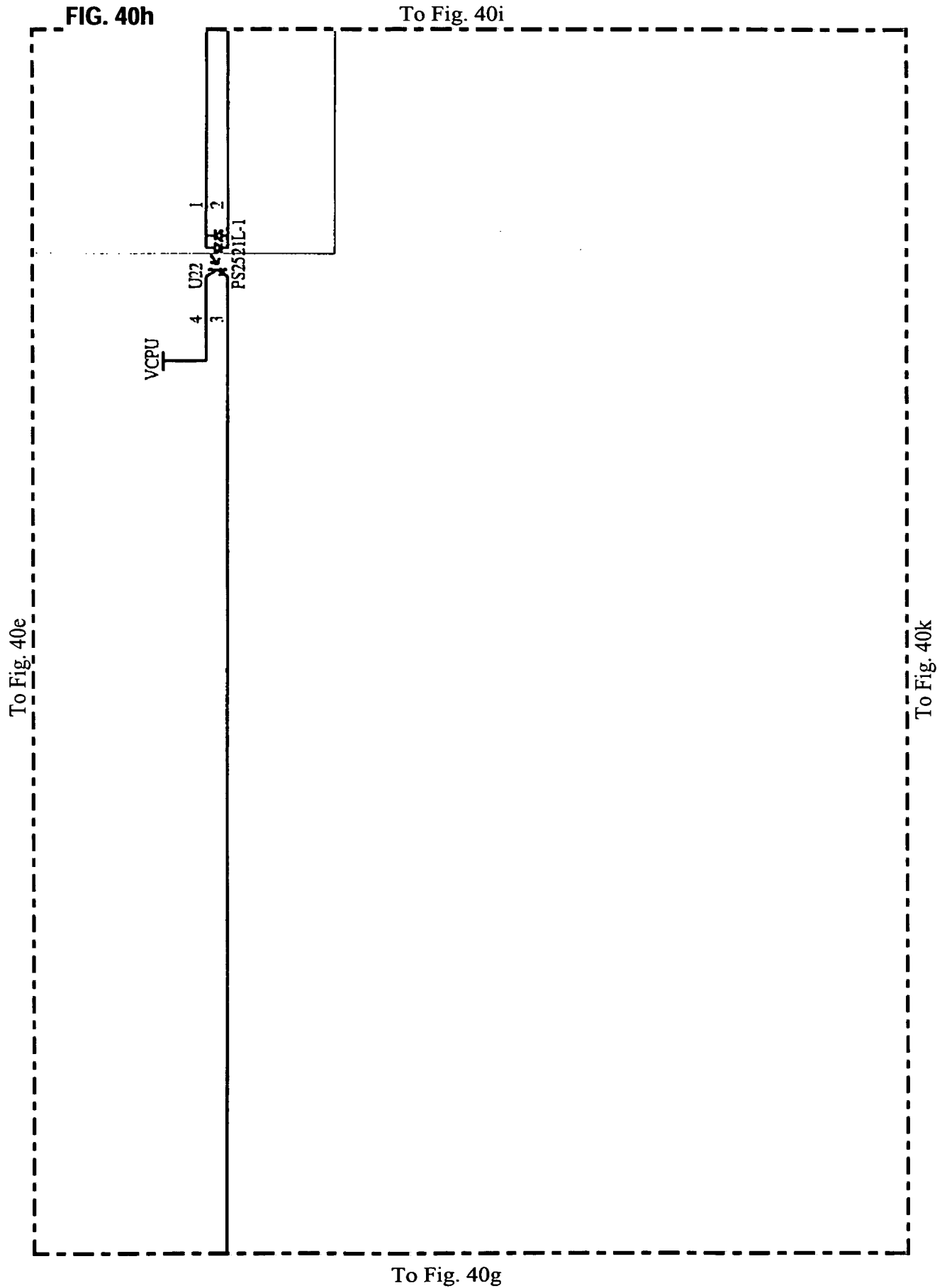
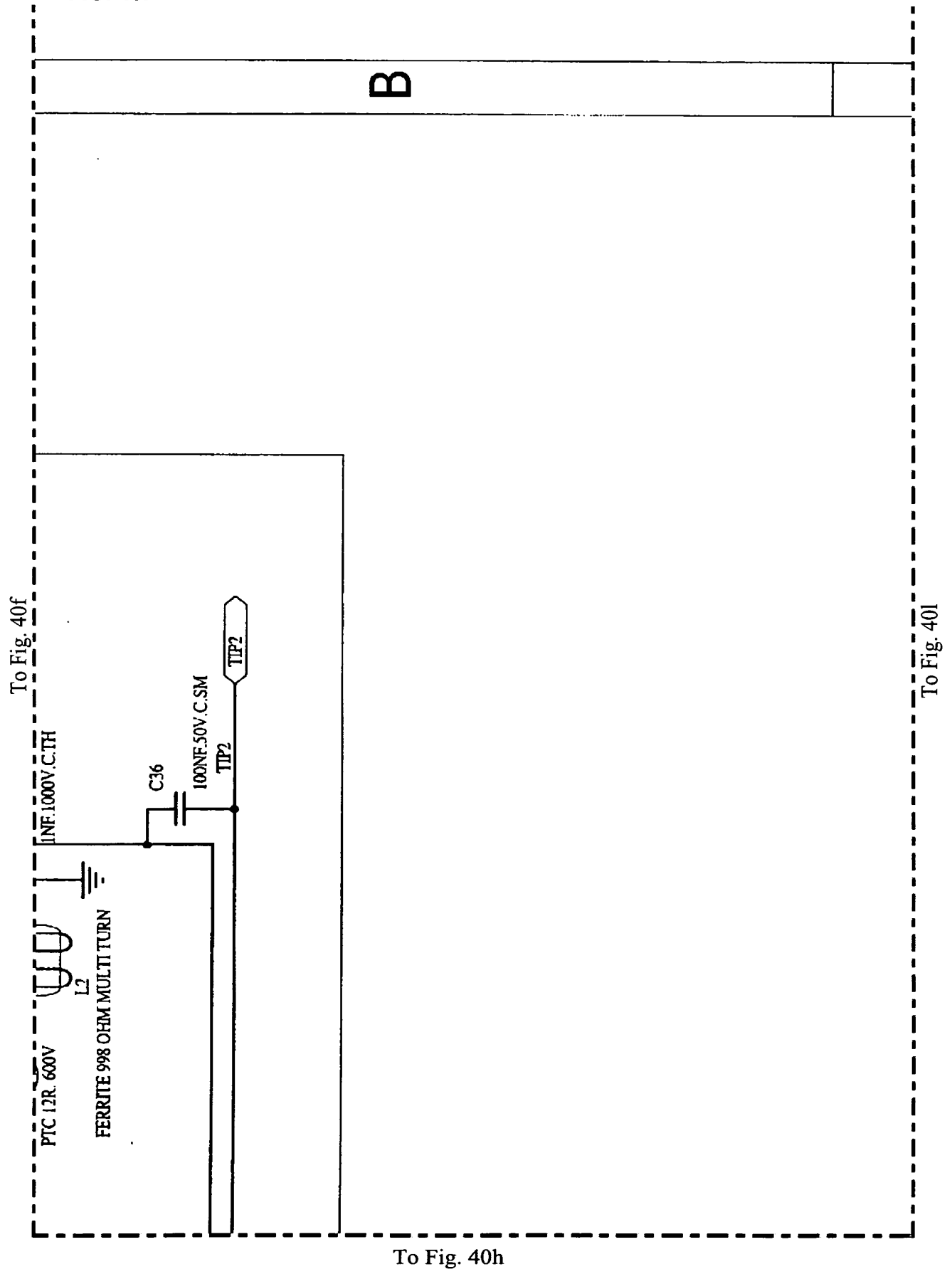
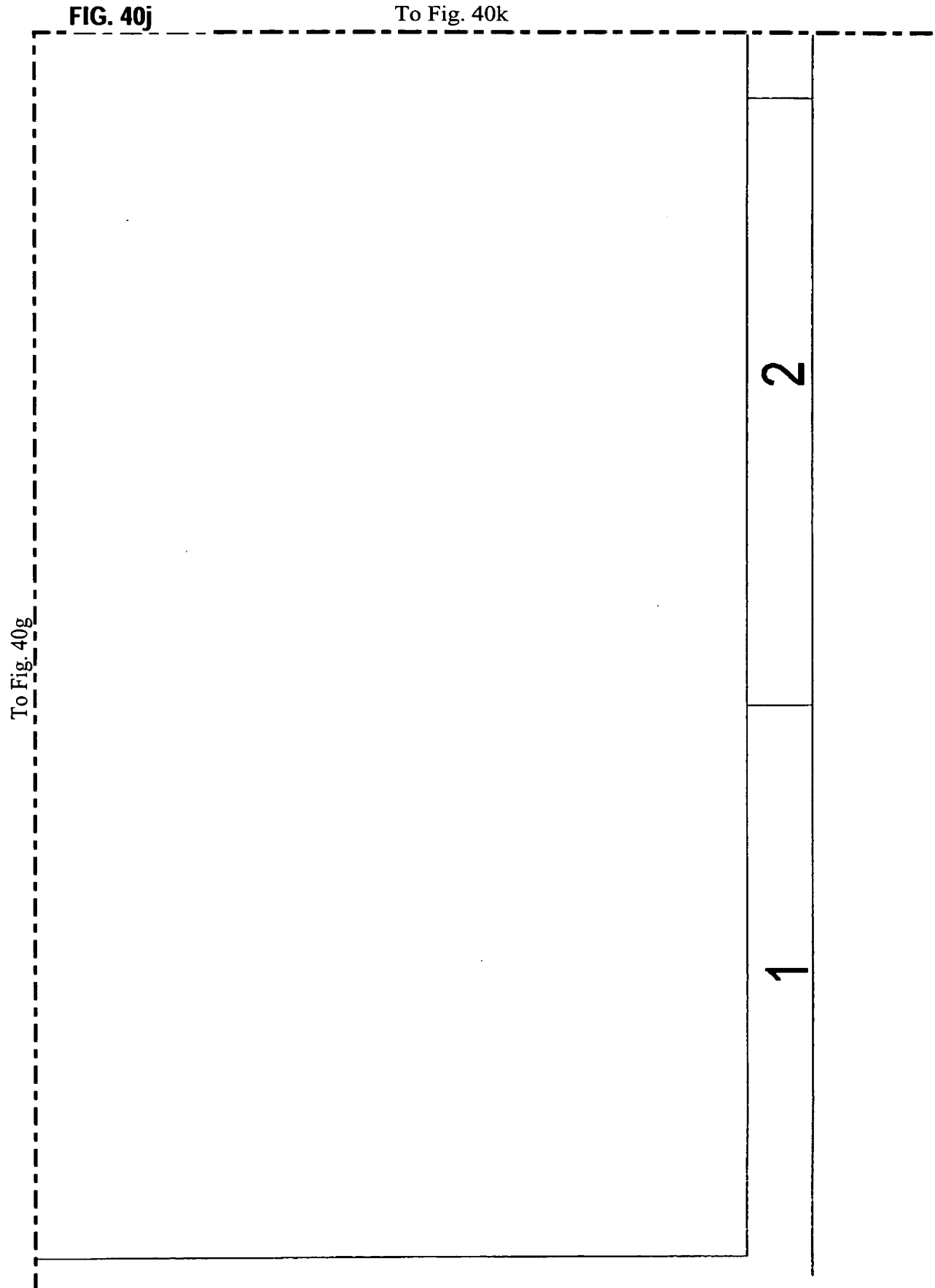


FIG. 40i





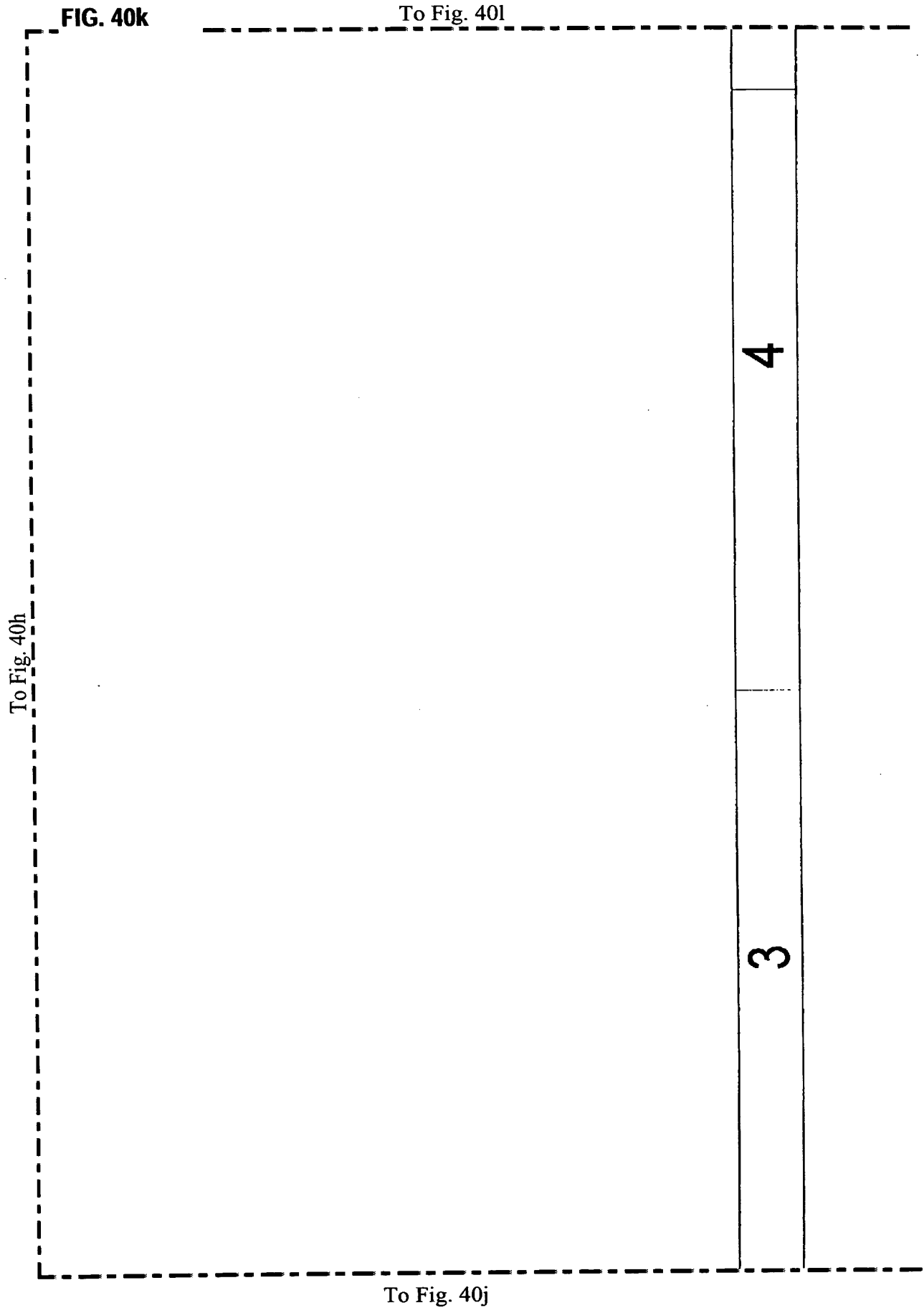


FIG. 40i

